Plastic Darlington Complementary Silicon Power Transistors

These devices are designed for general-purpose amplifier and low-speed switching applications.

Features

- High DC Current Gain h_{FE} = 2000 (Typ) @ I_C = 2.0 Adc
- Monolithic Construction with Built-in Base-Emitter Resistors to Limit Leakage - Multiplication
- Choice of Packages MJE700 and MJE800 Series
- Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage MJE700, MJE800 MJE702, MJE703, MJE802, MJE803	V _{CEO}	60 80	Vdc
Collector-Base Voltage MJE700, MJE800 MJE702, MJE703, MJE802, MJE803	V _{CB}	60 80	Vdc
Emitter-Base Voltage	V _{EB}	5.0	Vdc
Collector Current	I _C	4.0	Adc
Base Current	I _B	0.1	Adc
Total Power Dissipation @ T _C = 25°C Derate above 25°C	P _D	40 0.32	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$\theta_{\sf JC}$	6.25	°C/W

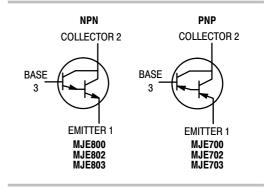
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



ON Semiconductor®

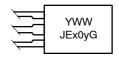
http://onsemi.com

4.0 AMPERE DARLINGTON POWER TRANSISTORS COMPLEMENTARY SILICON 40 WATT 50 WATT





MARKING DIAGRAM



Y = Year WW = Work

WW = Work Week JEx0y = Device Code

x = 7 or 8y = 0, 2, or 3

G = Pb-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

1

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characte	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage (Not $(I_C = 50 \text{ mAdc}, I_B = 0)$	e 1) MJE700, MJE800 MJE702, MJE703, MJE802, MJE803	V _{(BR)CEO}	60 80	_ _	Vdc
Collector Cutoff Current (V _{CE} = 60 Vdc, I _B = 0) (V _{CE} = 80 Vdc, I _B = 0)	MJE700, MJE800 MJE702, MJE703, MJE802, MJE803	I _{CEO}	- -	100 100	μAdc
	Ісво	- -	100 500	μAdc	
Emitter Cutoff Current (V _{BE} = 5.0 Vdc, I _C = 0)		I _{EBO}	-	2.0	mAdc
ON CHARACTERISTICS					
	MJE700, MJE702, MJE800, MJE802 MJE703, MJE803 All devices	h _{FE}	750 750 100	- - -	-
Collector–Emitter Saturation Voltage (Note ($I_C = 1.5 \text{ Adc}$, $I_B = 30 \text{ mAdc}$) ($I_C = 2.0 \text{ Adc}$, $I_B = 40 \text{ mAdc}$) ($I_C = 4.0 \text{ Adc}$, $I_B = 40 \text{ mAdc}$)	1) MJE700, MJE702, MJE800, MJE802 MJE703, MJE803 All devices	V _{CE(sat)}	- - -	2.5 2.8 3.0	Vdc
$\begin{aligned} \text{Base-Emitter On Voltage (Note 1)} \\ \text{(I}_{\text{C}} &= 1.5 \text{ Adc, V}_{\text{CE}} = 3.0 \text{ Vdc)} \\ \text{(I}_{\text{C}} &= 2.0 \text{ Adc, V}_{\text{CE}} = 3.0 \text{ Vdc)} \\ \text{(I}_{\text{C}} &= 4.0 \text{ Adc, V}_{\text{CE}} = 3.0 \text{ Vdc)} \end{aligned}$	MJE700, MJE702, MJE800, MJE802 MJE703, MJE803 All devices	V _{BE(on)}	- - -	2.5 2.5 3.0	Vdc
DYNAMIC CHARACTERISTICS					
Small-Signal Current Gain (I _C = 1.5 Adc, V _{CE} = 3.0 Vdc, f = 1.0 N	ЛНz)	h _{fe}	1.0	_	-

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

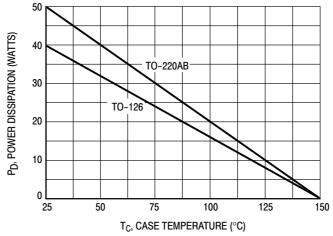


Figure 1. Power Derating

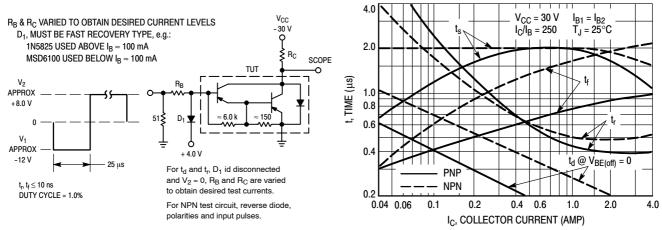


Figure 2. Switching Times Test Circuit

Figure 3. Switching Times

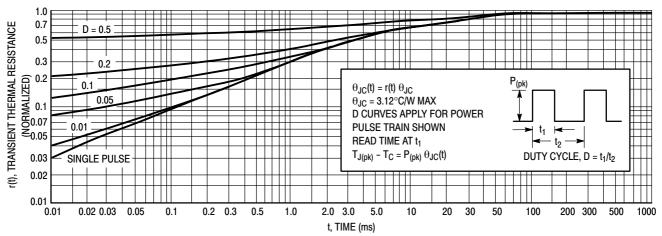
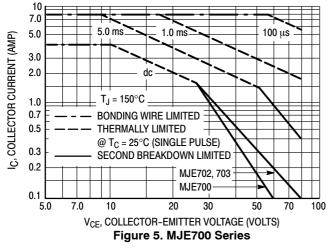
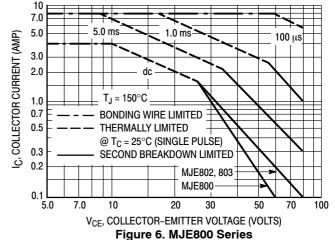


Figure 4. Thermal Response (MJE700, 800 Series)

ACTIVE-REGION SAFE-OPERATING AREA



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate $I_C - V_{CE}$ limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.



The data of Figures 5 and 6 are based on $T_{J(pk)} = 150^{\circ} C$; T_C is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} < 150^{\circ} C$. $T_{J(pk)}$ may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

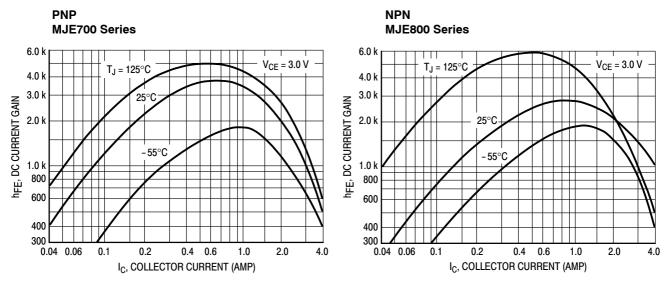


Figure 7. DC Current Gain

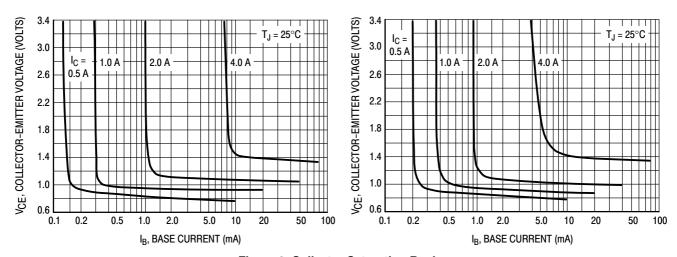
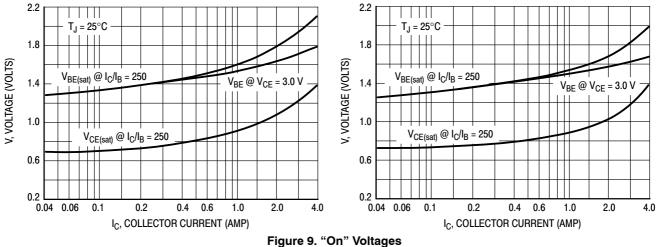


Figure 8. Collector Saturation Region

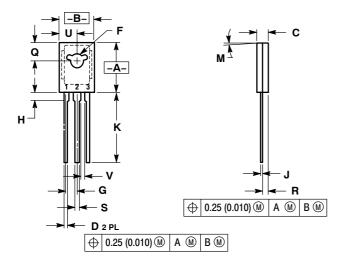


ORDERING INFORMATION

Device	Package	Shipping
MJE700	TO-225	
MJE700G	TO-225 (Pb-Free)	
MJE702	TO-225	
MJE702G	TO-225 (Pb-Free)	
MJE703	TO-225	
MJE703G	TO-225 (Pb-Free)	50 Units / Bulk
MJE800	TO-225	50 Offits / Bulk
MJE800G	TO-225 (Pb-Free)	
MJE802	TO-225	
MJE802G	TO-225 (Pb-Free)	
MJE803	TO-225	
MJE803G	TO-225 (Pb-Free)	

PACKAGE DIMENSIONS

TO-225 CASE 77-09 ISSUE Z



NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- 077-01 THRU -08 OBSOLETE, NEW STANDARD 077-09.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.425	0.435	10.80	11.04
В	0.295	0.305	7.50	7.74
C	0.095	0.105	2.42	2.66
D	0.020	0.026	0.51	0.66
F	0.115	0.130	2.93	3.30
G	0.094 BSC		2.39 BSC	
Н	0.050	0.095	1.27	2.41
J	0.015	0.025	0.39	0.63
K	0.575	0.655	14.61	16.63
M	5° TYP		5° TYP	
Q	0.148	0.158	3.76	4.01
R	0.045	0.065	1.15	1.65
S	0.025	0.035	0.64	0.88
U	0.145	0.155	3.69	3.93
٧	0.040		1.02	

STYLE 1:

EMITTER 2

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