# HCS410

## KEELOQ® Code Hopping Encoder and Transponder

## FEATURES

### Security

- Two programmable 64-bit encoder keys
- 16/32-bit bi-directional challenge and response using one of two keys
- 69-bit transmission length
- 32-bit unidirectional code hopping, 37-bit non-encrypted portion
- Encoder keys are read protected
- Programmable 28/32-bit serial number
- 60/64-bit, read-protected seed for secure learning
- Three IFF encryption algorithms
- Delayed increment mechanism
- Asynchronous transponder communication
- Queuing information transmitted

### Operating

- 2.0V - 6.6V operation, 13V encoder only operation
- Three switch inputs [S2, S1, S0]—seven functions
- Batteryless bi-directional transponder
- Selectable baud rate and code word blanking
- Automatic code word completion
- Battery low signal transmitted
- Non-volatile synchronization
- PWM or Manchester RF encoding
- Combined transmitter, transponder operation
- Anti-collision of multiple transponders
- Passive proximity activation
- Device protected against reverse battery
- Intelligent damping for high Q LC-circuits

### Other

- 37-bit nonencrypted part contains 28/32-bit serial number, 4/0-bit function code, 1-bit battery low, 2-bit CRC, 2-bit queue
- Simple programming interface
- On-chip tunable RC oscillator (±10%)
- On-chip EEPROM
- 64-bit user EEPROM in transponder mode
- Battery-low LED indication
- SQTP serialization quick-time programming
- 8-pin PDIP/SOIC/TSSOP and die

## PACKAGE TYPES

## BLOCK DIAGRAM

### Typical Applications

- Automotive remote entry systems
- Automotive alarm systems
- Automotive immobilizers
- Gate and garage openers
- Electronic door locks (Home/Office/Hotel)
- Burglar alarm systems
- Proximity access control

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## DESCRIPTION

The HCS410 is a code hopping transponder device designed for secure entry systems. The HCS410 utilizes the patented KEELOQ® code hopping system and bi-directional challenge-and-response for logical and physical access control. High security learning mechanisms make this a turnkey solution when used with the KEELOQ decoders. The encoder keys and synchronization information are stored in protected on-chip EEPROM.

A low cost batteryless transponder can be implemented with the addition of an inductor and two capacitors. A packaged module including the inductor and capacitor will also be offered.

A single HCS410 can be used as an encoder for Remote Keyless Entry (RKE) and a transponder for immobilization in the same circuit and thereby dramatically reducing the cost of hybrid transmitter/transponder circuits.

## 1.0 SYSTEM OVERVIEW

### 1.1 Key Terms

- Anti-Collision – Allows two transponders to be in the files simultaneously and be verified individually.
- CH Mode – Code Hopping Mode. The HCS410 transmits a 69-bit transmission each time it is activated, with at least 32-bits changing each time the encoder is activated.
- Encoder Key – A unique 64-bit key generated and programmed into the encoder during the manufacturing process. The encoder key controls the encryption algorithm and is stored in EEPROM on the encoder device.
- IFF – Identify friend or foe is a means of validating a token. A decoder sends a random challenge to the token and checks that the response of the token is a valid response.
- KEELOQ Encryption Algorithm – The high security level of the HCS410 is based on the patented KEELOQ technology. A block cipher encryption algorithm based on a block length of 32 bits and a key length of 64 bits is used. The algorithm obscures the information in such a way that even if the unencrypted/challenge information differs by only one bit from the information in the previous transmission/challenge, the next coded transmission/response will be totally different. Statistically, if only one bit in the 32-bit string of information changes, approximately 50 percent of the coded transmission will change.
- Learn – The HCS product family facilitates several learning strategies to be implemented on the decoder. The following are examples of what can be done.

  **Normal Learn** –The receiver uses the same information that is transmitted during normal operation to derive the transmitter's encoder key, decrypt the discrimination value and the synchronization counter.

  **Secure Learn*** – The transmitter is activated through a special button combination to transmit a stored 60-bit value (random seed) that can be used for key generation or be part of the key. Transmission of the random seed can be disabled after learning is completed.
- Manufacturer's Code – A 64-bit word, unique to each manufacturer, used to produce a unique encoder key in each transmitter (encoder).
- Passive Proximity Activation – When the HCS410 is brought into in a magnetic field without a command given by the base station, the HCS410 can be programmed to give an RF transmission.
- Transport Code – A 32-bit transport code needs to be given before the HCS410 can be inductively programmed. This prevents accidental programming of the HCS410.


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## 1.2 KEELOQ Code Hopping Encoders

When the HCS410 is used as a code hopping encoder device, it is ideally suited to keyless entry systems, primarily for vehicles and home garage door openers. It is meant to be a cost-effective, yet secure solution to such systems. The encoder portion of a keyless entry system is meant to be carried by the user and operated to gain access to a vehicle or restricted area.

Most keyless entry systems transmit the same code from a transmitter every time a button is pushed. The relative number of code combinations for a low end system is also a relatively small number. These shortcomings provide the means for a sophisticated thief to create a device that 'grabs' a transmission and retransmits it later or a device that scans all possible combinations until the correct one is found.

The HCS410 employs the KEELOQ code hopping technology and an encryption algorithm to achieve a high level of security. Code hopping is a method by which the code transmitted from the transmitter to the receiver is different every time a button is pushed. This method, coupled with a transmission length of 69 bits, virtually eliminates the use of code 'grabbing' or code 'scanning'.

The HCS410 has a small EEPROM array which must be loaded with several parameters before use. The most important of these values are:

- A 28/32-bit serial number which is meant to be unique for every encoder
- 64-bit seed value
- A 64-bit encoder key that is generated at the time of production
- A 16-bit synchronization counter value.
- Configuration options

The 16-bit synchronization counter value is the basis for the transmitted code changing for each transmission, and is updated each time a button is pressed. Because of the complexity of the code hopping encryption algorithm, a change in one bit of the synchronization counter value will result in a large change in the actual transmitted code.

Once the encoder detects that a button has been pressed, the encoder reads the button and updates the synchronization counter. The synchronization counter value, the function bits, and the discrimination value are then combined with the encoder key in the encryption algorithm, and the output is 32 bits of encrypted information (Figure 1-1). The code hopping portion provides up to four billion changing code combinations. This data will change with every button press, hence, it is referred to as the code hopping portion of the code word.

The 32-bit code hopping portion is combined with the button information and the serial number to form the code word transmitted to the receiver. The code word format is explained in detail in Section 2.2.

**FIGURE 1-1: BASIC OPERATION OF A CODE HOPPING TRANSMITTER (ENCODER)**

### 1.3 KEELOQ IFF

The HCS410 can be used as an IFF transponder for verification of a token. In IFF mode the HCS410 is ideally suited for authentication of a key before disarming a vehicle immobilizer. Once the key has been inserted in the car's ignition the decoder would inductively poll the key validating it before disarming the immobilizer.

IFF validation of the token involves a random challenge being sent by a decoder to a token. The token then generates a response to the challenge and sends this response to the decoder (Figure 1-2). The decoder calculates an expected response using the same challenge. The expected response is compared to the response received from the token. If the responses match, the token is identified as a valid token and the decoder can take appropriate action.

The HCS410 can do either 16 or 32-bit IFF. The HCS410 has two encryption algorithms that can be used to generate a response to a challenge. In addition there are up to two encoder keys that can be used by the HCS410. Typically each HCS410 will be programmed with a unique encoder key(s).

In IFF mode, the HCS410 will wait for a command from the base station and respond to the command. The command can either request a read/write from user EEPROM or an IFF challenge response. A given 16 or 32-bit challenge will produce a unique 16/32-bit response, based on the IFF key and IFF algorithm used.

**FIGURE 1-2: BASIC OPERATION OF AN IFF TOKEN**

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## 2.0 DEVICE OPERATION

The HCS410 can either operate as a normal code hopping transmitter with one or two IFF keys (Figure 2-1) or as purely an IFF token with two IFF keys (Figure 2-2 and Figure 2-3). When used as a code hopping transmitter the HCS410 only needs the addition of buttons and RF circuitry for use as a transmitter. Adding the transponder function to the transmitter requires the addition of an inductor and two capacitors as shown in Figure 2-1 and Figure 2-2. A description of each pin is given in Table 2-1. Table 2-2 shows the function codes for using the HCS410.

**FIGURE 2-1: COMBINED TRANSMITTER/ TRANSPONDER CIRCUIT**

**FIGURE 2-2: TRANSPONDER CIRCUIT**

**FIGURE 2-3: 2-WIRE, 1 OR 2-KEY IFF TOKEN**

Figure 2-4 shows how to use the HCS410 with a 12V battery as a code hopping transmitter. The circuit uses the internal regulator, normally used for charging a capacitor/battery in LC mode, to generate a 6V supply for the HCS410.

**FIGURE 2-4: HCS410 ENCODER WITH 12V BATTERY**

**FIGURE 2-5: LED CONNECTION TO S2/LED OUTPUT**

**FIGURE 2-6: LC PIN BLOCK DIAGRAM**

## 2.1 Pinout Description

The HCS410 has the same footprint as all of the other devices in the KEELOQ family, except for the two pins that are reserved for transponder operations and the LED that is now located at the same position as the S2 switch input.

- S[0:1] – are inputs with Schmitt Trigger detectors and an internal 60k¾ (nominal) pull-down resistors.
- S2/LED – uses the same input detection circuit as S0/S1 but with an added PMOS transistor connected to VDD capable of sourcing enough current to drive an LED.
- LC[0:1] – is the transponder interface pins to be connected to an LC circuit for inductive communication. LC0 is connected to a detector for data input. Data output is achieved by clamping LC0 and LC1 to GND through two NMOS transistors. These pins are also connected to a rectifier and a regulator, providing power to the rest of the logic and for charging an external power source (Battery/Capacitor) through VDD.

  The input impedance of the LC pins is a function of input voltage. At low voltages, the input impedance is in the order of mega-ohms. **When laying out a PC board, care should be taken to ensure that there is no cross coupling between the LC pins and other traces on the board.** Glitches on the LC lines will cause the device to reset. A high-value resistor (220 KW) between LC0 and GND can be added to reduce sensitivity.

**TABLE 2-1: PINOUT DESCRIPTION**

| Name | Pin Number | Description |
|---|---|---|
| S0 | 1 | Switch input 0 |
| S1 | 2 | Switch input 1 |
| S2/LED | 3 | Switch input 2/LED output, Clock pin for programming mode |
| LC1 | 4 | Transponder interface pin |
| VSS | 5 | Ground reference connection |
| PWM | 6 | Pulse width modulation (PWM) output pin/Data pin for programming mode |
| LC0 | 7 | Transponder interface pin |
| VDD | 8 | Positive supply voltage connection |

**TABLE 2-2: FUNCTION CODES**

| | LC0 | S2 | S1 | S0 | Comments |
|---|---|---|---|---|---|
| 1 | 0 | 0 | 0 | 1 | Normal Code Hopping transmission |
| 2 | 0 | 0 | 1 | 0 | Normal Code Hopping transmission |
| 3 | 0 | 0 | 1 | 1 | Delayed seed transmission if allowed by SEED and TMPSD/Normal Code Hopping transmission |
| 4 | 0 | 1 | 0 | 0 | Normal Code Hopping transmission |
| 5 | 0 | 1 | 0 | 1 | Normal Code Hopping transmission |
| 6 | 0 | 1 | 1 | 0 | Normal Code Hopping transmission |
| 7 | 0 | 1 | 1 | 1 | Immediate seed transmission if allowed by SEED and TMPSD/Normal Code Hopping transmission |
| 8 | 1 | 0 | 0 | 0 | Transponder mode |


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## 2.2 Code Hopping Mode (CH Mode)

The HCS410 wakes up upon detecting a switch closure and then delays approximately 30 ms for switch debounce (Figure 2-7). The synchronization counter value, fixed information, and switch information are encrypted to form the code hopping portion. The encrypted or code hopping portion of the transmission changes every time a button is pressed, even if the same button is pushed again. Keeping a button pressed for a long time results in the same code word being transmitted until the button is released or time-out occurs. A code that has been transmitted will not occur again for more than 64K transmissions. Overflow information programmed into the encoder can be used by the decoder to extend the number of unique transmissions to more than 192K.

If, during the transmit process, it is detected that a new button(s) has been added, a reset will immediately be forced and the code word will not be completed. Please note that buttons removed will not have any effect on the code word unless no buttons remain pressed in which case the current code word will be completed and the power down will occur. If, after a button combination is pressed, and the same button combination is pressed again within 2 seconds of the first press, the current transmission will be aborted and a new trans-

**FIGURE 2-7: CODE HOPPING ENCODER OPERATION**

**Note 1:** 30 ms debounce on press and release of all buttons.
**2:** Completes a minimum of 3 code words if MTX3 is set.

### 2.2.1 TRANSMISSION DATA FORMAT

The HCS410 transmission (CH Mode) is made up of several parts (Figure 2-10 and Figure 2-11). Each transmission is begun with a preamble and a header, followed by the encrypted and then the fixed data. The actual data is 69 bits which consists of 32 bits of encrypted data and 37 bits of fixed data. Each transmission is followed by a guard period before another transmission can begin. Refer to Table 6-4 and Table 6-5 for transmission timing specifications. The combined encrypted and nonencrypted sections increase the number of combinations to 1.47 x $10^{20}$.

The HCS410 transmits a 69-bit code word when a button is pressed. The 69-bit word is constructed from a Fixed Code portion and Code Hopping portion (Figure 2-8).

The **Encrypted Data** is generated from 4 function bits, 2 overflow bits, and 10 discrimination bits, and the 16-bit synchronization counter value (Figure 2-8).

The **Nonencrypted Code Data** is made up of 2 QUE bits, 2 CRC bits, a VLOW bit, 4 function bits, and the 28-bit serial number. If the extended serial number (32 bits) is selected, the 4 function code bits will not be transmitted (Figure 2-8).

**FIGURE 2-8: HOP CODE WORD ORGANIZATION (RIGHT-MOST BIT IS CLOCKED OUT FIRST)**

**FIGURE 2-9: SEED CODE WORD ORGANIZATION**

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### 2.2.2 TRANSMISSION DATA MODULE

The Data Modulation Format is selectable between Pulse Width Modulation (PWM) format and Manchester encoding. Both formats are preceded by a preamble and synchronization header, followed by the 69-bits of data. Manchester encoding has a leading and closing ‘1’ for each code word.

The same code word is continuously sent as long as the input pins are kept high with a guard time separating the code words. All of the timing values are in multiples of a Basic Timing Element ($T_E$), which can be changed using the baud rate option bits.

**FIGURE 2-10: TRANSMISSION FORMAT—MANCH = 0**

**FIGURE 2-11: TRANSMISSION FORMAT—MANCH = 1**

## 2.3 Code Hopping Mode Special Features

### 2.3.1 CODE WORD COMPLETION

Code word completion is an automatic feature that ensures that the entire code word is transmitted, even if the button is released before the transmission is complete. The HCS410 encoder powers itself up when a button is pushed and powers itself down after the command is finished (Figure 2-7). If MTX3 is set in the configuration word, a minimum of three transmissions will be transmitted when the HCS410 is activated, even if the buttons are released.

If less than seven words have been transmitted when the buttons are released, the HCS410 will complete the current word. If more than seven words have been transmitted, and the button is released, the PWM output is immediately switched off.

### 2.3.2 CODE WORD BLANKING ENABLE

Federal Communications Commission (FCC) part 15 rules specify the limits on fundamental power and harmonics that can be transmitted. Power is calculated on the worst case average power transmitted in a 100ms window. It is therefore advantageous to minimize the duty cycle of the transmitted word. This can be achieved by minimizing the duty cycle of the individual bits and by blanking out consecutive words. Code Word Blanking Enable (CWBE) is used for reducing the average power of a transmission (Figure 2-12). Using the CWBE allows the user to transmit a higher amplitude transmission if the transmission length is shorter. The FCC puts constraints on the average power that can be transmitted by a device, and CWBE effectively prevents continuous transmission by only allowing the transmission of every second or fourth word. This reduces the average power transmitted and hence, assists in FCC approval of a transmitter device.

The HCS410 will either transmit all code words, 1 in 2 or 1 in 4 code words, depending on the baud rate selected and the code word blanking option. See Section 3.7 for additional details.

### 2.3.3 CRC (CYCLE REDUNDANCY CHECK) BITS

The CRC bits are calculated on the 65 previously transmitted bits. The CRC bits can be used by the receiver to check the data integrity before processing starts. The CRC can detect all single bit and 66% of double bit errors. The CRC is computed as follows:

**EQUATION 2-1: CRC CALCULATION**

$$CRC[1]_{n+1} = CRC[0]_n \oplus Di_n$$

and

$$CRC[0]_{n+1} = (CRC[0]_n \oplus Di_n) \oplus CRC[1]_n$$

with

$$CRC[1,\ 0]_0 = 0$$

and $Di_n$ the nth transmission bit 0 ð n ð 64

**FIGURE 2-12: CODE WORD BLANKING ENABLE**

•Patents have been applied for.


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### 2.3.4 SEED TRANSMISSION

In order to increase the level of security in a system, it is possible for the receiver to implement what is known as a secure learning function. This can be done by utilizing the seed value on the HCS410 which is stored in EEPROM. Instead of the normal key generation method being used to create the encoder key, this seed value is used and there should not be any mathematical relationship between serial numbers and seeds for the best security. See Section 3.7.3 for additional details.

### 2.3.5 PASSIVE PROXIMITY ACTIVATION

If the HCS410 is brought into a magnetic field it enters IFF mode. In this mode it sends out ACK pulses on the LC lines. If the HCS410 doesn't receive any response to the first set of ack pulses within 50 ms the HCS410 will transmit a normal code hopping transmission for 2 seconds if XPRF is set in the configuration word. The function code during this transmission is S2:S0 = 000.

### 2.3.6 AUTO-SHUTOFF

The Auto-shutoff function automatically stops the device from transmitting if a button inadvertently gets pressed for a long period of time. This will prevent the device from draining the battery if a button gets pressed while the transmitter is in a pocket or purse. Time-out period is approximately 20 seconds.

### 2.3.7 VLOW: VOLTAGE LOW INDICATOR

The VLOW bit is transmitted with every transmission (Figure 2-8). VLOW is set when the operating voltage has dropped below the low voltage trip point, approximately 2.2V or 4.4V selectable at 25°C. This VLOW signal is transmitted so the receiver can give an indication to the user that the transmitter battery is low.

### 2.3.8 QUE0:QUE1: QUEUING INFORMATION

If a button is pressed, released for more than 30 ms, and pressed again within 2 seconds of the first press, the QUE counter is incremented (Figure 2-7). The transmission that the HCS410 is busy with is aborted and a new transmission is begun with the new QUE bits set. These bits can be used by the decoder to perform secondary functions using only a single button without the requirement that the decoder receive more than one completed transmission. For example if none of the QUE bits are set the decoder only unlocks the driver's door, if QUE0 is set (double press on the transmitter) the decoder unlocks all the doors.

| | |
|---|---|
| **Note 1:** | The QUE will not overflow. |
| **2:** | The button must be pressed for more than 50 ms. |

**FIGURE 2-13: QUE COUNTER TIMING DIAGRAM**

### 2.3.9 LED OUTPUT

The S2/LED line can be used to drive a LED when the HCS410 is transmitting. If this option is enabled in the configuration word the S2 line is driven high periodically when the HCS410 is transmitting as shown in Figure 2-14. The LED output operates with a 30 ms on and 480 ms off duty cycle when the supply voltage is above the level indicated by the VLOW bit in the configuration word. When the supply voltage drops below the voltage indicated by the VLOW bit the HCS410 will indicate this by turning the LED on for 200ms at the start of a transmission and remain off for the rest of the transmission.

### 2.3.10 DELAYED INCREMENT

The HCS410 has a delayed increment feature that increments the counter by 12, 20 seconds after the last button press occurred. The 20-second time-out is reset and the queue counter will increment if another press occurs before the 20 seconds expires. The queue counter is cleared after the buttons have been released for more than 2 seconds. Systems that use this feature will circumvent the latest jamming-code grabbing attackers.

### 2.3.11 OTHER CONFIGURABLE OPTIONS

Other configurable code hopping options include an

- Transmission-rate selection
- Extended serial number.

These are described in more detail in Section 3.7.

**FIGURE 2-14: LED INDICATION DURING TRANSMISSION**

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## 2.4 IFF Mode

IFF mode allows the decoder to perform an IFF validation, to write to the user EEPROM and to read from the user EEPROM. Each operation consists of the decoder sending an opcode data and the HCS410 giving a response.

There are two IFF modes: IFF1 and IFF2. IFF1 allows only one key IFF, while IFF2 allows two keys to be used.

| **Note:** | When IFF2 is enabled, seed transmissions will not be allowed. |
|---|---|

It is possible to use the HCS410 as an IFF token without using a magnetic field for coupling. The HCS410 can be directly connected to the data line of the decoder as shown in Figure 2-3. The HCS410 gets its power from the data line as it would in normal transponder mode. The communication is identical to the communication used in transponder mode.

### 2.4.1 IFF MODE ACTIVATION

The HCS410 will enter IFF mode if the capacitor/inductor resonant circuit generates a voltage greater than approximately 1.0 volts on LC0. After the verified application of power and elapse of the normal reset period, the device will start responding by pulsing the DATA line (LC0/1) with pulses as shown in Figure 2-17. This action will continue until the pulse train is terminated by receiving a start signal of duration 2$T_E$, on the LC inputs before the next expected marker pulse. The device now enters the IFF mode and expects to receive an 'Opcode' and a 0/16/32-bit Data-stream to react on. The data rate ($T_E$) is determined by the TBSL bits in the configuration word. See Section 3.0 for additional details.

### 2.4.2 IFF DECODER COMMANDS

As shown in Figure 2-15, a logic 1 and 0 are differentiated by the time between two rising edges. A long pulse indicates a 1; a short pulse, a 0.

**FIGURE 2-15: MODULATION FOR IFF COMMUNICATION**

**FIGURE 2-16: OVERVIEW OF IFF OPERATION**

**FIGURE 2-17: DECODER IFF COMMANDS AND WAVEFORMS**

**TABLE 2-3: IFF TIMING PARAMETERS**

| Parameter | Symbol | Minimum | Typical | Maximum | Units |
|---|---|---|---|---|---|
| Time Element<br>IFFB = 0<br>IFFB = 1 | TE | —<br>— | 200<br>100 | —<br>— | µs |
| PPM Command Bit Time<br>Data = 1<br>Data = 0 | TBITC | 3.5<br>5.5 | 4<br>6 | —<br>— | TE |
| PPM Response Bit Time<br>Data = 1<br>Data = 0 | TBITR | —<br>— | 2<br>3 | —<br>— | TE |
| PPM Command Minimum High Time | TPMH | 1.5 | — | — | TE |
| Response Time (Minimum for Read) | TRT | 6.5 | — | — | ms |
| Opcode to Data Input Time | TOTD | 1.8 | — | — | ms |
| Transport Code to Data Input Time | TTTD | 6.8 | — | — | ms |
| IFF EEPROM Write Time (16 bits) | TWR | — | — | 30 | ms |


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### 2.4.3 HCS410 RESPONSES

The responses from the HCS410 are in PPM format. See Figure 2-17 for additional information. Every response from the HCS410 is preceded by a “2 bit preamble” of $01_2$, and then 16/32 bits of data.

### 2.4.4 IFF RESPONSE

The 16/32-bit response to a 16/32-bit challenge, is transmitted once, after which the device is ready to accept another command. The same applies to the result of a Read command. The opcode written to the device specifies the challenge length and algorithm used. The response always starts with a leading preamble of $01_2$ followed by the 16/32 bits of data.

### 2.4.5 IFF WRITE

The decoder can write to USER[0:3], SER[0:1], and the configuration word in the EEPROM.

After the HCS410 has written the word into the EEPROM, it will give two acknowledge pulses ($T_E$ wide and $T_E$ apart) on the LC pins.

When writing to the serial number or configuration word, the user must send the transport code before the write will begin (Section 3.4) .

| | |
|---|---|
| **Note:** | If the configuration word is written, the device must be reset to allow the new configuration settings to come into effect. |

### 2.4.6 IFF READ

The decoder can read USER[0:3], SER[0:1], and the configuration word in the EEPROM. After the data has been read, the device is ready to receive a command again.

Each read command is followed by a 16-bit data response. The response always starts with a leading preamble of $01_2$ and then the 16-bits of data.

### 2.4.7 IFF PROGRAMMING

Upon receiving a programming opcode and the transport code, the EEPROM is erased (Section 3.4). Thereafter, the first 16 bits of data can be written. After indicating that a write command has been successfully completed the device is ready to receive the next 16 bits. After a complete memory map was received, it will be transmitted in PPM format on the LC pins as 16-bit words. This enables wireless programming of the device.

After the EEPROM is erased, the configuration word is reloaded. This results in oscillator tuning bits of 0000 being used during programming. When using IFF programming, the user should read the configuration word and store the oscillator bits in the memory map to be programmed. A program command should be sent and the next set of ACK pulses transmitted by the HCS410 should be used to determine the $T_E$. A second program command can then be sent, and the device programmed using the $T_E$ just calibrated.

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## 2.5 IFF Opcodes

**TABLE 2-4: LIST OF IFF COMMANDS**

| Command | Description | Expected data In | Response |
|---|---|---|---|
| 00000 | Select HCS410, used if Anti-collision enabled | 1 to 32 bits of the serial number (SER) | Encoder select acknowledge if SER match |
| 00001 | Read configuration word | None | 16-bit configuration word |
| 00010 | Read low serial number | None | Lower 16 bits of serial number (SER0) |
| 00011 | Read high serial number | None | Higher 16 bits of serial number (SER1) |
| 00100 | Read user area 0 | None | 16 Bits of User EEPROM USR0 |
| 00101 | Read user area 1 | None | 16 Bits of User EEPROM USR1 |
| 00110 | Read user area 2 | None | 16 Bits of User EEPROM USR2 |
| 00111 | Read user area 3 | None | 16 Bits of User EEPROM USR3 |
| 01000 | Program HCS410 EEPROM | Transport code (32 bits); Complete memory map: 18 x 16 bit words (288 bits) | Write acknowledge pulse after each 16-bit word, 288 bits transmitted in 18 bursts of 16-bit words |
| 01001 | Write configuration word | Transport code (32 bits); 16 Bit configuration word | Write acknowledge pulse |
| 01010 | Write low serial number | Transport code (32 bits); Lower 16 bits of serial number (SER0) | Write acknowledge pulse |
| 01011 | Write high serial number | Transport code (32 bits); Higher 16 bits of serial number (SER1) | Write acknowledge pulse |
| 01100 | Write user area 0 | 16 Bits of User EEPROM USR0 | Write acknowledge pulse |
| 01101 | Write user area 1 | 16 Bits of User EEPROM USR1 | Write acknowledge pulse |
| 01110 | Write user area 2 | 16 Bits of User EEPROM USR2 | Write acknowledge pulse |
| 01111 | Write user area 3 | 16 Bits of User EEPROM USR3 | Write acknowledge pulse |
| 1X000 | IFF1 using key-1 and IFF algorithm | 32-Bit Challenge | 32-Bit Response |
| 1X001 | IFF1 using key-1 and HOP algorithm | 32-Bit Challenge | 32-Bit Response |
| 1X100 | IFF2 32-bit using key-2 and IFF algorithm | 32-Bit Challenge | 32-Bit Response |
| 1X101 | IFF2 32-bit using key-2 and HOP algorithm | 32-Bit Challenge | 32-Bit Response |


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## 2.6 IFF Special Features

### 2.6.1 ANTI-COLLISION (ACOLI)

When the ACOLI bit is set in the configuration word, anti-collision mode is entered. The HCS410 will start sending ACK pulses when it enters a magnetic field. The ACK pulses stop as soon as the HCS410 detects a start bit from the decoder. A ‘select encoder’ opcode (00000) is then sent out by the decoder, followed by a 32-bit serial number. If the serial number matches the HCS410’s serial number, the HCS410 will acknowledge with the acknowledge sequence as shown in Figure 2-18. The HCS410 can then be addressed as normal. If the serial number does not match, the IFF encoder will stop transmitting ACK pulses until it is either removed from the field or the correct serial number is given.

**FIGURE 2-18: SERIAL NUMBER CORRECT ACKNOWLEDGE SEQUENCE**

### 2.6.2 TRANSPONDER IN/RF OUT

When in transponder mode with ACOLI and XPRF set, the outputs of the HCS410’s LC0:LC1 pins are echoed on the PWM output line. After transmitting the data on the LC pins, the data is then transmitted on the PWM line. The transmission format mirrors a code hopping transmission. The response replaces the 32-bit code hopping portion of the transmission. If the response is a 16-bit response, the 16 bits are duplicated to make up the 32-bit code hopping portion. The preamble, serial number, CRC, and queuing bits are all transmitted as normal (Figure 2-19).

This feature will be used in applications which use RF for long distance unidirectional authentication and short distance IFF.

| | |
|---|---|
| **Note:** | If code word blanking is enabled, the HCS410 will not give any ACK pulses after a read, write or IFF. |

### 2.6.3 INTELLIGENT DAMPING

If the LC circuit on the transponder has a high Q-factor, the circuit will keep on resonating for a long time after the field has been shut down by the decoder. This makes fast communication from the decoder to the HCS410 difficult. If the IDAMP bit is set to 0, the HCS410 will clamp the LC pins for 5 µs every 1/4 TE, whenever the HCS410 is expecting data from the decoder. The intelligent dumping pulses start 64 TE after the acknowledge pulses have been sent and continue for 64 TE. If the HSC410 detects data from the base station while sending out dump pulses, the dump pulses will continue to be sent. This option can be set in the configuration word.

## 2.7 LED Indicator

If a signal is detected on LC0, the LED pin goes high for 30 ms every 8s (IFFB = 0) or 4s (IFFB 1) to indicate that the power source is charging.

**FIGURE 2-19: IFF INDUCTIVE IN RF OUT**

**FIGURE 2-20: LED INDICATOR WHEN CHARGING POWER SOURCE**

*Patents have been applied for.

## 3.0 EEPROM ORGANIZATION AND CONFIGURATION

The HCS410 has nonvolatile EEPROM memory which is used to store user programmable options. This information includes encoder keys, serial number, and up to 64-bits of user information.

The HCS410 has two modes in which it operates as specified by the configuration word. In the first mode the HCS410 has a single encoder key which is used for encrypting the code hopping portion of a CH Mode transmission and generating a response during IFF validation. Seed transmissions are allowed in this mode. In the second mode the HCS410 is a transponder device with two encoder keys.

The two different operating modes of the HCS410 lead to different EEPROM memory maps.

In IFF1 mode, the HCS410 can act as a code hopping encoder with Seed transmission, and as an IFF token with one key.

| IFF1 Mode |
|---|
| 64-bit Encoder Key 1 |
| 64-bit Seed/Transport Code<br>(SEED0, SEED1, SEED2, SEED3) |
| 32-bit Serial Number<br>(SER0, SER1) |
| 64-bit User Area<br>(USR0, USR1, USER2, USR3) |
| 10-bit Discrimination Value and 2 Overflow Bits. |
| 16-bit Synchronization Counter |
| Configuration Data |

In IFF2 mode, the HCS410 is able to act as a code hopping transmitter and an IFF token with two encoder keys.

| IFF2 Mode |
|---|
| 64-bit Encoder Key 1 |
| 64-bit Encoder Key 2/Transport Code |
| 32-bit Serial Number<br>(SER0, SER1) |
| 64-bit User EEPROM<br>(USR0, USR1, USER2, USR3) |
| 10-bit Discrimination Value and 2 Overflow Bits. |
| 16-bit Synchronization Counter |
| Configuration Data |

### 3.1 Encoder Key 1 and 2

The 64-bit encoder key1 is used by the transmitter to create the encrypted message transmitted to the receiver in Code Hopping Mode. An IFF operation, can use encoder key1 or key2 to generate the response to a challenge received. The key(s) is created and programmed at the time of production using a key generation algorithm. Inputs to the key generation algorithm are the serial number or seed for the particular transmitter being used and a secret manufacturer's code. While a number of key generation algorithms are supplied by Microchip, a user may elect to create their own method of key generation. This may be done providing that the decoder is programmed with the same means of creating the key for decryption purposes. If a seed is used (CH Mode), the seed will also form part of the input to the key generation algorithm.

### 3.2 Discrimination Value and Overflow

The discrimination value forms part of the code hopping portion of a code hopping transmission. The least significant 10 bits of the discrimination value are typically set to the least significant bits of the serial number. The most significant 2 bits of the discrimination value are the overflow bits (OVR1: OVR0). These are used to extend the range of the synchronization counter. When the synchronization counter wraps from $FFFF_{16}$ to $0000_{16}$ OVR0 is cleared and the second time a wrap occurs OVR1 is cleared.

Once cleared, the overflow bits cannot be set again, thereby creating a permanent record of the counter overflow.

### 3.3 16-bit Synchronization Counter

This is the 16-bit synchronization counter value that is used to create the code hopping portion for transmission. This value will be changed after every transmission. The synchronization counter is not used in IFF mode.

*Patents have been applied for.


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### 3.4 60/64-bit Seed Word/Transport Code

This is the 60-bit seed code that is transmitted when seed transmission is selected. This allows the system designer to implement the secure learn feature or use this fixed code word as part of a different key generation/tracking process or purely as a fixed code transmission. The seed is not available in IFF2-mode. A Seed transmission can be initiated in two ways, depending on the button inputs (Figure 3-1).

Seed transmission is available for function codes (Table 2-2) S[2:0] = 111 and S[2:0] = 011 (delayed). The delayed seed transmission starts with a normal code hopping transmission being transmitted for 3 seconds, before switching to a seed transmission. The two seed transmissions are shown in Figure 3-1.

The least significant 32-bits of the seed are used as the transport code. The transport code is used to write-protect the serial number, configuration word, as well as preventing accidental programming of the HCS410 when in IFF mode.

| | |
|---|---|
| **Note:** | If both SEED and TMPSD are set, IFF2 mode is enabled. |

### 3.5 Encoder Serial Number

There are 32 bits allocated for the serial number and a selectable configuration bit (XSER) determines whether 32 or 28 bits will be transmitted. The serial number is meant to be unique for every transmitter.

### 3.6 User Data

The 64-bit user EEPROM can be reprogrammed and read at any time using the IFF interface.

**FIGURE 3-1: SEED TRANSMISSION**

## 3.7 Configuration Data

The configuration data is used to select various encoder options. Further explanations of each of the bits are described in the following sections.

**TABLE 3-1: CONFIGURATION OPTIONS SEED**

| Symbol | Description |
|---|---|
| CWBE | Code Word Blanking Enable |
| IDAMP | Intelligent Damping for High Q LC Tank. |
| SEED/ IFF2 | Enable Seed Transmissions |
| TMPSD/ IFF2 | Temporary Seed Transmissions |
| OSC0:3 | Onboard Oscillator Tuning Bits |
| MTX3 | Minimum 3 Code Words Transmitted |
| VLOW | Low Voltage Trip Point Selection |
| LED | Enable LED output |
| BSL0:1 | Baudrate Select |
| TBSL | Transponder Baud Rate |
| MANCH | Manchester Modulation Mode |
| ACOLI | Anti Collision Communication Enable |
| XPRF | Passive Proximity Activation |
| DINC | Delayed Increment Enable |
| XSER | Extended Serial Number |

### 3.7.1 CWBE: CODE WORD BLANKING ENABLE BSL: BAUD RATE SELECT

Selecting this option allows code blanking as shown in Table 3-3. If this option is not selected, all code words are transmitted.

### 3.7.2 IDAMP: INTELLIGENT DAMPING

If IDAMP is set to ‘1’ intelligent damping is disabled.

### 3.7.3 SEED, TMPSD: SEED TRANSMISSION

| SEED | TMPSD | Description |
|---|---|---|
| 0 | 0 | No Seed/1 IFF Key |
| 0 | 1 | Seed Limited* |
| 1 | 0 | Always Enabled |
| 1 | 1 | IFF2/No Seed/2 IFF Keys |

* Seed transmissions are allowed till the sychronization counter crosses a $XX7F_{16}$ boundary. e.g. If the counter is initialized to $0000_{16}$ when the device is programmed, seed transmissions will be allowed until the counter wraps from $007F_{16}$ to $0080_{16}$ giving the user 127 transmissions before seed transmissions are disabled.

### 3.7.4 OSC: OSCILLATOR TUNING BITS

These bits allow the onboard oscillator to be tuned to within 10% of the nominal oscillator speed over both temperature and voltage.

**TABLE 3-2: OSCILLATOR TUNING**

| OSC | Description |
|---|---|
| 1000 | Fastest |
| 1001<br>1010<br>•<br>•<br>•<br>1111 | Faster |
| 0000 | Nominal |
| 0001<br>0010<br>•<br>•<br>•<br>0110 | Slower |
| 0111 | Slowest |

**TABLE 3-3: BAUD RATE SELECTION**

| | | Code Hopping Transmissions (TE) | | | Transponder Communication (TE) | |
|---|---|---|---|---|---|---|
| BSL 1 | BSL 0 | PWM | Manchester | Codes Word Transmitted* | TBSL | PPM |
| 0 | 0 | 400 µs | 800 µs | All | 0 | 200 µs |
| 0 | 1 | 200 µs | 400 µs | 1 of 2 | — | — |
| 1 | 0 | 100 µs | 200 µs | 1 of 2 | — | — |
| 1 | 1 | 100 µs | 200 µs | 1 of 4 | 1 | 100 µs |

**Note:** *If code word blanking is enabled.


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### 3.7.5 **MTX3:** MINIMUM CODE WORDS COMPLETED

If this bit is set, the HCS410 will transmit a minimum of 3 words before it powers itself down. If this bit is cleared, the HCS410 will only complete the current transmission. This feature will only work if $V_{DD}$ is connected directly to the battery as shown in Figure 2-1.

### 3.7.6 **$V_{LOW}$:** LOW VOLTAGE TRIP POINT

The low voltage trip point select bit is used to tell the HCS410 what Vdd level is being used. This information will be used by the device to determine when to send the voltage low signal to the receiver. When this bit is set, the Vdd level is assumed to be operating from a 5 volt or 6 volt supply. If the bit is cleared, then the Vdd level is assumed to be 3.0 volts. Refer to Figure 6-3 for voltage trip point. When the battery reaches the Vlow point, the LED will flash once for 200 ms on during a code hopping transmission.

### 3.7.7 **LED:** OUTPUT ENABLE

If this bit is set, the S2 doubles as an LED output line. If this bit is cleared (0), S2 is only used as an input.

### 3.7.8 **TBSL:** TRANSPONDER BAUD RATE SELECT

This option selects the baud rate for IFF communication between a $T_E$ of 100 µs or 200 µs.

### 3.7.9 **MANCH:** MANCHESTER CODE ENCODING

MANCH selects between Manchester code modulation and PWM modulation in code hopping mode. If MANCH = 1, Manchester code modulation is selected. If MANCH is cleared, PWM modulation is selected.

### 3.7.10 **ACOLI:** ANTI-COLLISION COMMUNICATION AND **XPRF**: TRANSPONDER ECHOING ON PWM OUTPUT

**ACOLI = 1, XPRF = 0**

If ACOLI is set the anti-collision operation during bi-directional transponder mode (IFF) is enabled. This feature is useful in situations where multiple transponders enter the magnetic field simultaneously.

**ACOLI = 0, XPRF = 1**

If XPRF is set, and ACOLI is cleared, proximity activation is enabled. the HCS410 starts sending out ACK pulses when it detects a magnetic field. If the HCS410 doesn't receive a start bit from the decoder within 50 ms of sending the first set of ACK pulses, the HCS410 will transmit a code hopping transmission PWM pin for 2 seconds.

**ACOLI = 1, XPRF = 1**

If both the ACOLI and XPRF are set, all of the HCS410 transponder responses are echoed on the PWM output, as described in Section 2.6.2.

### 3.7.11 **DINC:** DELAYED INCREMENT

If DINC is set to '1', the delayed increment feature is enabled. If DINC is cleared, the counter only increments once each time the button is pressed.

### 3.7.12 **XSER:** EXTENDED SERIAL NUMBER

If XSER is set, bits 60 to 63 of the transmission are the most significant bits of the serial number or seed. If XSER bit is cleared, bits 60 to 63 of the transmission are set to the function code used to activate the device (S2:S1:S0:0).

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## 4.0 INTEGRATING THE HCS410 INTO A SYSTEM

Use of the HCS410 in a system requires a compatible decoder. This decoder is typically a microcontroller with compatible firmware. Firmware routines that accept transmissions from the HCS410, decrypt the code hopping portion of the data stream and perform IFF functions are available. These routines provide system designers the means to develop their own decoding system.

### 4.1 Key Generation

The serial number for each transmitter is programmed by the manufacturer at the time of production. The generation of the encoder key is done using a key generation algorithm (Figure 4-1). Typically, inputs to the key generation algorithm are the serial number of the transmitter or seed value, and a 64-bit manufacturer's code. The manufacturer's code is chosen by the system manufacturer and must be carefully controlled. The manufacturer's code is a pivotal part of the overall system security.

**FIGURE 4-1: CREATION AND STORAGE OF ENCODER KEY DURING PRODUCTION**

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## 4.2 Learning an HCS410 to a Receiver

In order for a transmitter to be used with a decoder, the transmitter must first be 'learned'. Several learning strategies can be followed in the decoder implementation. When a transmitter is learned to a decoder, it is suggested that the decoder stores the serial number and current synchronization counter value (synchronization counter stored in CH Mode only) in EEPROM. The decoder must keep track of these values for every transmitter that is learned (Figure 4-2 and Figure 4-3).

**FIGURE 4-2: TYPICAL CH MODE LEARN SEQUENCE**

The maximum number of transmitters that can be learned is only a function of how much EEPROM memory storage is available. The decoder must also store the manufacturer's code in order to learn an HCS410, although this value will not change in a typical system so it is usually stored as part of the microcontroller ROM code. Storing the manufacturer's code as part of the ROM code is also better for security reasons.

**FIGURE 4-3: TYPICAL IFF LEARN SEQUENCE**

## 4.3 CH Mode Decoder Operation

In a typical decoder operation (Figure 4-4), the key generation on the decoder side is done by taking the serial number from a transmission and combining that with the manufacturer's code to create the same encoder key that is stored in the HCS410. Once the encoder key is obtained, the rest of the transmission can be decrypted. The decoder waits for a transmission and immediately checks the serial number to determine if it is a learned transmitter. If it is, the code hopping portion of the transmission is decrypted using the stored key. It uses the discrimination bits to determine if the decryption was valid. If everything up to this point is valid, the synchronization counter value is evaluated.

**FIGURE 4-4: TYPICAL CH MODE DECODER OPERATION**

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### 4.3.1 SYNCHRONIZATION WITH DECODER

The KEELOQ technology features a sophisticated synchronization technique (Figure 4-5) which does not require the calculation and storage of future codes. If the stored counter value for that particular transmitter and the counter value that was just decrypted are within a window of say 16, the counter is stored and the command is executed. If the counter value was not within the single operation window, but is within the double operation window of say 32K window, the transmitted synchronization counter value is stored in temporary location and it goes back to waiting for another transmission. When the next valid transmission is received, it will compare the new value with the one in temporary storage. If the two values are sequential, it is assumed that the counter had just gotten out of the single operation 'window', but is now back in sync, so the new synchronization counter value is stored and the command executed. If a transmitter has somehow gotten out of the double operation window, the transmitter will not work and must be relearned. Since the entire window rotates after each valid transmission, codes that have been used are part of the 'blocked' (32K) codes and are no longer valid. This eliminates the possibility of grabbing a previous code and retransmitting to gain entry.

| | |
|---|---|
| **Note:** | The synchronization method described in this section is only a typical implementation and because it is usually implemented in firmware, it can be altered to fit the needs of a particular system |

**FIGURE 4-5: SYNCHRONIZATION WINDOW**

**FIGURE 4-6: BASIC OPERATION OF A CODE HOPPING RECEIVER (DECODER)**

## 4.4 IFF Decoder Operation

In a typical IFF decoder, the key generation on the decoder side is done by reading the serial number from a token and combining that with the manufacturer's code to recreate the encoder key that is stored on the token. The decoder polls for the presence of a token. Once detected the decoder reads the serial number. If the token has been learned, the decoder sends a challenge and reads the token's response. The decoder uses the encoder key stored in EEPROM and decrypt response. The decrypt response is compared to the challenge. If they match the appropriate output is activated.

**FIGURE 4-7: TYPICAL IFF DECODER OPERATION**

**FIGURE 4-8: BASIC OPERATION OF AN IFF RECEIVER (DECODER)**

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## 5.0 DEVELOPMENT SUPPORT

The PIC® microcontrollers and dsPIC® digital signal controllers are supported with a full range of software and hardware development tools:

- Integrated Development Environment
  - MPLAB® IDE Software
- Compilers/Assemblers/Linkers
  - MPLAB C Compiler for Various Device Families
  - HI-TECH C for Various Device Families
  - MPASM™ Assembler
  - MPLINK™ Object Linker/ MPLIB™ Object Librarian
  - MPLAB Assembler/Linker/Librarian for Various Device Families
- Simulators
  - MPLAB SIM Software Simulator
- Emulators
  - MPLAB REAL ICE™ In-Circuit Emulator
- In-Circuit Debuggers
  - MPLAB ICD 3
  - PICkit™ 3 Debug Express
- Device Programmers
  - PICkit™ 2 Programmer
  - MPLAB PM3 Device Programmer
- Low-Cost Demonstration/Development Boards, Evaluation Kits, and Starter Kits

### 5.1 MPLAB Integrated Development Environment Software

The MPLAB IDE software brings an ease of software development previously unseen in the 8/16/32-bit microcontroller market. The MPLAB IDE is a Windows® operating system-based application that contains:

- A single graphical interface to all debugging tools
  - Simulator
  - Programmer (sold separately)
  - In-Circuit Emulator (sold separately)
  - In-Circuit Debugger (sold separately)
- A full-featured editor with color-coded context
- A multiple project manager
- Customizable data windows with direct edit of contents
- High-level source code debugging
- Mouse over variable inspection
- Drag and drop variables from source to watch windows
- Extensive on-line help
- Integration of select third party tools, such as IAR C Compilers

The MPLAB IDE allows you to:

- Edit your source files (either C or assembly)
- One-touch compile or assemble, and download to emulator and simulator tools (automatically updates all project information)
- Debug using:
  - Source files (C or assembly)
  - Mixed C and assembly
  - Machine code

MPLAB IDE supports multiple debugging tools in a single development paradigm, from the cost-effective simulators, through low-cost in-circuit debuggers, to full-featured emulators. This eliminates the learning curve when upgrading to tools with increased flexibility and power.

## 5.2 MPLAB C Compilers for Various Device Families

The MPLAB C Compiler code development systems are complete ANSI C compilers for Microchip's PIC18, PIC24 and PIC32 families of microcontrollers and the dsPIC30 and dsPIC33 families of digital signal controllers. These compilers provide powerful integration capabilities, superior code optimization and ease of use.

For easy source level debugging, the compilers provide symbol information that is optimized to the MPLAB IDE debugger.

## 5.3 HI-TECH C for Various Device Families

The HI-TECH C Compiler code development systems are complete ANSI C compilers for Microchip's PIC family of microcontrollers and the dsPIC family of digital signal controllers. These compilers provide powerful integration capabilities, omniscient code generation and ease of use.

For easy source level debugging, the compilers provide symbol information that is optimized to the MPLAB IDE debugger.

The compilers include a macro assembler, linker, preprocessor, and one-step driver, and can run on multiple platforms.

## 5.4 MPASM Assembler

The MPASM Assembler is a full-featured, universal macro assembler for PIC10/12/16/18 MCUs.

The MPASM Assembler generates relocatable object files for the MPLINK Object Linker, Intel® standard HEX files, MAP files to detail memory usage and symbol reference, absolute LST files that contain source lines and generated machine code and COFF files for debugging.

The MPASM Assembler features include:

- Integration into MPLAB IDE projects
- User-defined macros to streamline assembly code
- Conditional assembly for multi-purpose source files
- Directives that allow complete control over the assembly process

## 5.5 MPLINK Object Linker/ MPLIB Object Librarian

The MPLINK Object Linker combines relocatable objects created by the MPASM Assembler and the MPLAB C18 C Compiler. It can link relocatable objects from precompiled libraries, using directives from a linker script.

The MPLIB Object Librarian manages the creation and modification of library files of precompiled code. When a routine from a library is called from a source file, only the modules that contain that routine will be linked in with the application. This allows large libraries to be used efficiently in many different applications.

The object linker/library features include:

- Efficient linking of single libraries instead of many smaller files
- Enhanced code maintainability by grouping related modules together
- Flexible creation of libraries with easy module listing, replacement, deletion and extraction

## 5.6 MPLAB Assembler, Linker and Librarian for Various Device Families

MPLAB Assembler produces relocatable machine code from symbolic assembly language for PIC24, PIC32 and dsPIC devices. MPLAB C Compiler uses the assembler to produce its object file. The assembler generates relocatable object files that can then be archived or linked with other relocatable object files and archives to create an executable file. Notable features of the assembler include:

- Support for the entire device instruction set
- Support for fixed-point and floating-point data
- Command line interface
- Rich directive set
- Flexible macro language
- MPLAB IDE compatibility


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## 5.7 MPLAB SIM Software Simulator

The MPLAB SIM Software Simulator allows code development in a PC-hosted environment by simulating the PIC® MCUs and dsPIC® DSCs on an instruction level. On any given instruction, the data areas can be examined or modified and stimuli can be applied from a comprehensive stimulus controller. Registers can be logged to files for further run-time analysis. The trace buffer and logic analyzer display extend the power of the simulator to record and track program execution, actions on I/O, most peripherals and internal registers.

The MPLAB SIM Software Simulator fully supports symbolic debugging using the MPLAB C Compilers, and the MPASM and MPLAB Assemblers. The software simulator offers the flexibility to develop and debug code outside of the hardware laboratory environment, making it an excellent, economical software development tool.

## 5.8 MPLAB REAL ICE In-Circuit Emulator System

MPLAB REAL ICE In-Circuit Emulator System is Microchip's next generation high-speed emulator for Microchip Flash DSC and MCU devices. It debugs and programs PIC® Flash MCUs and dsPIC® Flash DSCs with the easy-to-use, powerful graphical user interface of the MPLAB Integrated Development Environment (IDE), included with each kit.

The emulator is connected to the design engineer's PC using a high-speed USB 2.0 interface and is connected to the target with either a connector compatible with in-circuit debugger systems (RJ11) or with the new high-speed, noise tolerant, Low-Voltage Differential Signal (LVDS) interconnection (CAT5).

The emulator is field upgradable through future firmware downloads in MPLAB IDE. In upcoming releases of MPLAB IDE, new devices will be supported, and new features will be added. MPLAB REAL ICE offers significant advantages over competitive emulators including low-cost, full-speed emulation, run-time variable watches, trace analysis, complex breakpoints, a ruggedized probe interface and long (up to three meters) interconnection cables.

## 5.9 MPLAB ICD 3 In-Circuit Debugger System

MPLAB ICD 3 In-Circuit Debugger System is Microchip's most cost effective high-speed hardware debugger/programmer for Microchip Flash Digital Signal Controller (DSC) and microcontroller (MCU) devices. It debugs and programs PIC® Flash microcontrollers and dsPIC® DSCs with the powerful, yet easy-to-use graphical user interface of MPLAB Integrated Development Environment (IDE).

The MPLAB ICD 3 In-Circuit Debugger probe is connected to the design engineer's PC using a high-speed USB 2.0 interface and is connected to the target with a connector compatible with the MPLAB ICD 2 or MPLAB REAL ICE systems (RJ-11). MPLAB ICD 3 supports all MPLAB ICD 2 headers.

## 5.10 PICkit 3 In-Circuit Debugger/ Programmer and PICkit 3 Debug Express

The MPLAB PICkit 3 allows debugging and programming of PIC® and dsPIC® Flash microcontrollers at a most affordable price point using the powerful graphical user interface of the MPLAB Integrated Development Environment (IDE). The MPLAB PICkit 3 is connected to the design engineer's PC using a full speed USB interface and can be connected to the target via an Microchip debug (RJ-11) connector (compatible with MPLAB ICD 3 and MPLAB REAL ICE). The connector uses two device I/O pins and the reset line to implement in-circuit debugging and In-Circuit Serial Programming™.

The PICkit 3 Debug Express include the PICkit 3, demo board and microcontroller, hookup cables and CDROM with user's guide, lessons, tutorial, compiler and MPLAB IDE software.

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## 5.11 PICkit 2 Development Programmer/Debugger and PICkit 2 Debug Express

The PICkit™ 2 Development Programmer/Debugger is a low-cost development tool with an easy to use interface for programming and debugging Microchip's Flash families of microcontrollers. The full featured Windows® programming interface supports baseline (PIC10F, PIC12F5xx, PIC16F5xx), midrange (PIC12F6xx, PIC16F), PIC18F, PIC24, dsPIC30, dsPIC33, and PIC32 families of 8-bit, 16-bit, and 32-bit microcontrollers, and many Microchip Serial EEPROM products. With Microchip's powerful MPLAB Integrated Development Environment (IDE) the PICkit™ 2 enables in-circuit debugging on most PIC® microcontrollers. In-Circuit-Debugging runs, halts and single steps the program while the PIC microcontroller is embedded in the application. When halted at a breakpoint, the file registers can be examined and modified.

The PICkit 2 Debug Express include the PICkit 2, demo board and microcontroller, hookup cables and CDROM with user's guide, lessons, tutorial, compiler and MPLAB IDE software.

## 5.12 MPLAB PM3 Device Programmer

The MPLAB PM3 Device Programmer is a universal, CE compliant device programmer with programmable voltage verification at $V_{DDMIN}$ and $V_{DDMAX}$ for maximum reliability. It features a large LCD display (128 x 64) for menus and error messages and a modular, detachable socket assembly to support various package types. The ICSP™ cable assembly is included as a standard item. In Stand-Alone mode, the MPLAB PM3 Device Programmer can read, verify and program PIC devices without a PC connection. It can also set code protection in this mode. The MPLAB PM3 connects to the host PC via an RS-232 or USB cable. The MPLAB PM3 has high-speed communications and optimized algorithms for quick programming of large memory devices and incorporates an MMC card for file storage and data applications.

## 5.13 Demonstration/Development Boards, Evaluation Kits, and Starter Kits

A wide variety of demonstration, development and evaluation boards for various PIC MCUs and dsPIC DSCs allows quick application development on fully functional systems. Most boards include prototyping areas for adding custom circuitry and provide application firmware and source code for examination and modification.

The boards support a variety of features, including LEDs, temperature sensors, switches, speakers, RS-232 interfaces, LCD displays, potentiometers and additional EEPROM memory.

The demonstration and development boards can be used in teaching environments, for prototyping custom circuits and for learning about various microcontroller applications.

In addition to the PICDEM™ and dsPICDEM™ demonstration/development board series of circuits, Microchip has a line of evaluation kits and demonstration software for analog filter design, KEELOQ® security ICs, CAN, IrDA®, PowerSmart battery management, SEEVAL® evaluation system, Sigma-Delta ADC, flow rate sensing, plus many more.

Also available are starter kits that contain everything needed to experience the specified device. This usually includes a single application and debug capability, all on one board.

Check the Microchip web page (www.microchip.com) for the complete list of demonstration, development and evaluation kits.


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## 6.0 ELECTRICAL CHARACTERISTICS

### TABLE 6-1: ABSOLUTE MAXIMUM RATING

| Symbol | Item | Rating | Units |
|---|---|---|---|
| $V_{DD}$ | Supply voltage | -0.3 to 6.6 | V |
| $V_{IN}$* | Input voltage | -0.3 to $V_{DD}$ + 0.3 | V |
| $V_{OUT}$ | Output voltage | -0.3 to $V_{DD}$ + 0.3 | V |
| $I_{OUT}$ | Max output current | 50 | mA |
| $T_{STG}$ | Storage temperature | -55 to +125 | C **(Note)** |
| $T_{LSOL}$ | Lead soldering temp | 300 | C **(Note)** |
| $V_{ESD}$ | ESD rating (Human Body Model) | 4000 | V |

**Note:** Stresses above those listed under “ABSOLUTE MAXIMUM RATINGS” may cause permanent damage to the device.

* If a battery is inserted in reverse, the protection circuitry switches on, protecting the device and draining the battery.

### TABLE 6-2: DC AND TRANSPONDER CHARACTERISTICS

**Commercial (C):** $T_{AMB}$ = 0°C to 70°C
**Industrial (I):** $T_{AMB}$ = -40°C to 85°C

| Parameter | Symbol | 2.0V < $V_{DD}$ < 6.3V | | | Unit | Conditions |
|---|---|---|---|---|---|---|
| | | Min | Typ(1) | Max | | |
| Average operating current(2) | $I_{DD}$ (avg) | — | 50<br>160 | 100<br>300 | µA | $V_{DD}$ = 3.0V<br>$V_{DD}$ = 6.3V |
| Programming current | $I_{DDP}$ | — | 1.0<br>2.2 | 1.8<br>3.5 | mA | $V_{DD}$ = 3.0V<br>$V_{DD}$ = 6.3V |
| Standby current | $I_{DDS}$ | — | 0.1 | 100 | nA | |
| High level input voltage | $V_{IH}$ | 0.55 $V_{DD}$ | — | $V_{DD}$ + 0.3 | V | |
| Low level input voltage | $V_{IL}$ | -0.3 | — | 0.15 $V_{DD}$ | V | |
| High level output voltage | $V_{OH}$ | 0.8 $V_{DD}$<br>0.8 $V_{DD}$ | — | — | V | $V_{DD}$ = 2V, $I_{OH}$ =- .45 mA<br>$V_{DD}$ = 6.3V, $I_{OH}$,= -2 mA |
| Low level output voltage | $V_{OL}$ | —<br>— | —<br>— | 0.08 $V_{DD}$<br>0.08 $V_{DD}$ | V | $V_{DD}$ = 2V, $I_{OH}$ = 0.5 mA<br>$V_{DD}$ = 6.3V,$I_{OH}$ = 5mA |
| LED output current | $I_{LED}$ | 3.0 | 4.0 | 7.0 | mA | $V_{DD}$ = 3.0V, $V_{LED}$ = 1.5V |
| Switch input resistor | RS | 40 | 60 | 80 | kΩ | |
| PWM input resistor | $R_{PWM}$ | 80 | 120 | 160 | kΩ | |
| LC input current | $I_{LC}$ | — | — | 10.0 | mA | $V_{LCC}$=15 $V_{P-P}$ |
| LC input clamp voltage | $V_{LCC}$ | — | 15 | — | V | $I_{LC}$ <10 mA |
| LC induced output current | $V_{DDI}$ | — | | 5.0 | mA | $V_{LCC}$ > 10V |
| LC induced output voltage | $V_{DDV}$ | 5.0<br>4.5 | 6.3<br>5.6 | 6.8<br>6.8 | V | 10 V < $V_{LCC}$, $I_{DD}$ = 0 mA<br>10 V < $V_{LCC}$, $I_{DD}$ = -1 mA |
| Carrier frequency | fc | — | 125 | — | kHz | |
| External LC Inductor value | L | — | 900 | — | µH | |
| External LC Capacitor value | C | — | 1.8 | — | nF | |

**Note 1:** Typical values at 25°C.
**2:** No load connected.
**3:** LC inputs are clamped at 15 volts.

**FIGURE 6-1: POWER UP AND TRANSMIT TIMING**

**TABLE 6-3: POWER UP AND TRANSMIT TIMING REQUIREMENTS**

**VDD = +2.0V to 6.3V**
**Commercial (C): TAMB = 0° C to +70° C**
**Industrial (I): TAMB = -40° C to +85° C**

| Parameter | Symbol | Min | Typ. | Max | Unit | Remarks |
|---|---|---|---|---|---|---|
| Time to second button press | TBP | 44 + Code Word Time | 58 + Code Word Time | 63 + Code Word Time | ms | **(Note 1)** |
| Transmit delay from button detect | TTD | 39 | 44 | 48 | ms | **(Note 2)** |
| Debounce delay | TDB | 31 | 35 | 39 | ms | |
| Auto-shutoff time-out period | TTO | 18 | 20 | 22 | s | **(Note 3)** |

**Note 1:** TBP is the time in which a second button can be pressed without completion of the first code word and the intention was to press the combination of buttons.
**2:** Transmit delay maximum value if the previous transmission was successfully transmitted.
**3:** The auto-shutoff timeout period is not tested.


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**FIGURE 6-2: HCS410 NORMALIZED TE VS. TEMP**

**Note:** Values are for calibrated oscillator.

**TABLE 6-4: CODE WORD TRANSMISSION TIMING PARAMETERS—PWM MODEÞ**

| VDD = +2.0V to 6.3V<br>Commercial (C): TAMB = 0°C to +70°C<br>Industrial (I): TAMB = -40°C to +85°C | | | Code Words Transmitted | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|
| | | | BSL1 = 0, BSL0 = 0 | | | | BSL1 = 0, BSL0 = 1 | | | |
| **Symbol** | **Characteristic** | **Number of TE** | **Min.** | **Typ.** | **Max.** | **Number of TE** | **Min.** | **Typ.** | **Max.** | **Units** |
| TE | Basic pulse element | 1 | 360 | 400 | 440 | 1 | 180.0 | 200.0 | 220.0 | µs |
| TBP | PWM bit pulse width | 3 | 1080 | 1200 | 1320 | 3 | 540.0 | 600.0 | 660.0 | µs |
| TP | Preamble duration | 32 | 12 | 12.8 | 14 | 32 | 5.76 | 6.0 | 7.04 | ms |
| TH | Header duration | 10 | 3.6 | 4.0 | 4.4 | 10 | 1.80 | 2.0 | 2.20 | ms |
| THOP | Code hopping duration | 96 | 35 | 38.4 | 42 | 96 | 17.28 | 19.20 | 21.12 | ms |
| TFIX | Fixed code duration | 111 | 39.96 | 44.4 | 48.84 | 111 | 19.98 | 22.20 | 24.42 | ms |
| TG | Guard time | 46 | 16.6 | 18.4 | 20.2 | 46 | 8.3 | 9.6 | 10.1 | ms |
| — | Total transmit time | 295 | 106.2 | 118.0 | 129.8 | 295 | 53.1 | 59.0 | 64.9 | ms |

**Note:** The timing parameters are not tested but derived from the oscillator clock.

| VDD = +2.0V to 6.3V<br>Commercial (C): TAMB = 0°C to +70°C<br>Industrial (I): TAMB = -40°C to +85°C | | | Code Words Transmitted | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|
| | | | BSL1 = 1, BSL0 = 0 | | | | BSL1 = 0, BSL0 = 1 | | | |
| **Symbol** | **Characteristic** | **Number of TE** | **Min.** | **Typ.** | **Max.** | **Number of TE** | **Min.** | **Typ.** | **Max.** | **Units** |
| TE | Basic pulse element | 1 | 180.0 | 200.0 | 220.0 | 1 | 90.0 | 100.0 | 110.0 | µs |
| TBP | PWM bit pulse width | 3 | 540.0 | 600.0 | 660.0 | 3 | 270.0 | 300.0 | 330.0 | µs |
| TP | Preamble duration | 32 | 5.76 | 6.0 | 7.04 | 32 | 2.88 | 3.0 | 3.52 | ms |
| TH | Header duration | 10 | 1.80 | 2.0 | 2.20 | 10 | 0.90 | 1.0 | 1.10 | ms |
| THOP | Code hopping duration | 96 | 17.28 | 19.20 | 21.12 | 96 | 8.64 | 9.60 | 10.56 | ms |
| TFIX | Fixed code duration | 111 | 19.98 | 22.2 | 24.42 | 111 | 9.99 | 11.1 | 12.21 | ms |
| TG | Guard time | 46 | 8.3 | 9.6 | 10.1 | 46 | 41 | 4.6 | 5.1 | ms |
| — | Total transmit time | 295 | 53.1 | 59.0 | 64.9 | 295 | 26.6 | 29.5 | 32.5 | ms |

**Note:** The timing parameters are not tested but derived from the oscillator clock.

### TABLE 6-5: CODE WORD TRANSMISSION TIMING PARAMETERS—MANCHESTER MODE

| VDD = +2.0V to 6.3V<br>Commercial (C): TAMB = 0°C to +70°C<br>Industrial (I): TAMB = -40°C to +85°C | | | Code Words Transmitted | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|
| | | | BSL1 = 0, BSL0 = 0 | | | | BSL1 = 0, BSL0 = 1 | | | |
| **Symbol** | **Characteristic** | **Number of TE** | **Min.** | **Typ.** | **Max.** | **Number of TE** | **Min.** | **Typ.** | **Max.** | **Units** |
| TE | Basic pulse element | 1 | 720.0 | 800.0 | 880.0 | 1 | 360.0 | 400.0 | 440.0 | µs |
| TP | Preamble duration | 32 | 23.04 | 25.60 | 28.16 | 32 | 11.52 | 12.80 | 14.08 | ms |
| TH | Header duration | 4 | 2.88 | 3.20 | 3.52 | 4 | 1.44 | 1.60 | 1.76 | ms |
| TSTART | Start bit | 2 | 1.44 | 1.60 | 1.76 | 2 | 0.72 | 0.80 | 0.88 | ms |
| THOP | Code hopping duration | 64 | 46.08 | 51.20 | 56.32 | 64 | 23.04 | 25.60 | 28.16 | ms |
| TFIX | Fixed code duration | 74 | 53.28 | 59.20 | 65.12 | 74 | 26.64 | 29.60 | 32.56 | ms |
| TSTOP | Stop bit | 2 | 1.44 | 1.60 | 1.76 | 2 | 0.72 | 0.80 | 0.88 | ms |
| TG | Guard time | 32 | 23.0 | 25.6 | 28.2 | 32 | 11.5 | 12.8 | 14.1 | ms |
| — | Total transmit time | 210 | 151.2 | 168 | 184.8 | 210 | 75.6 | 84.0 | 92.4 | ms |

**Note:** The timing parameters are not tested but derived from the oscillator clock.

| VDD = +2.0V to 6.3V<br>Commercial (C): TAMB = 0°C to +70°C<br>Industrial (I): TAMB = -40°C to +85°C | | | Code Words Transmitted | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|
| | | | BSL1 = 1, BSL0 = 0 | | | | BSL1 = 1, BSL0 = 1 | | | |
| **Symbol** | **Characteristic** | **Number of TE** | **Min.** | **Typ.** | **Max.** | **Number of TE** | **Min.** | **Typ.** | **Max.** | **Units** |
| TE | Basic pulse element | 1 | 360.0 | 400.0 | 440.0 | 1 | 180.0 | 200.0 | 220.0 | µs |
| TP | Preamble duration | 32 | 11.52 | 12.80 | 14.08 | 32 | 5.76 | 6.40 | 7.04 | ms |
| TH | Header duration | 4 | 1.44 | 1.60 | 1.76 | 4 | 0.72 | 0.80 | 0.88 | ms |
| TSTART | Start bit | 2 | 0.72 | 0.80 | 0.88 | 2 | 0.36 | 0.40 | 0.44 | ms |
| THOP | Code hopping duration | 64 | 23.04 | 25.60 | 28.16 | 64 | 11.52 | 12.80 | 14.08 | ms |
| TFIX | Fixed code duration | 74 | 26.64 | 29.60 | 32.56 | 74 | 13.32 | 14.8 | 16.28 | ms |
| TSTOP | Stop bit | 2 | 0.72 | 0.80 | 0.88 | 2 | 0.36 | 0.40 | 0.44 | ms |
| TG | Guard time | 32 | 11.5 | 12.8 | 14.1 | 32 | 5.8 | 6.4 | 7.0 | ms |
| — | Total transmit time | 210 | 75.6 | 84.0 | 92.4 | 210 | 37.8 | 42.0 | 46.2 | ms |

**Note:** The timing parameters are not tested but derived from the oscillator clock.

### FIGURE 6-3: TYPICAL VOLTAGE TRIP POINTS

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# 7.0 PACKAGING INFORMATION

## 7.1 Package Marking Information

8-Lead PDIP

Example

8-Lead SOIC

Example

| | | |
|---|---|---|
| **Legend:** | XX...X | Customer specific information* |
| | Y | Year code (last digit of calendar year) |
| | YY | Year code (last 2 digits of calendar year) |
| | WW | Week code (week of January 1 is week ‘01’) |
| | NNN | Alphanumeric traceability code |

**Note**: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line thus limiting the number of available characters for customer specific information.

* Standard PIC® MCU device marking consists of Microchip part number, year code, week code, and traceability code. For PIC device marking beyond this, certain price adders apply. Please check with your Microchip Sales Office. For QTP devices, any special marking adders are included in QTP price.

## 7.2 Package Details

### 8-Lead Plastic Dual In-Line (P) – 300 mil Body [PDIP]

**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging

| Units | | INCHES | | |
|---|---|---|---|---|
| Dimension Limits | | MIN | NOM | MAX |
| Number of Pins | N | 8 | | |
| Pitch | e | .100 BSC | | |
| Top to Seating Plane | A | – | – | .210 |
| Molded Package Thickness | A2 | .115 | .130 | .195 |
| Base to Seating Plane | A1 | .015 | – | – |
| Shoulder to Shoulder Width | E | .290 | .310 | .325 |
| Molded Package Width | E1 | .240 | .250 | .280 |
| Overall Length | D | .348 | .365 | .400 |
| Tip to Seating Plane | L | .115 | .130 | .150 |
| Lead Thickness | c | .008 | .010 | .015 |
| Upper Lead Width | b1 | .040 | .060 | .070 |
| Lower Lead Width | b | .014 | .018 | .022 |
| Overall Row Spacing § | eB | – | – | .430 |

**Notes:**

1. Pin 1 visual index feature may vary, but must be located with the hatched area.
2. § Significant Characteristic.
3. Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed .010" per side.
4. Dimensioning and tolerancing per ASME Y14.5M.
   BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing C04-018B


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### 8-Lead Plastic Small Outline (SN) - Narrow, 3.90 mm Body [SOIC]

**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging

Microchip Technology Drawing No. C04-057C Sheet 1 of 2

## 8-Lead Plastic Small Outline (SN) - Narrow, 3.90 mm Body [SOIC]

**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging

| Units | | MILLIMETERS | | |
|---|---|---|---|---|
| Dimension Limits | | MIN | NOM | MAX |
| Number of Pins | N | 8 | | |
| Pitch | e | 1.27 BSC | | |
| Overall Height | A | - | - | 1.75 |
| Molded Package Thickness | A2 | 1.25 | - | - |
| Standoff § | A1 | 0.10 | - | 0.25 |
| Overall Width | E | 6.00 BSC | | |
| Molded Package Width | E1 | 3.90 BSC | | |
| Overall Length | D | 4.90 BSC | | |
| Chamfer (Optional) | h | 0.25 | - | 0.50 |
| Foot Length | L | 0.40 | - | 1.27 |
| Footprint | L1 | 1.04 REF | | |
| Foot Angle | $\varphi$ | 0° | - | 8° |
| Lead Thickness | c | 0.17 | - | 0.25 |
| Lead Width | b | 0.31 | - | 0.51 |
| Mold Draft Angle Top | $\alpha$ | 5° | - | 15° |
| Mold Draft Angle Bottom | $\beta$ | 5° | - | 15° |

**Notes:**

1. Pin 1 visual index feature may vary, but must be located within the hatched area.
2. § Significant Characteristic
3. Dimensions D and E1 do not include mold flash or protrusions. Mold flash or protrusions shall not exceed 0.15mm per side.
4. Dimensioning and tolerancing per ASME Y14.5M
   BSC: Basic Dimension. Theoretically exact value shown without tolerances.
   REF: Reference Dimension, usually without tolerance, for information purposes only.

Microchip Technology Drawing No. C04-057C Sheet 2 of 2


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### 8-Lead Plastic Small Outline (SN) – Narrow, 3.90 mm Body [SOIC]

**Note:** For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging

RECOMMENDED LAND PATTERN

| Units | | MILLIMETERS | | |
|---|---|---|---|---|
| Dimension Limits | | MIN | NOM | MAX |
| Contact Pitch | E | 1.27 BSC | | |
| Contact Pad Spacing | C | | 5.40 | |
| Contact Pad Width (X8) | X1 | | | 0.60 |
| Contact Pad Length (X8) | Y1 | | | 1.55 |

Notes:

1. Dimensioning and tolerancing per ASME Y14.5M

   BSC: Basic Dimension. Theoretically exact value shown without tolerances.

Microchip Technology Drawing No. C04-2057A

## APPENDIX A: ADDITIONAL INFORMATION

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Code hopping encoder patents issued in European countries and U.S.A.

Secure learning patents issued in European countries, U.S.A. and R.S.A.

## REVISION HISTORY

### Revision F (June 2011)

- Updated the following sections: Development Support, The Microchip Web Site, Reader Response and HCS410 Product Identification System
- Added new section **Appendix A**
- Minor formatting and text changes were incorporated throughout the document


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## HCS410 PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales office.

**HCS410 — /P**

| | | |
|---|---|---|
| **Package:** | P = | Plastic DIP (300 mil Body), 8-lead |
| | SN = | Plastic SOIC (150 mil Body), 8-lead |
| | ST = | TSSOP (4.4 mm Body), 8-lead |
| **Temperature Range:** | Blank = | 0°C to +70°C |
| | I = | –40°C to +85°C |
| **Device:** | HCS410 | Code Hopping Encoder |
| | HCS410T | Code Hopping Encoder (Tape and Reel) |

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