



PNP PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

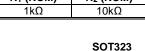
Features

- **Epitaxial Planar Die Construction**
- **Built-In Biasing Resistors**
- Surface Mount Package Suited for Automated Assembly
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

R ₁ (NOM)	R ₂ (NOM)
1kΩ	10kΩ

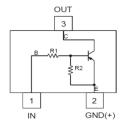
Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads. Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.006 grams (Approximate)





Top View



Device Schematic

Ordering Information (Note 5)

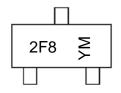
Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ADTA113ZUAQ-7	Automotive	2F8	7	8	3,000
ADTA113ZUAQ-13	Automotive	2F8	13	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information

SOT323



2F8 = Product Type Marking Code YM = Date Code Marking

Y = Year (ex: F = 2018)

M = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	2020	2021	202	22 20)23 2	2024	2025	2026	2027	2028
Code	F	G	Н		J		K	L	М	N	0	Р
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage <pin: (2)="" (3)="" to=""></pin:>	V _{CC}	-50	V
Input Voltage <pin: (1)="" (2)="" to=""></pin:>	V _{IN}	-10 to 5	V
Output Current	I _O	-100	mA
Output Current	I _C (Max)	-100	mA

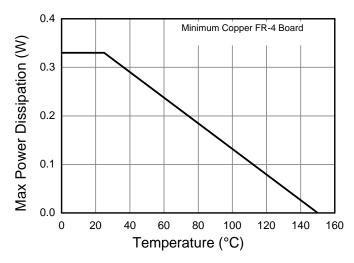
Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P_{D}	330	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	R _{0JA}	375	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

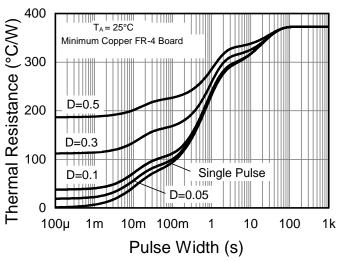
Note: 6. Mounted on FR-4 PC Board with minimum recommended pad layout.



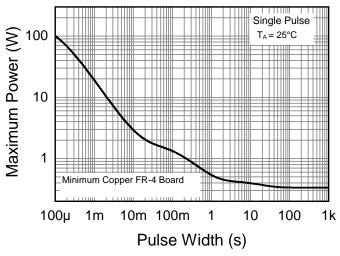
Thermal Characteristics and Derating Information



Derating Curve



Transient Thermal Impedance



Pulse Power Dissipation



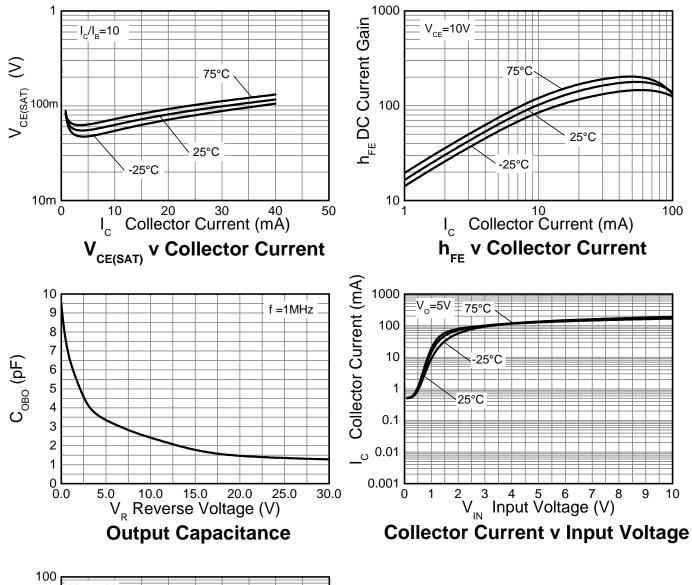
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

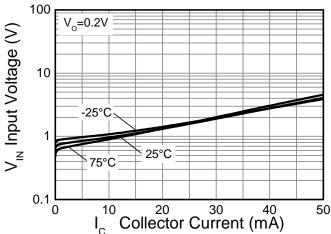
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Input Voltage	V _{I(OFF)} (Note 7)	-0.3	_		V	$V_{CC} = -5V$, $I_{O} = -100\mu A$
Input voltage	V _{I(ON)} (Note 8)	_	_	-3.0	V	$V_O = -0.3V$, $I_O = -20mA$
Output Voltage	V _{O(ON)}	_	-0.1	-0.3	V	$I_0/I_1 = -10 \text{mA} / -0.5 \text{mA}$
Input Current	I _I	_	_	-7.2	mΑ	$V_I = -5V$
Output Current	I _{O(OFF)}	_	_	-0.5	μΑ	$V_{CC} = -50V, V_{I} = 0V$
DC Current Gain	Gı	33	_	_	1	$V_0 = -5V$, $I_0 = -10mA$
Input Resistor (R ₁) Tolerance	ΔR_1	-30	_	30	%	_
Resistance Ratio Tolerance	$\Delta R_2/R_1$	-20	_	20	%	_
Gain-Bandwidth Product (Note 9)	f⊤	_	250	_	MHz	V _{CE} = -10V, I _E = -5mA, f = 100MHz

- 7. Guarantees that the device will be switched OFF if the Input Voltage is less than -0.3V. 8. Guarantees that the device will be switched ON if the Input Voltage is more than -3V. 9. Transistor For Reference Only.



Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)





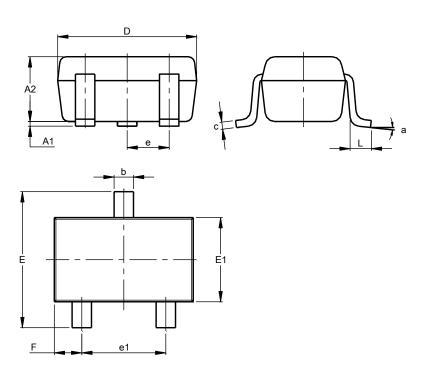
Input Voltage v Collector Current



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

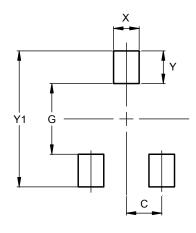


SOT323						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.25	0.40	0.30			
С	0.10	0.18	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	0.650 BSC					
e1	1.20	1.40	1.30			
F	0.375	0.475	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value
Dillicipiolis	(in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500



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