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FDB120N10 N-Channel PowerTrench[®] MOSFET 100 V, 74 A, 12 mΩ

Features

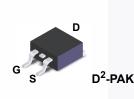
- $R_{DS(on)}$ = 9.7 m Ω (Typ.) @ V_{GS} = 10 V, I_D = 74 A
- · Fast Switching Speed
- Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{\text{DS}(\text{on})}$
- High Power and Current Handling Capability
- RoHS Compliant

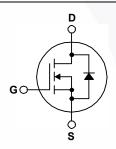
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench[®] process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

Applications

- Synchronous Rectification for ATX / Server / Telecom PSU
- Battery Protection Circuit
- Motor Drives and Uninterruptible Power Supplies
- Micro Solar Inverter





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted.

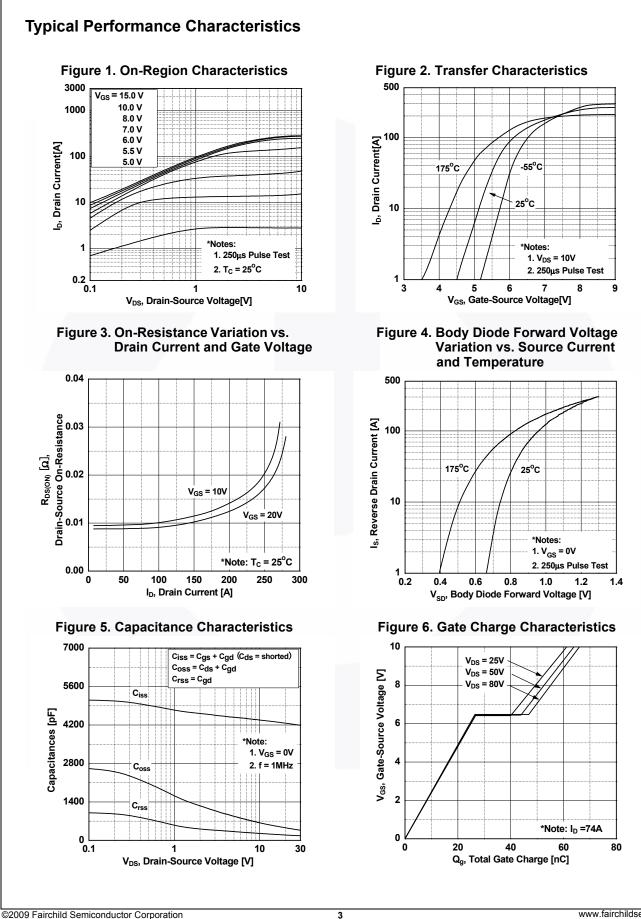
Symbol		Parameter	FDB120N10	Unit
V _{DSS}	Drain to Source Voltage	100	V	
V _{GSS}	Gate to Source Voltage		±20	V
I _D	Drain Current	- Continuous (T _C = 25 ^o C)	74	
	Drain Current	- Continuous (T _C = 100 ^o C)	52	- A
I _{DM}	Drain Current	- Pulsed (Note 1) 296	Α
E _{AS}	Single Pulsed Avalanche	2) 198	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		6.0	V/ns
P _D	Devuer Dissinction	$(T_{\rm C} = 25^{\rm o}{\rm C})$	170	W
	Power Dissipation	- Derate Above 25°C	1.14	W/ºC
T _J , T _{STG}	Operating and Storage T	-55 to +175	°C	
TL	Maximum Lead Tempera	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		

Thermal Characteristics

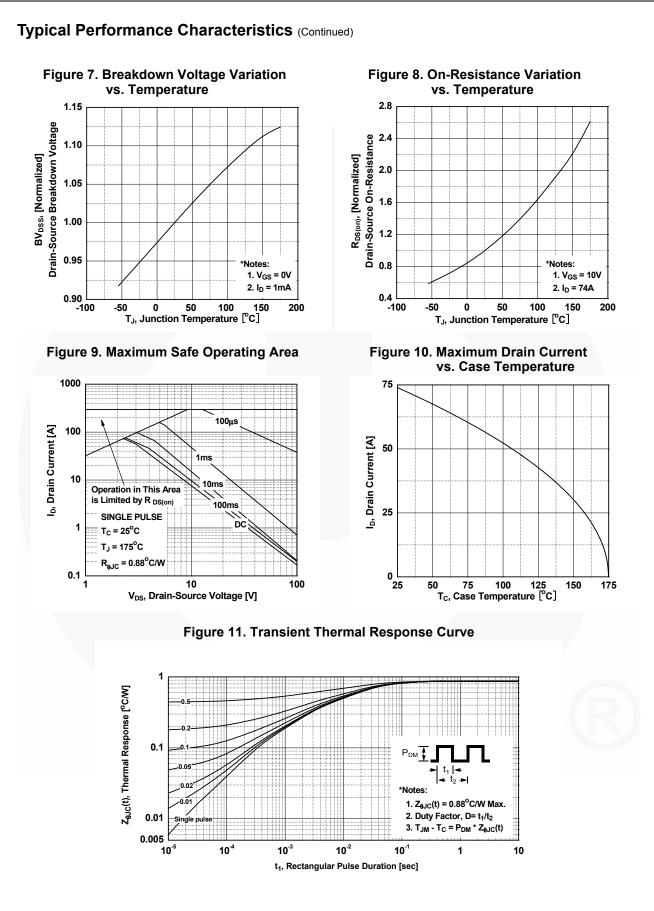
Symbol	Parameter FDB120N10		Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	0.88	
Р	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	62.5	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient (1 in ² Pad of 2-oz Copper), Max.	40	

Part Nun	•		Package	Packing Method	Reel Size	Тар	e Width	Qua	ntity
FDB120			D ² -PAK	Tape and Reel	330 mm	2	4 mm	800 units	
Electrica	I Chara	acteristics T _C = 25°C	unless oth	herwise noted.					
Symbol		Parameter		Test Condi	tions	Min.	Тур.	Max.	Unit
Off Charac	teristics	3							
BV _{DSS}	Drain to	Source Breakdown Voltage	e l _i	_D = 250 μA, V _{GS} = 0	V, T _C = 25°C	100	-	-	V
ΔΒV _{DSS} / ΔΤ _J	Breakdown Voltage Temperature Coefficient		١ _נ	$I_D = 250 \ \mu$ A, Referenced to 25° C		-	0.1	-	V/ºC
	Zero Ga	te Voltage Drain Current		V _{DS} = 100 V, V _{GS} = 0 V		-	-	1	μA
DSS	2010 00			/ _{DS} = 100 V, V _{GS} = 0		-	-	500	μΑ
I _{GSS}	Gate to	Body Leakage Current	V	$V_{\rm GS} = \pm 20 \text{ V}, \text{ V}_{\rm DS} = 0$	V	-	-	±100	nA
On Charac	teristics	5							
V _{GS(th)}	Gate Th	reshold Voltage	N	/ _{GS} = V _{DS} , I _D = 250 µ	μA	2.5	-	4.5	V
R _{DS(on)}	Static Dr	rain to Source On Resistand		$I_{GS} = 10 \text{ V}, I_D = 74 \text{ A}$		-	9.7	12	mΩ
9FS	Forward	Transconductance		V _{DS} = 10 V, I _D = 74 A			105	-	S
Dunamia C	horooto	riation							
-	Characteristics Input Capacitance						4215	5605	۳E
C _{iss}		Capacitance	\ \	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		-	4215	540	pF pF
C _{oss} C _{rss}	-	Transfer Capacitance	f				170	255	pF
Q _{g(tot)}		te Charge at 10V					66	86	nC
Q _{gs}		Source Gate Charge		/ _{DS} = 80 V I _D = 74 A,	,	-	26	-	nC
Q _{gd}		Drain "Miller" Charge	`	V _{GS} = 10 V (Note 4)		-	20	-	nC
	Charact		I				I		
Switching							07	64	
t _{d(on)}		Delay Time Rise Time		V _{DD} = 50 V, I _D = 74 A,		-	27	64 220	ns
t <u>r</u>		Delay Time		$I_{GS} = 10 \text{ V}, \text{ R}_{G} = 4.7$		•	105 39	88	ns
t _{d(off)} t _f		Fall Time					39 15	40	ns ns
4	Tuni-On				(Note 4)		10	40	113
Drain-Sour	ce Diod	e Characteristics							
I _S		n Continuous Drain to Sour				/-	-	74	Α
I _{SM}	Maximun	n Pulsed Drain to Source D					-	296	Α
V _{SD}	Drain to	Source Diode Forward Volta	age V	V _{GS} = 0 V, I _{SD} = 74 A		-	-	1.3	V
t _{rr}	Reverse	Recovery Time		V _{GS} = 0 V, I _{SD} = 74 A,		-	44	-	ns
Q _{rr}	Reverse Recovery Charge		d	dI _F /dt = 100 A/µs		-	67	-	nC

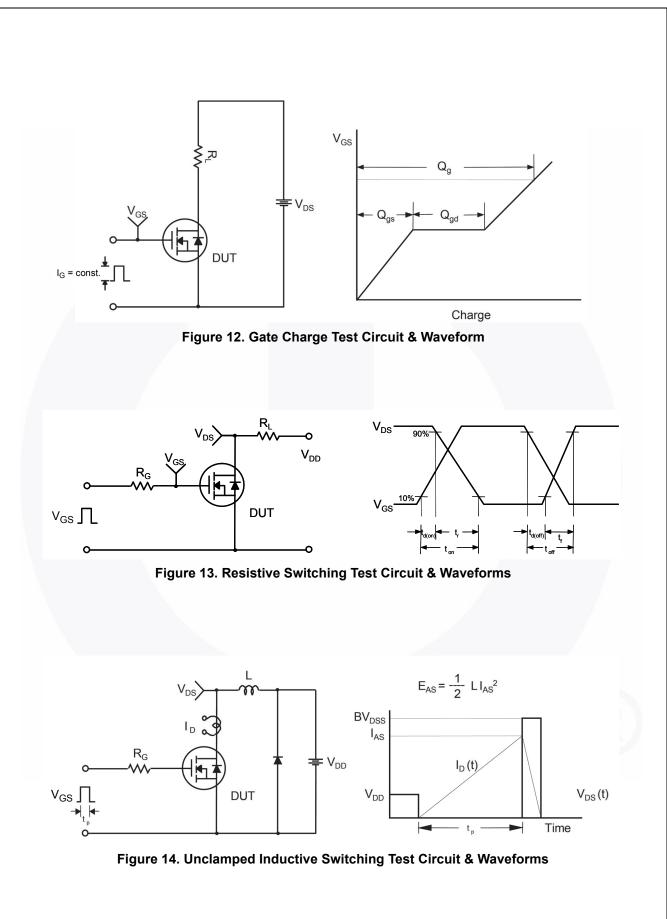
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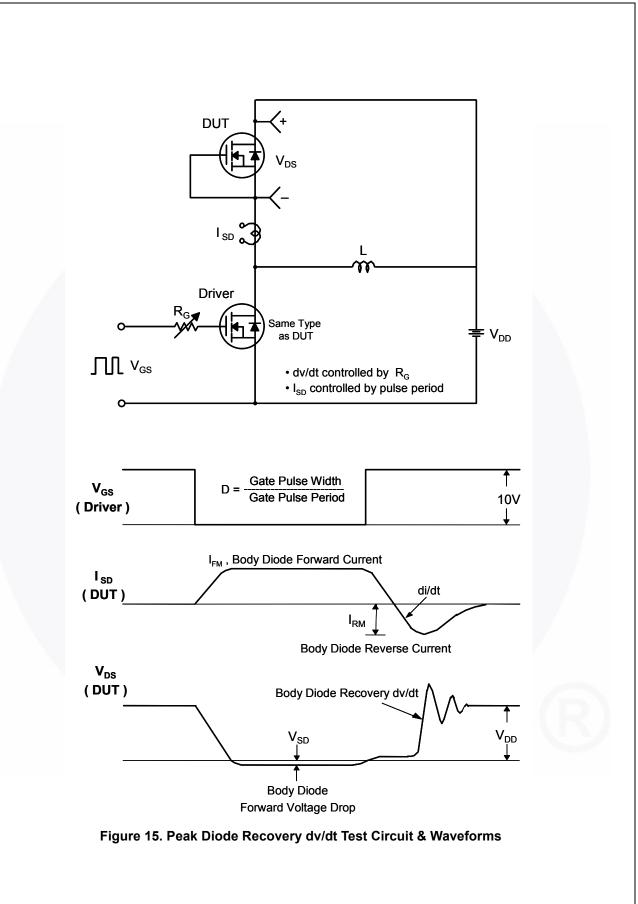
FDB120N10 Rev. C2

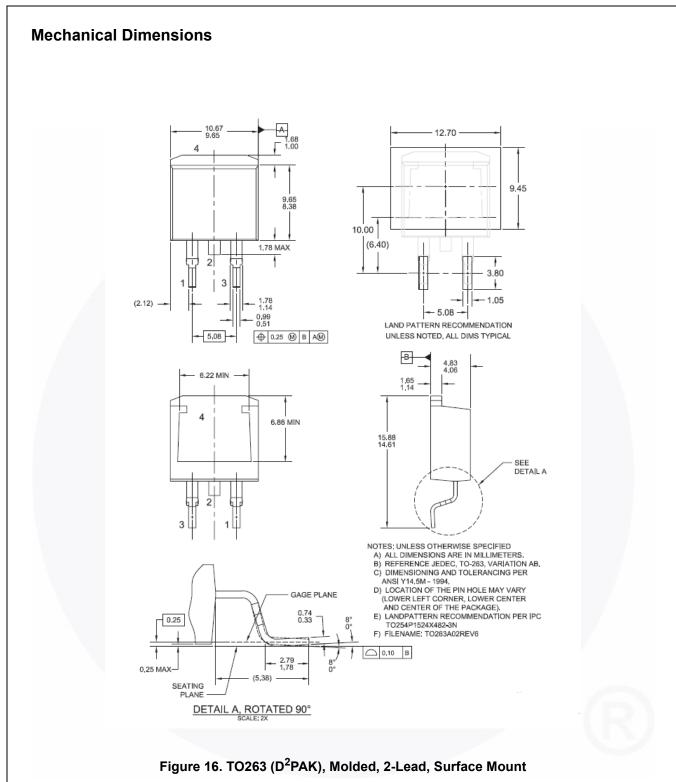


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Телефон: +7 812 627 14 35

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Адрес: 198099, Санкт-Петербург, Промышленная ул, дом № 19, литера Н, помещение 100-Н Офис 331