**Vishay Semiconductors** 

# **Optocoupler, Phototransistor Output, Very High Isolation Voltage**



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### DESCRIPTION

The CNY6XST, the high isolation voltage SMD version optocouplers consist of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin plastic package.

The single components are mounted opposite one another, providing a distance between input and output for highest safety requirements of > 3 mm.

### **VDE STANDARDS**

These couplers perform safety functions according to the following equipment standards:

- DIN EN 60747-5-2 (VDE 0884)
   Optocoupler for electrical safety requirements
- IEC 60065

Safety for mains-operated electronic and related household apparatus

• VDE 0160

Electronic equipment for electrical power installation

### FEATURES

- Rated recurring peak voltage (repetitive) V<sub>IORM</sub> = 1450 V<sub>peak</sub>
- Thickness through insulation ≥ 3 mm
- Creepage current resistance according to VDE 0303/IEC 60112 comparative tracking index: CTI ≥ 475
- Pb-free (e4)

RoHS

COMPLIANT

GREEN (5-2008)\*\*

- Moisture sensitivity level MSL4
  - Follow defined storage and soldering requirements
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

#### Note

\*\* Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

#### APPLICATIONS

- Solar and wind power diagnostic, monitoring, and communication equipment
- Welding equipment
- High voltage motors
- Switch-mode power supplies
- Line receiver
- Computer peripheral interface
- Microprocessor system interface
- Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):
  - for appl. class I to IV at mains voltage  $\leq$  300 V
  - for appl. class I to IV at mains voltage  $\leq 600 \text{ V}$
  - for appl. class I to III at mains voltage  $\leq$  1000 V according to DIN EN 60747-5-2 (VDE 0884)

## AGENCY APPROVALS

- DIN EN 60747-5-2 (VDE 0884) (pending)
- UL1577, file no. E76222 system code H, J, and K (pending)
- VDE related features:

- rated impulse voltage (transient overvoltage),  $V_{IOTM} = 12 \ kV_{peak}$ 

- isolation test voltage (partial discharge test voltage),  $V_{pd}$  = 2.8  $kV_{peak}$ 



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PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V <sub>R</sub>	5	V
Forward current		I <sub>F</sub>	75	mA
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	А
Power dissipation		P <sub>diss</sub>	120	mW
Junction temperature		Тj	100	°C
OUTPUT				
Collector emitter voltage		V <sub>CEO</sub>	32	V
Emitter collector voltage		V <sub>ECO</sub>	7	V
Collector current		Ι <sub>C</sub>	50	mA
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I <sub>CM</sub>	100	mA
Power dissipation		P <sub>diss</sub>	130	mW
Junction temperature		Tj	100	°C
COUPLER				
AC isolation test voltage CNY64AxxxST	t = 1 min	V <sub>ISO</sub>	8.2	kV <sub>RMS</sub>
DC isolation test voltage CNY65AxxxST	t = 1 s	V <sub>ISO</sub>	13.9	kV
Total power dissipation		P <sub>tot</sub>	250	mW
Ambient temperature range		T <sub>amb</sub>	- 55 to + 85	°C
Storage temperature range		T <sub>stg</sub>	- 55 to + 100	°C
Soldering temperature	2 mm from case, $\leq$ 10 s	T <sub>sld</sub>	260	°C

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>		1.32	1.6	V	
Junction capacitance	V <sub>R</sub> = 0, f = 1 MHz	Cj		50		pF	
OUTPUT							
Collector emitter voltage	I <sub>C</sub> = 1 mA	V <sub>CEO</sub>	32			V	
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7			V	
Collector emitter leakage current	$V_{CE} = 20 \text{ V}, \text{ I}_{F} = 0 \text{ mA}$	I <sub>CEO</sub>			200	nA	
COUPLER					•		
Collector emitter saturation voltage	I <sub>F</sub> = 10 mA, I <sub>C</sub> = 1 mA	V <sub>CEsat</sub>			0.3	V	
Cut-off frequency	$V_{CE}$ = 5 V, $I_F$ = 10 mA, $R_L$ = 100 $\Omega$	f <sub>c</sub>		110		kHz	
Coupling capacitance	f = 1 MHz	C <sub>k</sub>		0.3		pF	

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

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<b>CURRENT TRANSFER RATIO</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
I <sub>C</sub> /I <sub>F</sub>	V <sub>CE</sub> = 5 V, I <sub>F</sub> = 5 mA	CNY64ST	CTR	50		300	%	
		CNY65ST	CTR	50		300	%	
		CNY64AYST	CTR	50		150	%	
		CNY65AYST	CTR	50		150	%	
		CNY64ABST	CTR	80		150 150 240	%	
		CNY65ABST	CTR	80		240	%	
		CNY64AGRST	CTR	100		300	%	
		CNY65AGRST	CTR	100		300	%	

SAFETY AND INSULATION PARAMETERS								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s		$V_{pd}$	2.8			kV	
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s},$ (see figure 2)		V <sub>pd</sub>	2.2			kV	
	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$		R <sub>IO</sub>	10 <sup>12</sup>			Ω	
Insulation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$		R <sub>IO</sub>	10 <sup>11</sup>			Ω	
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)		R <sub>IO</sub>	10 <sup>9</sup>			Ω	
Forward current			I <sub>si</sub>			120	mA	
Power dissipation			P <sub>so</sub>			250	mW	
Rated impulse voltage			V <sub>IOTM</sub>			12	kV	
Safety temperature			T <sub>si</sub>			150	°C	
Tracking resistance (comparative tracking index)	Insulation group IVa		CTI	475				
Minimum external tracking (creepage distance)	Measured from	CNY64ST		≥ 9.5			mm	
	input pins to output pins	CNY65ST		≥14			mm	

#### Note

 According to DIN EN 60747-5-2 (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.



Fig. 1 - Safety Derating Diagram





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SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$V_S$ = 5 V, $I_C$ = 5 mA, $R_L$ = 100 $\Omega,$ (see figure 3)	t <sub>d</sub>		2.6		μs	
Rise time	$V_{S} = 5 \text{ V}, \text{ I}_{C} = 5 \text{ mA}, \text{ R}_{L} = 100 \Omega$ , (see figure 3)	t <sub>r</sub>		2.4		μs	
Fall time	$V_{S} = 5 \text{ V}, \text{ I}_{C} = 5 \text{ mA}, \text{ R}_{L} = 100 \Omega$ , (see figure 3)	t <sub>f</sub>		2.7		μs	
Storage time	$V_S$ = 5 V, $I_C$ = 5 mA, $R_L$ = 100 $\Omega,$ (see figure 3)	ts		0.3		μs	
Turn-on time	$V_{S} = 5 \text{ V}, \text{ I}_{C} = 5 \text{ mA}, \text{ R}_{L} = 100 \Omega$ , (see figure 3)	t <sub>on</sub>		5		μs	
Turn-off time	$V_{S} = 5 \text{ V}, \text{ I}_{C} = 5 \text{ mA}, \text{ R}_{L} = 100 \Omega$ , (see figure 3)	t <sub>off</sub>		3		μs	
Turn-on time	$V_S = 5 \text{ V}, \text{ I}_F = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega$ , (see figure 4)	t <sub>on</sub>		25		μs	
Turn-off time	$V_S$ = 5 V, $I_F$ = 10 mA, $R_L$ = 1 k $\Omega,$ (see figure 4)	t <sub>off</sub>		42.5		μs	



Fig. 3 - Test Circuit, Non-Saturated Operation



Fig. 5 - Switching Times



Fig. 4 - Test Circuit, Saturated Operation

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# **TYPICAL CHARACTERISTICS** ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)



Fig. 6 - Total Power Dissipation vs. Ambient Temperature



Fig. 7 - Forward Current vs. Forward Voltage



Fig. 8 - Collector Current vs. Collector Emitter Voltage (NS)



Fig. 9 - Collector Current vs. Collector Emitter Voltage



Fig. 10 - Leakage Current vs. Ambient Temperature



Fig. 11 - Normalized CTR (sat) vs. Ambient Temperature

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Fig. 12 - Normalized CTR (NS) vs. Ambient Temperature



Fig. 13 - Normalized CTR (NS) vs. Forward Current



Fig. 14 - Normalized CTR (sat) vs. Forward Current





Fig. 16 - F<sub>CTR</sub> vs. Phase Angle



Fig. 17 - Switching Time vs. Load Resistance

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### **SOLDERING GUIDLINES**

#### **Soldering Condition**

The CNY64AxST, CNY65AxST are lead (Pb)-free devices. They are suitable for reflow soldering. However due to large package size, the peak package body temperature should not go above 245 °C.

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#### Drypack

Devices are packed in moisture barrier bags (MBB) to prevent moisture absorption during transportation and storage. Each bag contains a desiccant bag.

#### PACKAGE DIMENSIONS in millimeters FOR CNY64AxxxST

#### Floor Life

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 72 h

Conditions: T<sub>amb</sub> < 30 °C, RH < 60 %

Moisture sensitivity level 4, according to J-STD-020.

#### Drying

In case of moisture absorption devices should be baked before soldering according to the recommended conditions shown below

48 h at 125 °C ± 5 °C, RH < 5%

(Not suitable for tape and reel)

In case the floor time has not exceeded 10 days the units can be baked in tape and reel according to the following conditions

168 h at 60 °C  $\pm$  5 °C, RH < 5 %

(Not suitable, if the floor time was exceeded by more than 10 days, or the allowed factory condition is exceeded)



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## PACKAGE DIMENSIONS in millimeters FOR CNY65AxxxST









Recommended footprint



6.544-5404.01-4

# PACKAGE MARKING (example)



technical drawings

according to DIN

specifications

#### Note

• The "T" at the end of the product designation is not marked on the package

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 $0.4 \pm 0.05$ 

Top cover tape

25.5

5.65

technical drawings

according to DIN specifications

## TAPE DIMENSIONS in millimeters FOR CNY64AxxxST



Leader (start) min. 400 mm without devices

Trailer (end) min. 200 mm without devices Drawing-No.: 9.700-5375.01-4 Issue: 1; 23.05.11

### TAPE DIMENSIONS in millimeters FOR CNY65AxxxST



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## **REEL DIMENSIONS** in millimeters



Issue: 1; 23.05.11

specifications

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