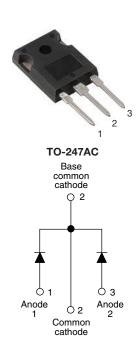




www.vishay.com

Vishay Semiconductors

HEXFRED® Ultrafast Soft Recovery Diode, 2 x 8 A



PRODUCT SUMMARY	
Package	TO-247AC
I _{F(AV)}	2 x 8 A
V_{R}	600 V
V _F at I _F	1.7 V
t _{rr} typ.	18 ns
T _J max.	150 °C
Diode variation	Common cathode

FEATURES

- Ultrafast and ultrasoft recovery
- Very low I_{RRM} and Q_{rr}
- Designed and qualified according to JEDEC-JESD47
- Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912





ROHS
COMPLIANT
HALOGEN
FREE
Available

BENEFITS

- Reduced RFI and EMI
- Reduced power loss in diode and switching transistor
- · Higher frequency operation
- · Reduced snubbing
- Reduced parts count

DESCRIPTION

VS-HFA16PA60C... is a state of the art center tap ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 V and 8 A per leg continuous current, the VS-HFA16PA60C... is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (IRRM) and does not exhibit any tendency to "snap-off" during the tb portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED VS-HFA16PA60C... is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Cathode to anode voltage	V_R		600	V				
Maximum continuous forward current per leg	I_	T _C = 100 °C	8					
per device	l _F		16	Α				
Single pulse forward current	I _{FSM}		60	A				
Maximum repetitive forward current	I _{FRM}		24					
Maximum power dissipation	P _D	T _C = 25 °C	36	W				
waxiiiiuiii powei uissipatioli	r _D	T _C = 100 °C	14] ""				
Operating junction and storage temperature range	T _J , T _{Stg}		- 55 to + 150	°C				



VS-HFA16PA60CPbF, VS-HFA16PA60C-N3

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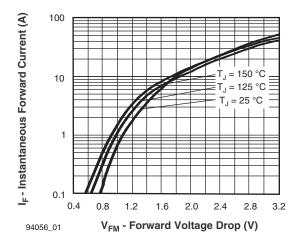
ELECTRICAL SPECIFICATIONS PER LEG (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS		
Cathode to anode breakdown voltage	V_{BR}	Ι _R = 100 μΑ		600	ı	-			
		I _F = 8.0 A		-	1.4	1.7	V		
Maximum forward voltage	V _{FM}	I _F = 16 A	See fig. 1	-	1.7	2.1			
		I _F = 8.0 A, T _J = 125 °C		-	1.4	1.7			
Maximum reverse			See fig. 2	-	0.3	5.0	μA		
leakage current	I _{RM}	$T_J = 125 ^{\circ}\text{C}, V_R = 0.8 \text{x} V_R \text{rated}$	See lig. 2	-	100	500	μΑ		
Junction capacitance	C _T	$V_R = 200 \text{ V}$ See fig.		-	10	25	pF		
Series inductance	L _S	Measured lead to lead 5 mm from body	-	8.0	-	nH			

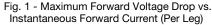
DYNAMIC RECOVERY CHARACTERISTICS PER LEG (T _J = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS		
_	t _{rr}	$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	18	-			
Reverse recovery time See fig. 5, 6 and 16	t _{rr1}	T _J = 25 °C		-	37	55	ns		
occ lig. 5, 5 and 15	t _{rr2}	T _J = 125 °C	$I_F = 8.0 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	55	90			
Peak recovery current See fig. 7 and 8	I _{RRM1}	T _J = 25 °C		-	3.5	5.0	A nC		
	I _{RRM2}	T _J = 125 °C		-	4.5	8.0			
Reverse recovery charge See fig. 9 and 10	Q _{rr1}	T _J = 25 °C		-	65	138			
	Q _{rr2}	T _J = 125 °C		-	124	360	IIC		
Peak rate of fall recovery current during t _b See fig. 11 and 12	dI _{(rec)M} /dt1	T _J = 25 °C		-	240	-	Δ /:		
	dI _{(rec)M} /dt2	T _J = 125 °C		-	210	-	A/μs		

THERMAL - MECHA	THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	YMBOL TEST CONDITIONS MIN. TYP. MAX.									
Lead temperature	T _{lead}	0.063" from case (1.6 mm) for 10 s	-	-	300	°C					
Junction to case, single leg conducting			-	-	3.5						
Junction to case, both leg conducting	R _{thJC}		-	-	1.75	12.001					
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	40	- K/W					
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.25	-						
Woight			-	6.0	-	g					
Weight			-	0.21	-	oz.					
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)					
Marking device		Case style TO-247AC (JEDEC)		HFA16	PA60C	•					



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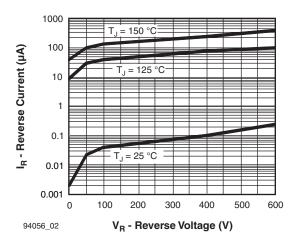


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Leg)

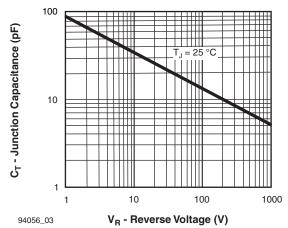


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

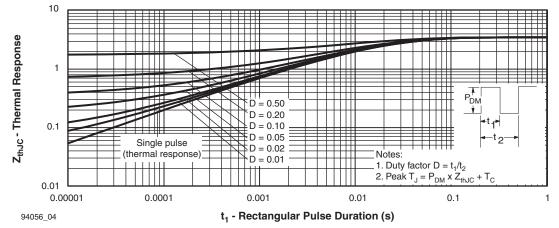


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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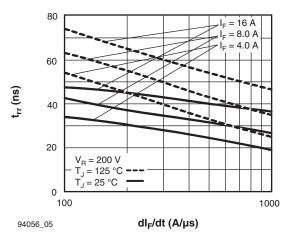


Fig. 5 - Typical Reverse Recovery Time vs. dl_E/dt (Per Leg)

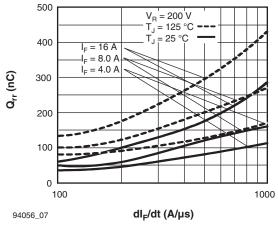


Fig. 7 - Typical Stored Charge vs. dl_F/dt (Per Leg)

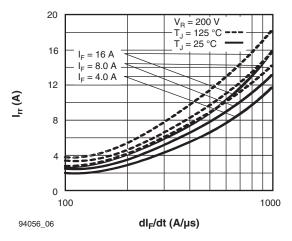


Fig. 6 - Typical Recovery Current vs. dl_F/dt (Per Leg)

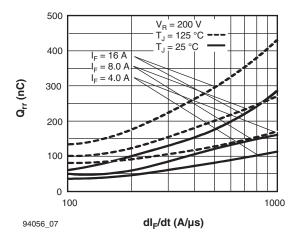


Fig. 8 - Typical dI_{(rec)M}/dt vs. dI_F/dt (Per Leg)

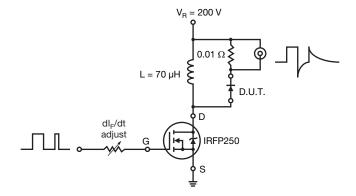
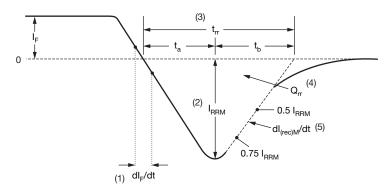


Fig. 9 - Reverse Recovery Parameter Test Circuit

VS-HFA16PA60CPbF, VS-HFA16PA60C-N3

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- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

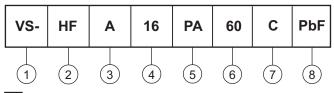
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 HEXFRED® family
- 3 Electron irradiated
- Current rating (16 = 16 A)
- 5 PA = TO-247AC
- 6 Voltage rating: (60 = 600 V)
- 7 Circuit configuration
 - C = Common cathode
- 8 Environmental digit:

PbF = Lead (Pb)-free and RoHS compliant

-N3 = Halogen-free, RoHS compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-HFA16PA60CPbF	25	500	Antistatic plastic tube						
VS-HFA16PA60C-N3	25	500	Antistatic plastic tube						

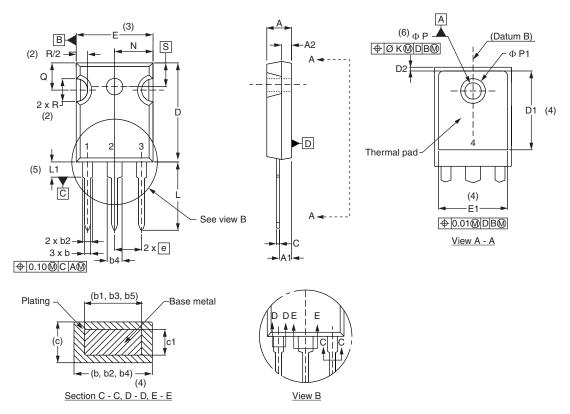
LINKS TO RELATED DOCUMENTS							
Dimensions		www.vishay.com/doc?95542					
Part marking information	TO-247ACPbF	www.vishay.com/doc?95226					
	TO-247AC-N3	www.vishay.com/doc?95007					



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TO-247 - 50 mils L/F

DIMENSIONS in millimeters and inches



CVMPOL	MILLIMETERS		INC	NOTES NOTES	SYMBOL	MILLIM	IETERS	INC	HES	NOTEC		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØK	0.2	254	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62 BSC 0.3				
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	7.39	-	0.291	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q



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Revision: 02-Oct-12 Document Number: 91000



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