

V_{DSS}	-12V
$R_{DS(on)}(Max.)$	260mΩ
I_D	±1.3A
P_D	0.8W

●Features

- 1) Pch MOSFET and schottky barrier diode are put in WEMT6 package.
- 2) High-speed switching and Low on-resistance.
- 3) Low voltage drive(1.5V drive)
- 4) Built in Low V_F schottky barrier diode.

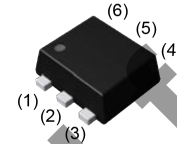
●Application

Switching

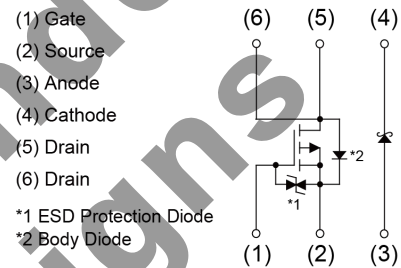
●Outline

SOT-563T

WEMT6



●Inner circuit



●Packaging specifications

Type	Packing	Embossed Tape
	Reel size (mm)	180
	Tape width (mm)	8
	Basic ordering unit (pcs)	8000
	Taping code	T2R
	Marking	U01

●Absolute maximum ratings ($T_a = 25^\circ\text{C}$, unless otherwise specified)

< MOSFET >

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	-12	V
Gate - Source voltage	V_{GSS}	±10	V
Continuous drain current	I_D	±1.3	A
Pulsed drain current	I_{DP}^{*1}	±2.6	A
Continuous source current (body diode)	I_S	-0.5	A
Pulsed source current (body diode)	I_{SP}^{*1}	-2.6	A
Power dissipation	P_D^{*2}	0.7	W/element
Junction temperature	T_j	150	°C

● **Absolute maximum ratings** ($T_a = 25^\circ\text{C}$)

< Diode >

Parameter	Symbol	Value	Unit
Repetitive peak reverse voltage	V_{RM}	25	V
Reverse voltage	V_R	20	V
Forward current	I_F	0.5	A
Forward current surge peak	I_{FSM}^{*3}	2.0	A
Power dissipation	P_D^{*2}	0.5	W/element
Junction temperature	T_j	150	$^\circ\text{C}$

< MOSFET + Diode >

Parameter	Symbol	Value	Unit
Power dissipation	P_D^{*2}	0.8	W/total
Operating junction and storage temperature range	T_{stg}	-55 to +150	$^\circ\text{C}$

● **Electrical characteristics** ($T_a = 25^\circ\text{C}$)

< MOSFET >

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 10\text{V}, V_{DS} = 0\text{V}$	-	-	± 10	μA
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = -1\text{mA}$	-12	-	-	V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -12\text{V}, V_{GS} = 0\text{V}$	-	-	-1	μA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = -6\text{V}, I_D = -1\text{mA}$	-0.3	-	-1.0	V
Static drain - source on - state resistance	$R_{DS(on)}^{*4}$	$V_{GS} = -4.5\text{V}, I_D = -1.3\text{A}$	-	190	260	m Ω
		$V_{GS} = -2.5\text{V}, I_D = -0.6\text{A}$	-	280	390	
		$V_{GS} = -1.8\text{V}, I_D = -0.6\text{A}$	-	400	600	
		$V_{GS} = -1.5\text{V}, I_D = -0.2\text{A}$	-	530	1060	
Forward Transfer Admittance	$ Y_{fs} ^{*4}$	$V_{DS} = -6\text{V}, I_D = -1.3\text{A}$	1.4	-	-	S

● **Electrical characteristics** ($T_a = 25^\circ\text{C}$)

< MOSFET >

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input capacitance	C_{iss}	$V_{GS} = 0\text{V}$	-	290	-	pF
Output capacitance	C_{oss}	$V_{DS} = -6\text{V}$	-	28	-	
Reverse transfer capacitance	C_{rss}	$f = 1\text{MHz}$	-	21	-	
Turn - on delay time	$t_{d(on)}^{*4}$	$V_{DD} \approx -6\text{V}, V_{GS} = -4.5\text{V}$	-	8	-	ns
Rise time	t_r^{*4}	$I_D = -0.6\text{A}$	-	10	-	
Turn - off delay time	$t_{d(off)}^{*4}$	$R_L = 10\Omega$	-	30	-	
Fall time	t_f^{*4}	$R_G = 10\Omega$	-	9	-	

● **Gate charge characteristics** ($T_a = 25^\circ\text{C}$)

< MOSFET >

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Total gate charge	Q_g^{*4}	$V_{DD} \approx -6\text{V}, I_D = -1.3\text{A}$ $V_{GS} = -4.5\text{V}$	-	2.4	-	nC
Gate - Source charge	Q_{gs}^{*4}		-	0.6	-	
Gate - Drain charge	Q_{gd}^{*4}		-	0.4	-	

● **Body diode electrical characteristics** (Source-Drain) ($T_a = 25^\circ\text{C}$)

< MOSFET >

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Forward voltage	V_{SD}^{*4}	$V_{GS} = 0\text{V}, I_S = -1.3\text{A}$	-	-	-1.2	V

● **Electrical characteristics** ($T_a = 25^\circ\text{C}$)

< Diode >

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Forward voltage	V_F	$I_F = 0.1\text{A}$	-	-	0.36	V
		$I_F = 0.5\text{A}$	-	-	0.52	V
Reverse current	I_R	$V_R = 20\text{V}$	-	-	100	μA

*1 $P_w \leq 0\mu\text{s}$, Duty cycle $\leq 1\%$

*2 Mounted on a ceramic board (30×30×0.8mm)

*3 60Hz·1cycle

*4 Pulsed

●Electrical characteristic curves <MOSFET>

Fig.1 Typical Output Characteristics(I)

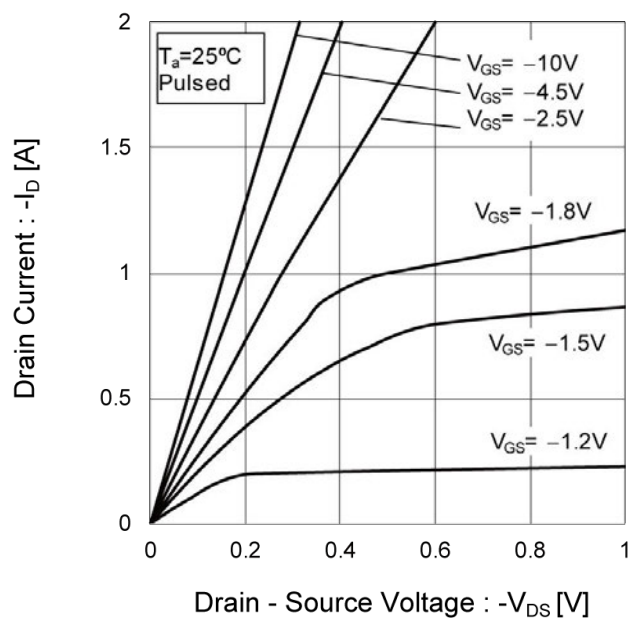


Fig.2 Typical Output Characteristics(II)

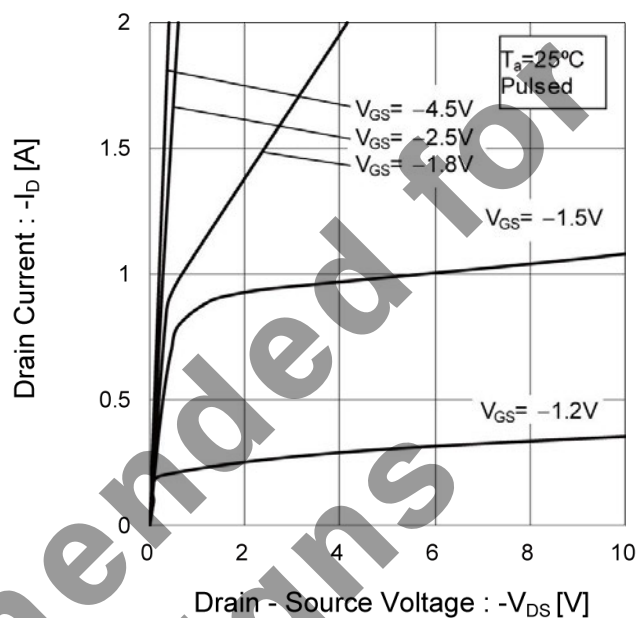


Fig.3 Breakdown Voltage vs. Junction Temperature

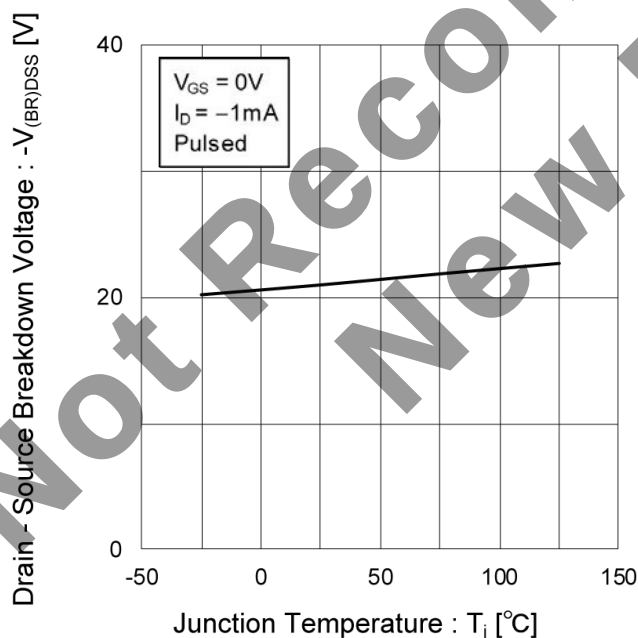
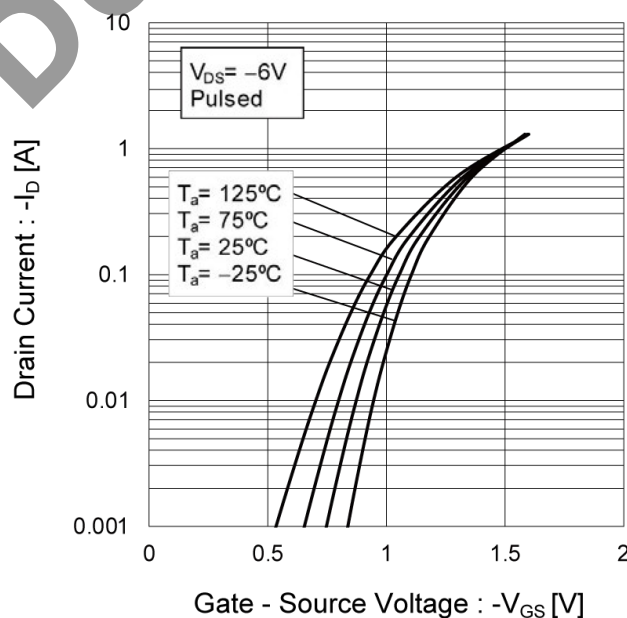


Fig.4 Typical Transfer Characteristics



●Electrical characteristic curves <MOSFET>

Fig.5 Gate Threshold Voltage vs.
Junction Temperature

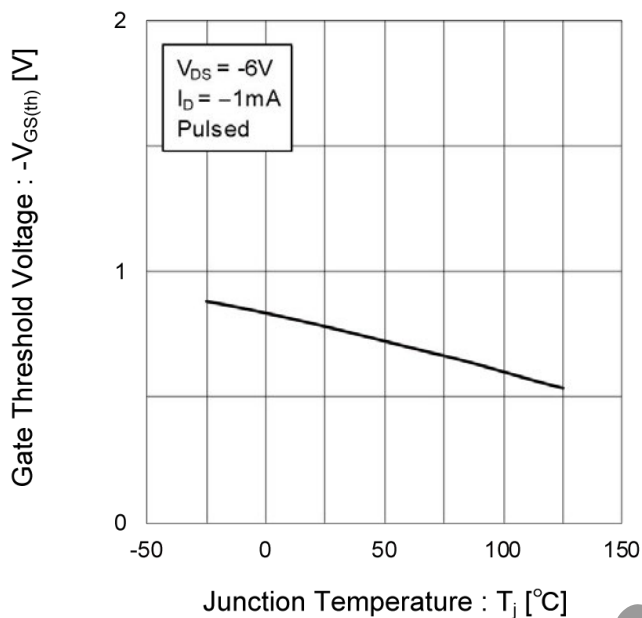


Fig.6 Forward Transfer Admittance
vs. Drain Current

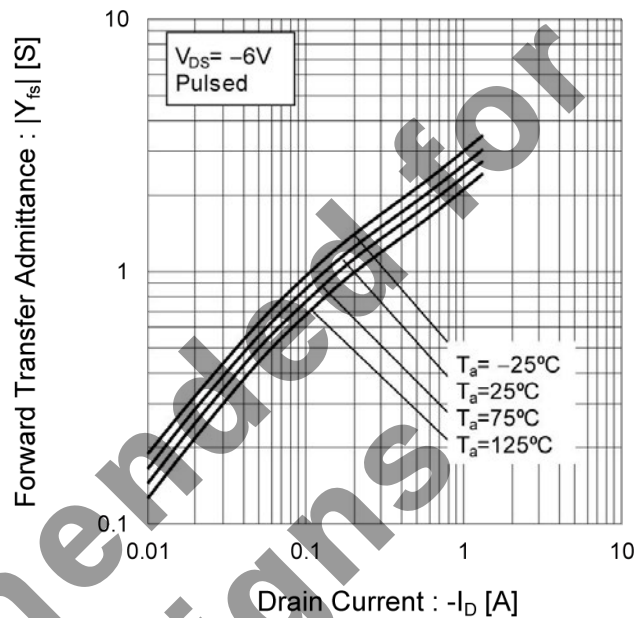


Fig.7 Drain Current Derating Curve

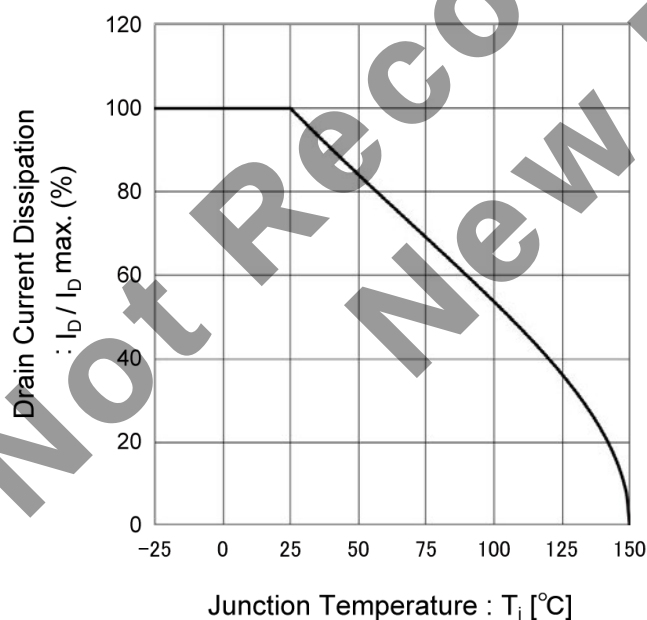
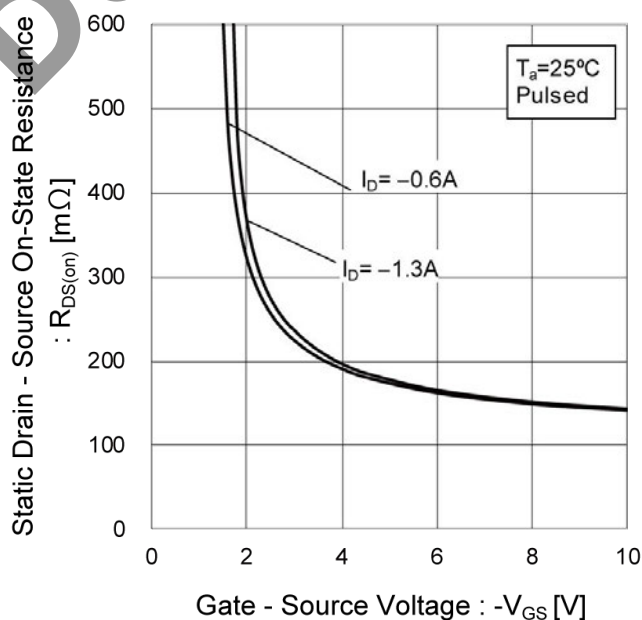


Fig.8 Static Drain - Source On - State
Resistance vs. Gate Source Voltage



●Electrical characteristic curves <MOSFET>

Fig.9 Static Drain - Source On - State Resistance vs. Junction Temperature

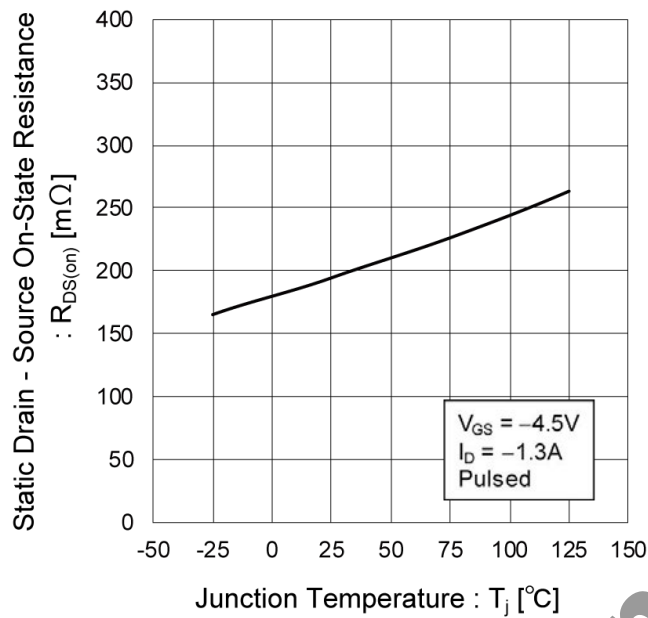
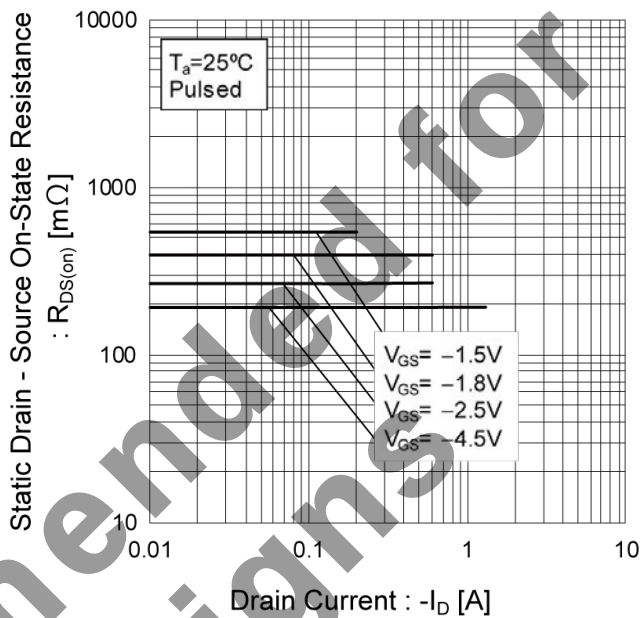


Fig.10 Static Drain - Source On - State Resistance vs. Drain Current (I)



●Electrical characteristic curves <MOSFET>

Fig.11 Static Drain - Source On - State Resistance vs. Drain Current (II)

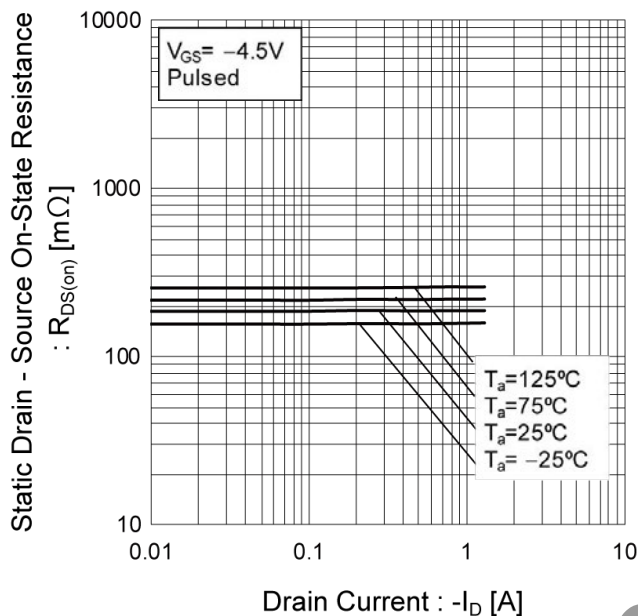


Fig.12 Static Drain - Source On - State Resistance vs. Drain Current (III)

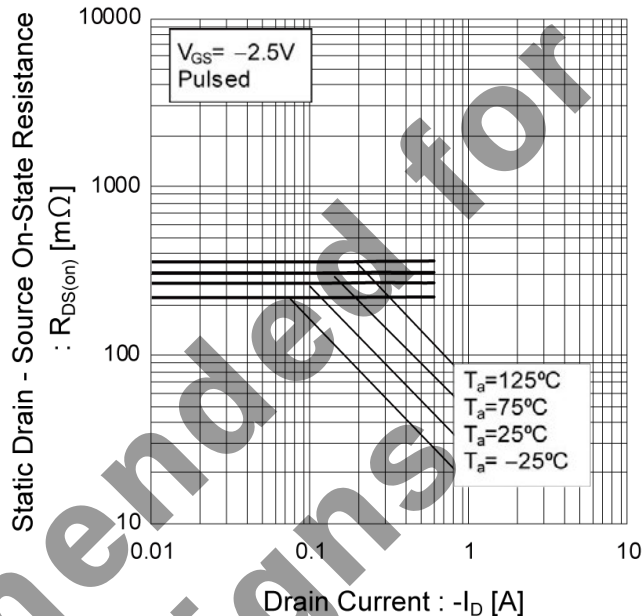


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current (IV)

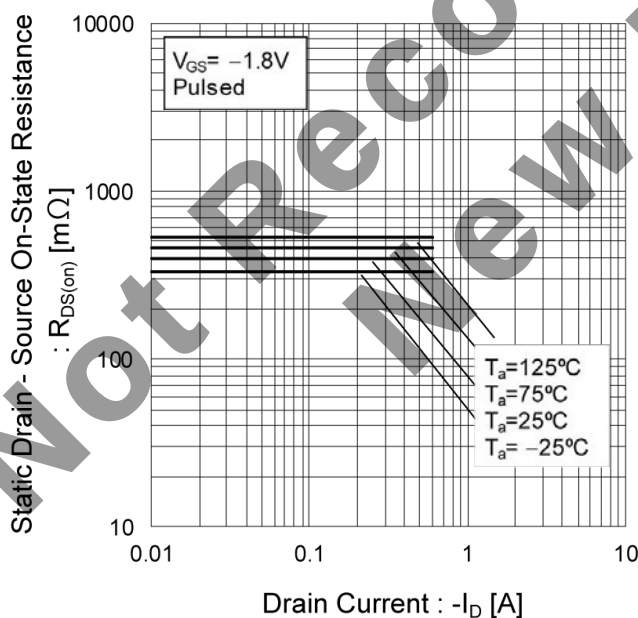
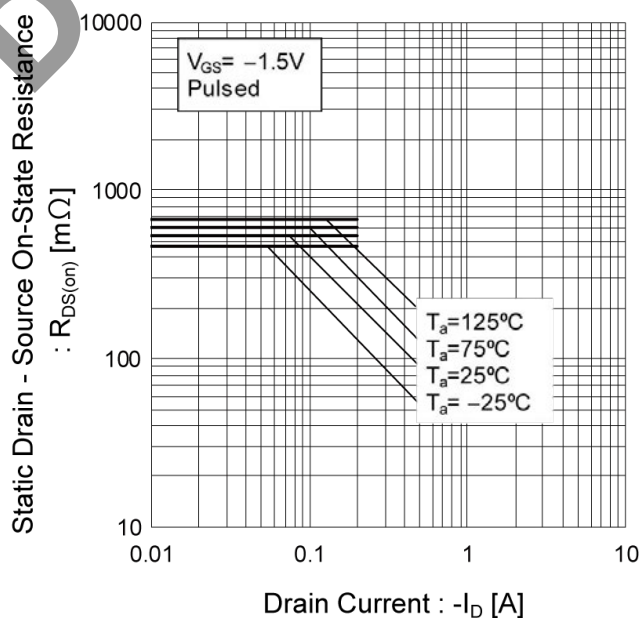


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (V)



●Electrical characteristic curves <MOSFET>

Fig.15 Typical Capacitance vs.
Drain - Source Voltage

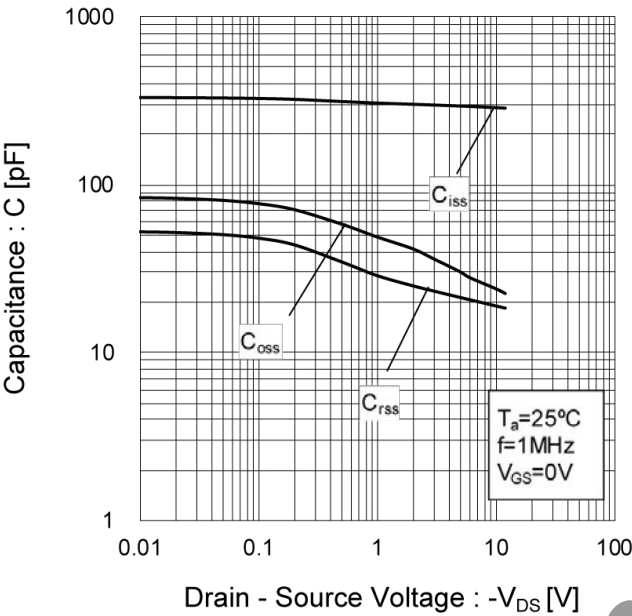


Fig.16 Switching Characteristics

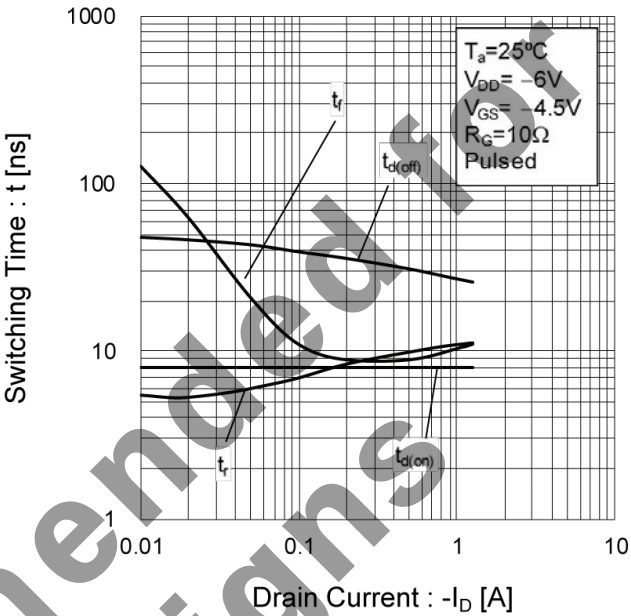


Fig.17 Dynamic Input Characteristics

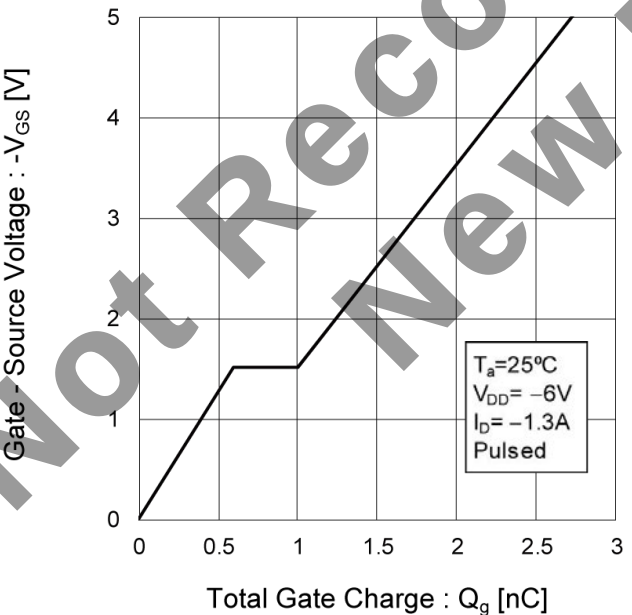
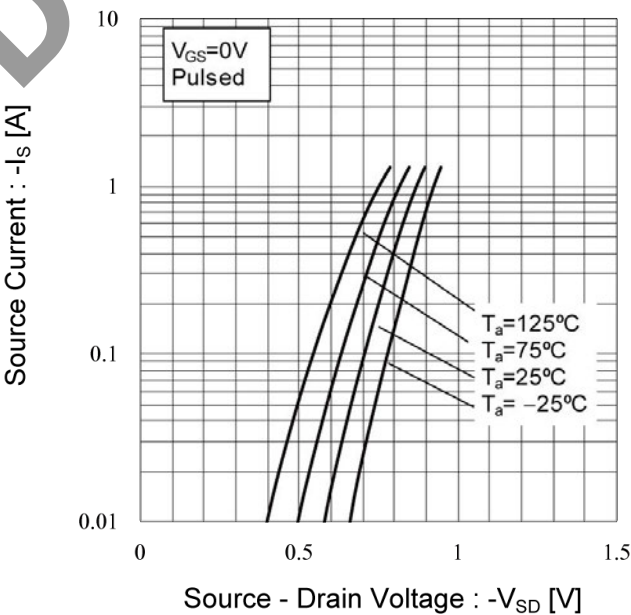


Fig.18 Source Current vs.
Source Drain Voltage



● Electrical characteristic curves <Di>

Fig.19 Reverse Current vs.
Reverse Voltage

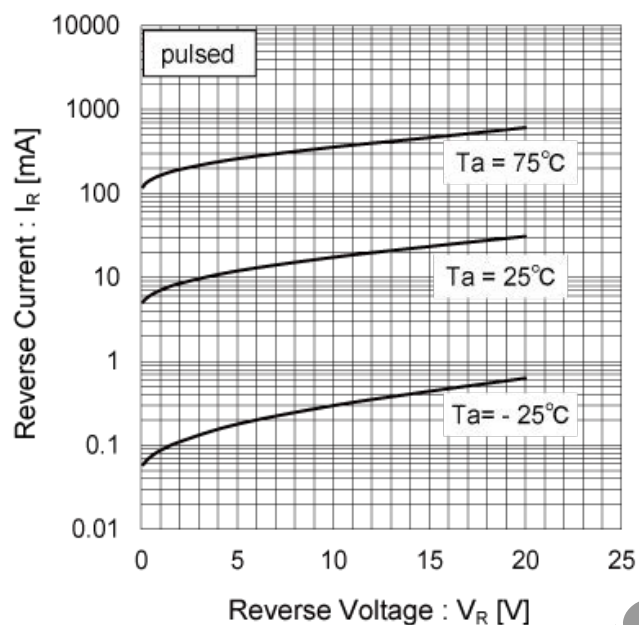
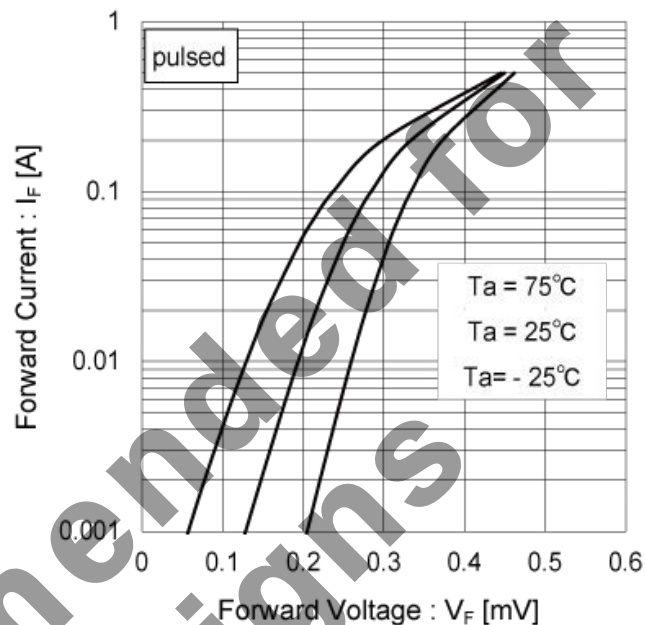


Fig.20 Forward Current vs.
Forward Voltage



● Measurement circuits

Fig. 1-1 SWITCHING TIME MEASUREMENT CIRCUIT

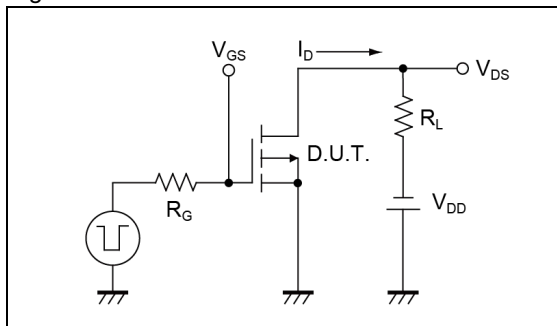


Fig. 1-2 SWITCHING WAVEFORMS

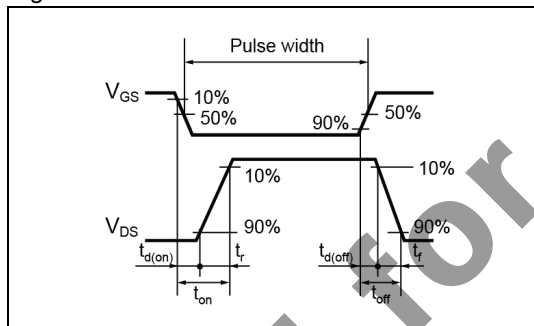


Fig. 2-1 GATE CHARGE MEASUREMENT CIRCUIT

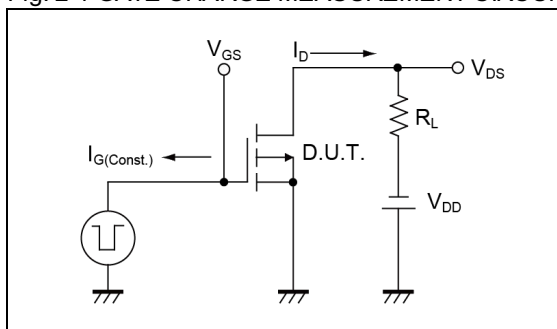
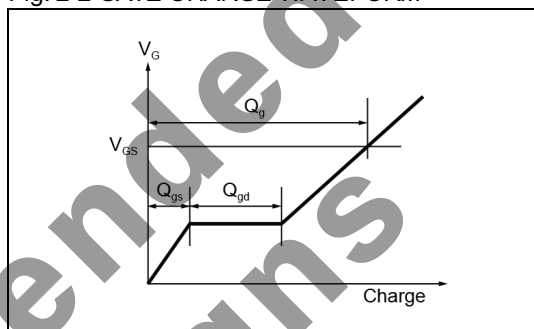


Fig. 2-2 GATE CHARGE WAVEFORM

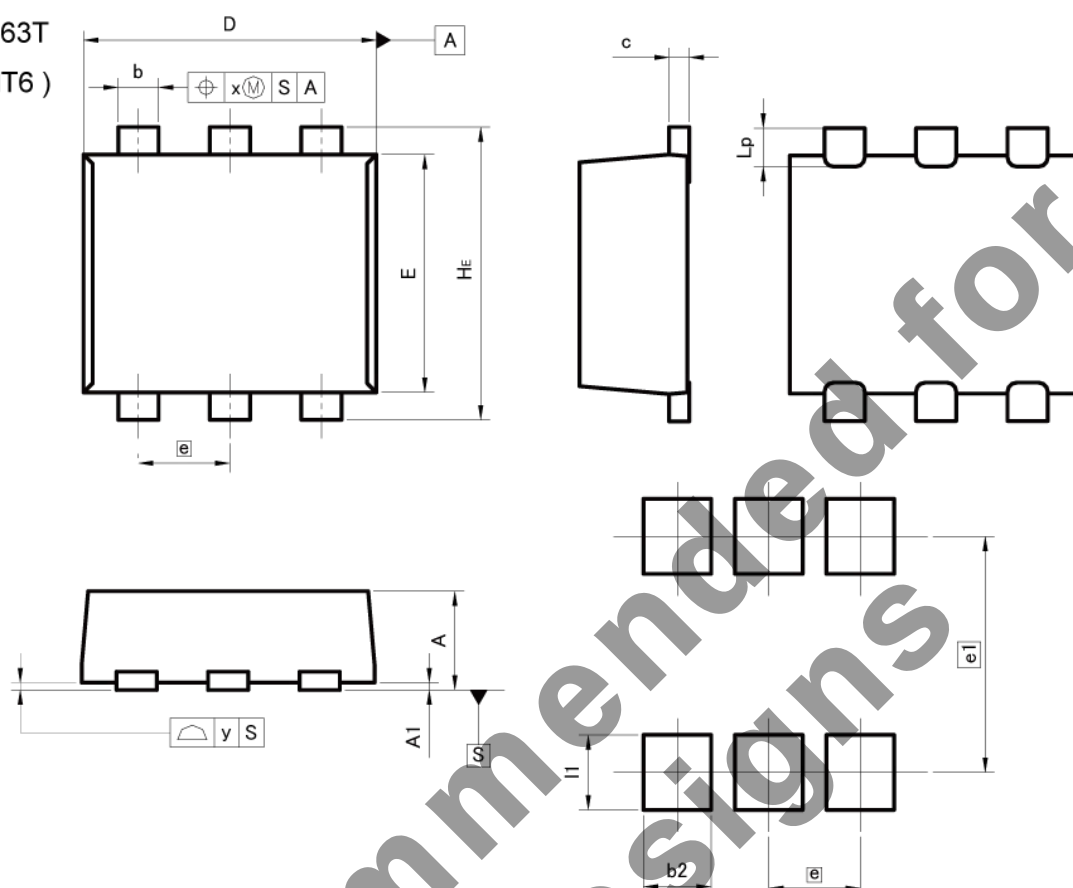


● Notice

1. SBD has a large reverse leak current compared to other type of diode. Therefore, it would raise a junction temperature, and increase a reverse power loss. Further rise of inside temperature would cause a thermal runaway. This built-in SBD has low V_F characteristics and therefore, higher leak current. Please consider enough the surrounding temperature, generating heat of MOSFET and the reverse current.
2. This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

●Dimensions

SOT-563T
(WEMT6)



Pattern of terminal position areas
[Not a pattern of soldering pads]

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.55	0.65	0.022	0.026
A1	0.00	0.05	0.000	0.002
b	0.17	0.27	0.007	0.011
c	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	1.20	1.40	0.047	0.055
e	0.50		0.020	
HE	1.50	1.70	0.059	0.067
Lp	0.11	0.31	0.004	0.012
x	—	0.10	—	0.004
y	—	0.10	—	0.004

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	—	0.37	—	0.015
e1	1.29		0.051	
l1	—	0.41	—	0.016

Dimension in mm/inches

Notice

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JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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 - Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - Sealing or coating our Products with resin or other coating materials
 - Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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помещение 100-Н Офис 331