

Product Summary

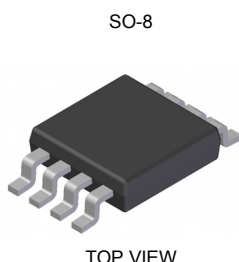
$V_{(BR)DSS}$	$R_{DS(on)}$ max	I_D max $T_A = +25^\circ\text{C}$
-20V	13m Ω @ $V_{GS} = -10\text{V}$	-9.3A
	16m Ω @ $V_{GS} = -4.5\text{V}$	-8.3A
	22m Ω @ $V_{GS} = -2.5\text{V}$	-7.2A

Description

This MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters

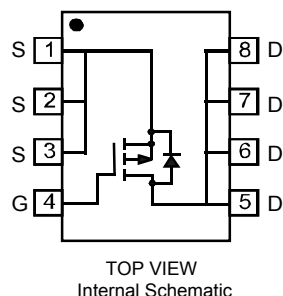


Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Available (Note 4)**

Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Weight: 0.074g (approximate)

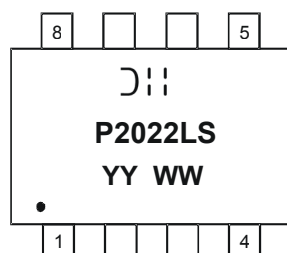


Ordering Information (Note 5)

Part Number	Compliance	Case	Packaging
DMP2022LSSQ-13	Automotive	SO-8	2,500/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_grade_definitions/.
 5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



D = Manufacturer's Marking
 P2022LS = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Year (ex: 13 = 2013)
 WW = Week (01 - 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage			V _{GSS}	±12	V
Drain Current (Note 6)	Steady State	T _A = +25°C T _A = +70°C	I _D	-9.3 -7.4	A
Pulsed Drain Current (Note 7)			I _{DM}	-35	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P _D	1.6	W
Thermal Resistance, Junction to Ambient	R _{θJA}	74	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-20	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±12V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	-0.6	-0.77	-1.1	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	8	13	mΩ	V _{GS} = -10V, I _D = -10A
		—	11	16		V _{GS} = -4.5V, I _D = -9A
		—	17	22		V _{GS} = -2.5V, I _D = -8A
		—	—	—		V _{GS} = -10V, I _D = -10A
Forward Transconductance	g _{fs}	—	28	—	S	V _{DS} = -10V, I _D = -10A
Diode Forward Voltage (Note 8)	V _{SD}	-0.5	-0.68	-1.2	V	V _{GS} = 0V, I _S = -3A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	2575	—	pF	V _{DS} = -10V, V _{GS} = 0V f = 1MHz
Output Capacitance	C _{oss}	—	326	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	261	—	pF	
Gate Resistance	R _G	—	10.9	—	Ω	V _{GS} = 0V V _{DS} = 0V, f = 1MHz
SWITCHING CHARACTERISTICS (Note 9)						
Total Gate Charge	Q _g	—	28.1 60.2	—	nC	V _{DS} = -10V, V _{GS} = -4.5V, I _D = -10A
Gate-Source Charge	Q _{gs}	—	5.9	—		V _{DS} = -10V, V _{GS} = -10V, I _D = -10A
Gate-Drain Charge	Q _{gd}	—	7.4	—		V _{DS} = -10V, V _{GS} = -10V, I _D = -10A
Turn-On Delay Time	t _{D(on)}	—	4.5	15	ns	V _{DD} = -15V, I _D = -1A, V _{GS} = -10V, R _{GEN} = 6Ω
Turn-On Rise Time	t _r	—	3.3	20		
Turn-Off Delay Time	t _{D(off)}	—	197	216		
Turn-Off Fall Time	t _f	—	60.5	153		

- Notes:
- Device mounted on 2 oz. Copper pads on FR-4 PCB.
 - Pulse width ≤10μs, Duty Cycle ≤1%.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

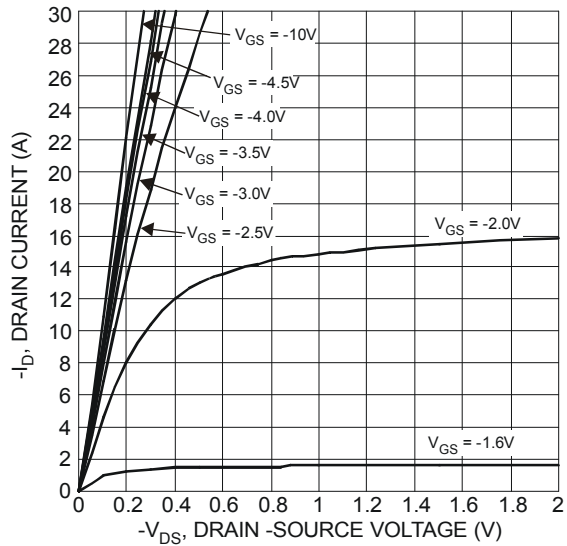


Figure 1 Typical Output Characteristics

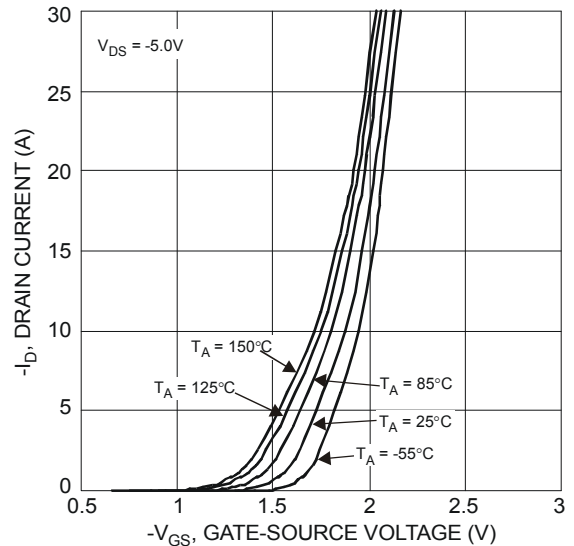


Figure 2 Typical Transfer Characteristics

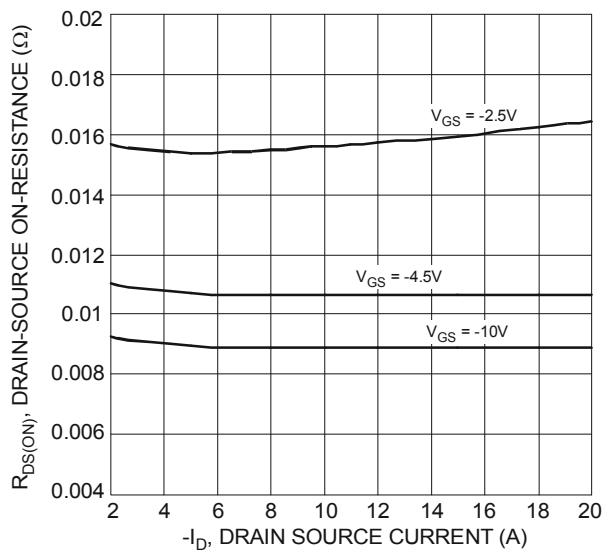


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

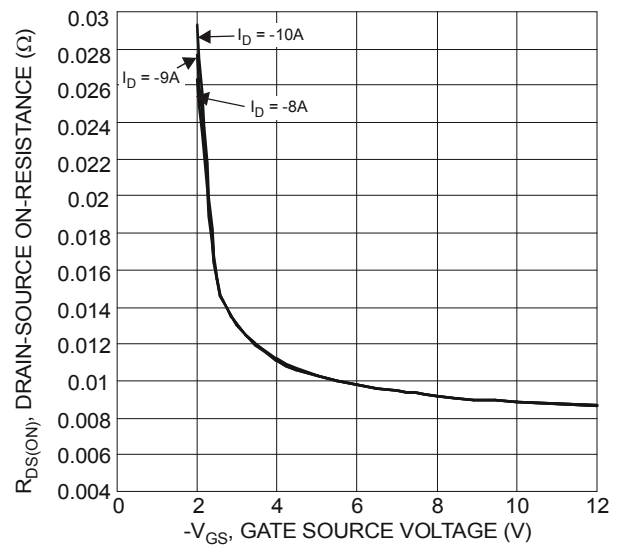


Figure 4 Typical Transfer Characteristics

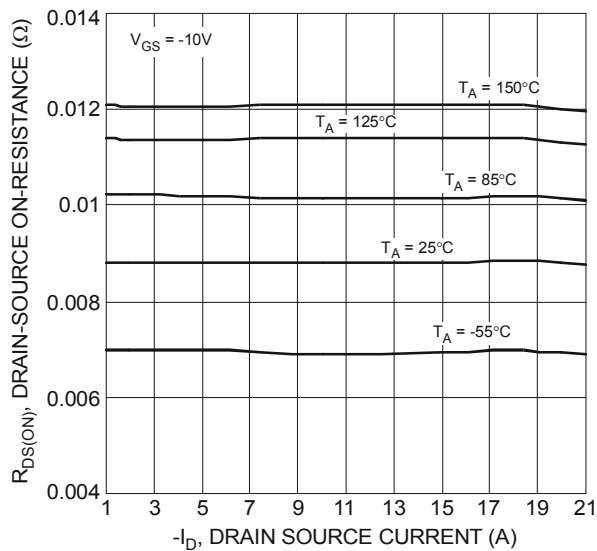


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

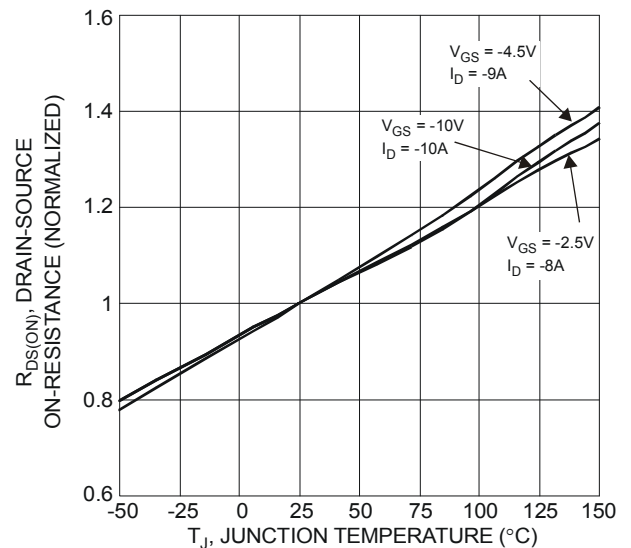
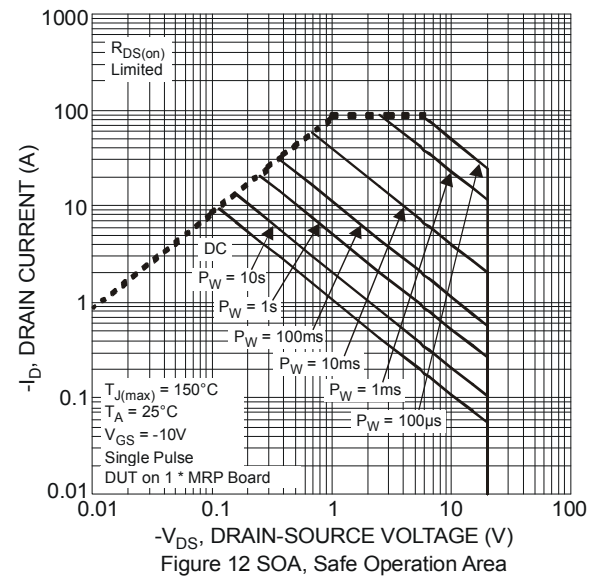
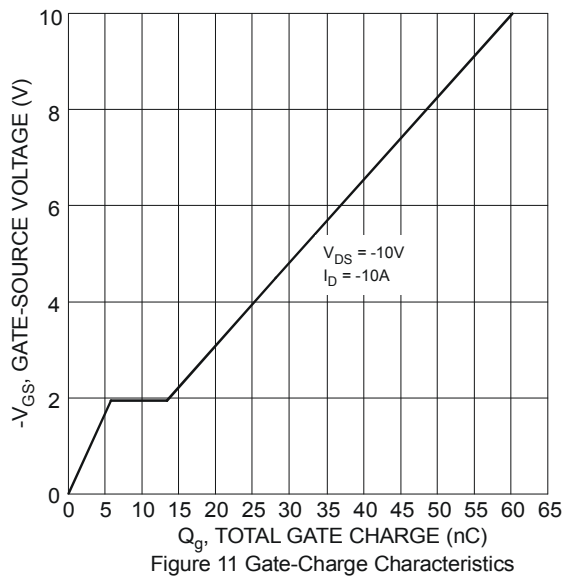
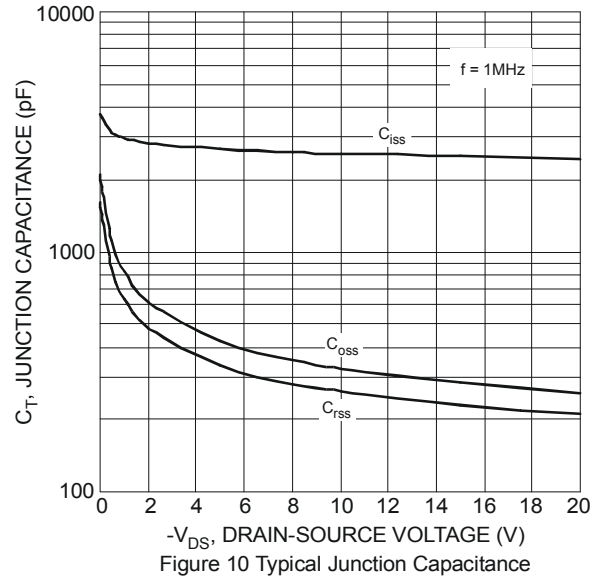
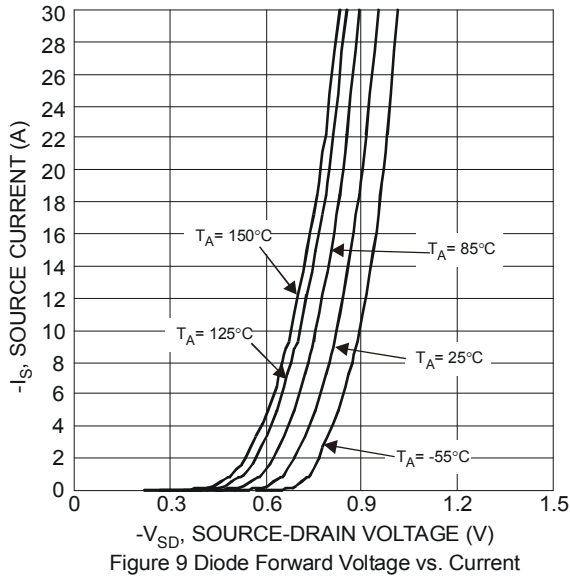
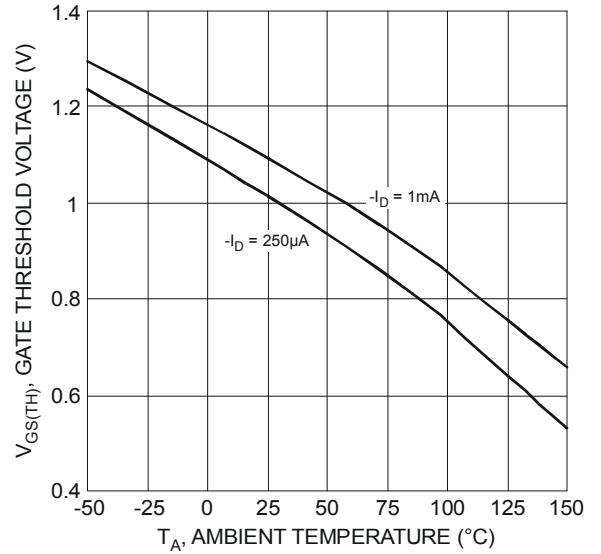
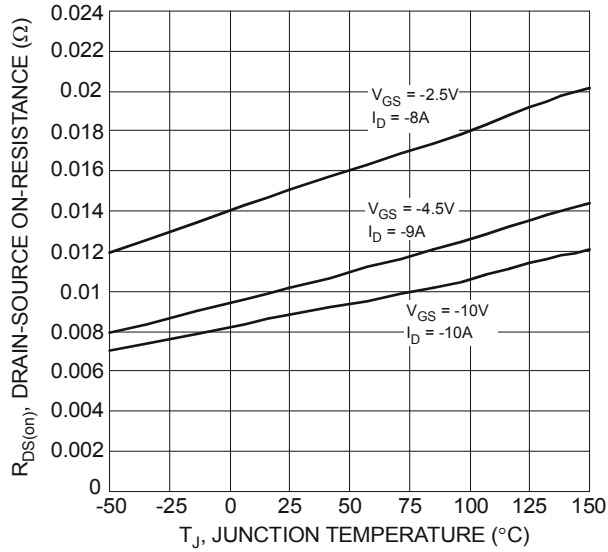
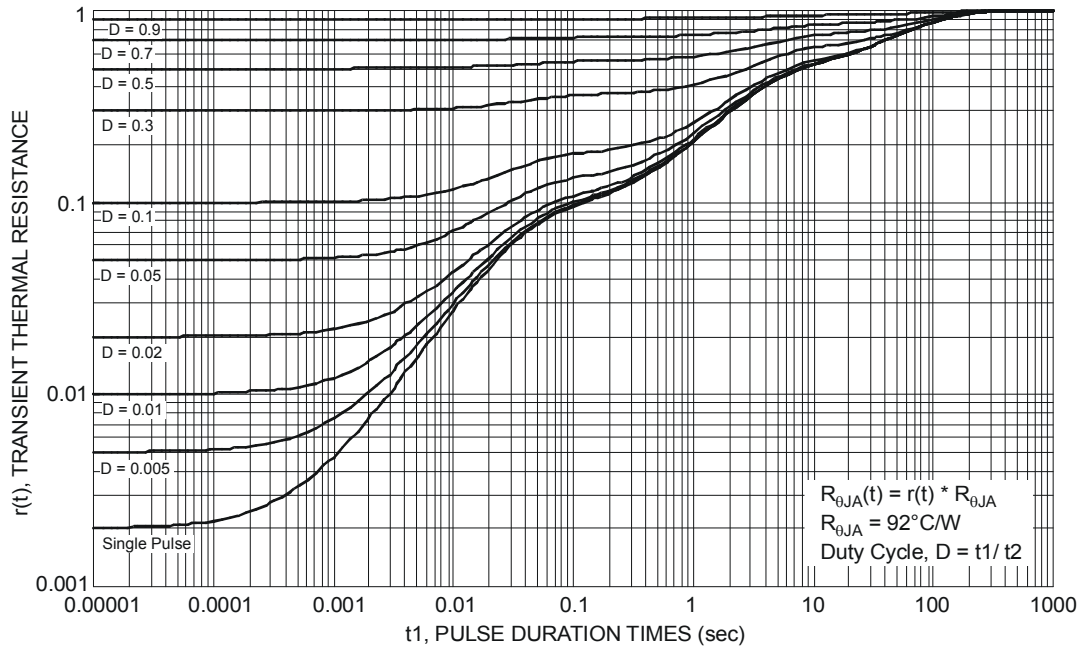


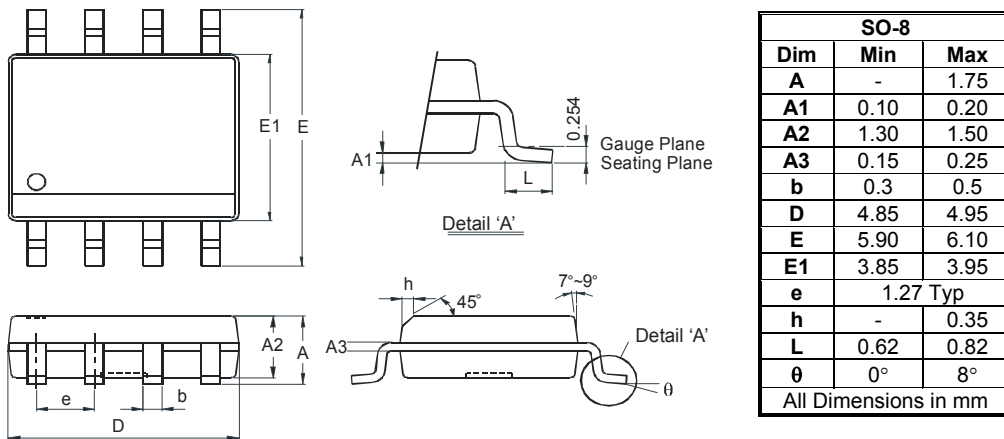
Figure 6 On-Resistance Variation with Temperature





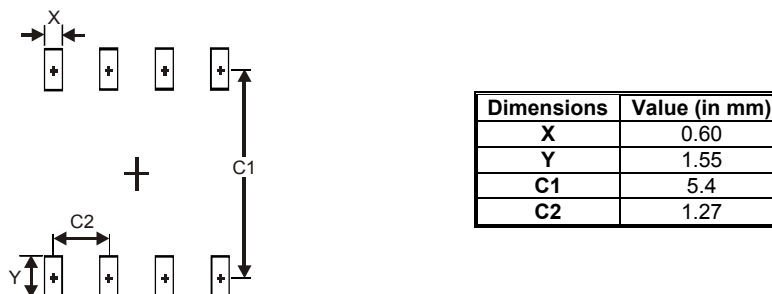
Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



Suggested Pad Layout

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