

A-TOPS ELECTRONICS COMPANY

PRODUCT SPECIFICATIONS

For Customer: _____

☐ : APPROVAL FOR SPECIFICATION

Customer Model No. _____

☐ : APPROVAL FOR SAMPLE

Module No.: AT043B35-15I-10

Date : 2012.5.30

Version : 1.5

Table of Contents

No.	Item	Page
1	Cover Sheet(Table of Contents)	
2	Revision Record	
3	General Specifications	
4	Outline Drawing	
5	Absolute Maximum Ratings	
6	Electrical Specifications and Instruction Code	
7	Optical Characteristics	
8	Reliability Test Items and Criteria	
9	Quality Level	
10	Packing Reliability	

For Customer's Acceptance:

Approved By	Comment

PREPARED	CHECKED	VERIFIED BY QA DEPT	VERIFIED BY R&D DEPT

A-TOPS ELECTRONICS COMPANY

2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2012.3.5	V1.0		The first release	Qiu
2012.5.22	V1.2		Adding size remark	Qiu
2012.5.23	V1.3		Power supply changing to 3.0V	Qiu
2012.5.29	V1.4		Clarification on backlight details.	Qiu
2012.5.30	V1.5		Modify 5.1 and 6.1 parameters.	Qiu

A-TOPS ELECTRONICS COMPANY

3.

AT043B35-15I-10 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The 4.3" display area contains 480 x 272 pixels and can display up to 262K colors. This product accords with RoHS

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	262k		1
Viewing Direction	6	O'Clock	
Operating temperature	-20~+70	°C	
Storage temperature	-30~+80	°C	
Module size	105.4(W)×67.1(H)×4.2(D)	mm	2
Active Area(W×H)	95.04X53.86	mm	
Number of Dots	480×272	dots	
Controller	HX8257A	-	
Power Supply Voltage	3.0	V	
Outline Dimensions	Refer to outline drawing	-	
Backlight	10-LEDs (white)	pcs	
Weight	30	g	
Interface	F.P.C 0.5mm	-	
Data Transfer	RGB888	-	
Polarizer Mode	Transmissive/negative	-	

environmental criterion.

Note 1: Color tune is slightly changed by temperature and driving voltage.

Note 2: Without FPC and Solder.

A-TOPS ELECTRONICS COMPANY

5. Absolute Maximum Ratings(Ta=25°C)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25°C)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V _{CC}	-0.3	3.5	V	1, 2
Logic Signal Input /Output Voltage	V _{I/OVCC}	-0.3	V _{CC} +0.5	V	
Power Supply Voltage for LCD Module	V _{op}	0	18	V	
Current of LED Backlight	I _{LED}	0	60	mA	

Notes:

1. If the module exceeds above absolute maximum ratings, it may become permanently damaged.

Using the module out of the following electrical characteristic, the module will be malfunction and/or cause poor reliability.

2. V_{CC} > V_{SS} must be maintained.
3. Please be sure users are grounded when handling LCD Module.

5.2 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30°C	80°C	-20°C	70°C	1,2
Humidity	0% RH	90% RH	0% RH	90% RH	--

1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature.

The phenomenon is reversible.

Specifications and Instruction Code

6. Driver IC Electrical Specifications and Instruction Code

6.1 Driver IC Electrical characteristics(V_{ss}=0V,T_a=25°C)

Parameter		Symbol	Condition	Min	Typ	Max	Unit	Note
Power supply		V _{CC}	T _a =25	2.6	3.0	3.5	V	
Input voltage	‘H’	V _{IH}	V _{CC} =3.0V	0.8V _{CC}	-	V _{CC}	V	
	‘L’	V _{IL}	V _{CC} =3.0V	0	-	0.2V _{CC}	V	
Current Consumption		I _{CC1}	Normal mode	-	15	30	mA	2
		I _{CC2}	Sleep mode	-	0.03	0.09	mA	2

Note:

1:When an optimum contrast is obtained in transmissive mode.

2: Tested in 1×1 chessboard pattern.

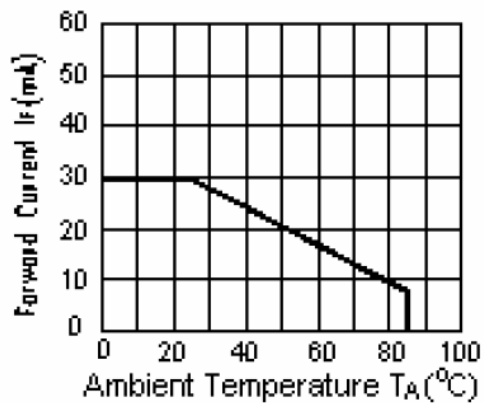
A-TOPS ELECTRONICS COMPANY

6.2 LED backlight specification(VSS=0V ,Ta=25°C)

Item		Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage		-	-	-	16.2	-	V	1
Forward current	Normal	I_{pn}	10-chip	-	40	-	mA	2
	Dimming	I_{pd}		-	-	-		

Note:

1. Supply voltage is voltage dropout on backlight module.
2. The backlight driver in constant current is recommended.
3. Normal backlight power consumption is 0.65W.



CIRCUIT DIAGRAM

I_{LED} VS TEMP

A-TOPS ELECTRONICS COMPANY

6.3 Interface signals

Pin No.	Symbol	I/O	Function
1	VLED-	I	LED back light(Cathode)
2	VLED+	I	LED back light(Anode)
3	GND		GND
4	VDD	I	Power supply
5-12	R0~R7	I	Red data bus
13-20	G0~G7	I	Green data bus
21-28	B0~B7	I	Blue data bus
29	GND		GND
30	PCLK	I	Data clock
31	DISP	I	Standby mode select pin
32	HSYNC	I	Line SYNC signal
33	VSYNC	I	Frame SYNC signal
34	DE	I	Data enable pin
35	NC		
36	GND		GND
37	X1	O	Touch Panel Control pin
38	Y1	O	
39	X2	O	
40	Y2	O	

A-TOPS ELECTRONICS COMPANY

7. Optical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp	$\theta=0^\circ$	450	500	-	Cd/m ²	1
Uniformity	Δ Bp	$\Phi=0^\circ$	70	80	-	%	1,2
Viewing Angle	3:00	$Cr \geq 10$	-	65	-	Deg	3
	6:00		-	55	-		
	9:00		-	65	-		
	12:00		-	45	-		
Contrast Ratio	Cr	$\theta=0^\circ$ $\Phi=0^\circ$	200	250	-	-	4
Response Time	T _r		-	16	-	ms	5
	T _f		-	12	-	ms	
Color of CIE Coordinate	W	x	0.23	0.28	0.33	-	1,6
		y	0.28	0.33	0.38	-	
	R	x	0.46-	0.51	0.56	-	
		y	0.29	0.34	0.39	-	
	G	x	0.26	0.31	0.36	-	
		y	0.51	0.56	0.61	-	
	B	x	0.10	0.15	0.20	-	
		y	0.09	0.14	0.19	-	
NTSC Ratio	S		45	60	-	%	

Note: The parameter is slightly changed by temperature, driving voltage and material

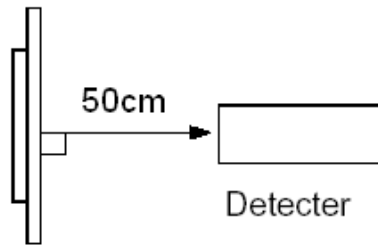
Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white.
The brightness is the average value of 9 measured spots. Measurement equipment PR-705 (Φ8mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25°C.
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

A-TOPS ELECTRONICS COMPANY

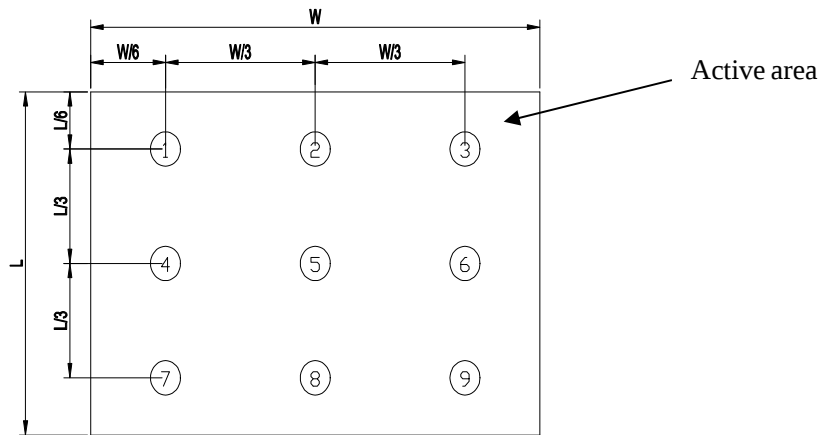


Note 2: The luminance uniformity is calculated by using following formula.

$$\Delta Bp = Bp (\text{Min.}) / Bp (\text{Max.}) \times 100 (\%)$$

Bp (Max.) = Maximum brightness in 9 measured spots

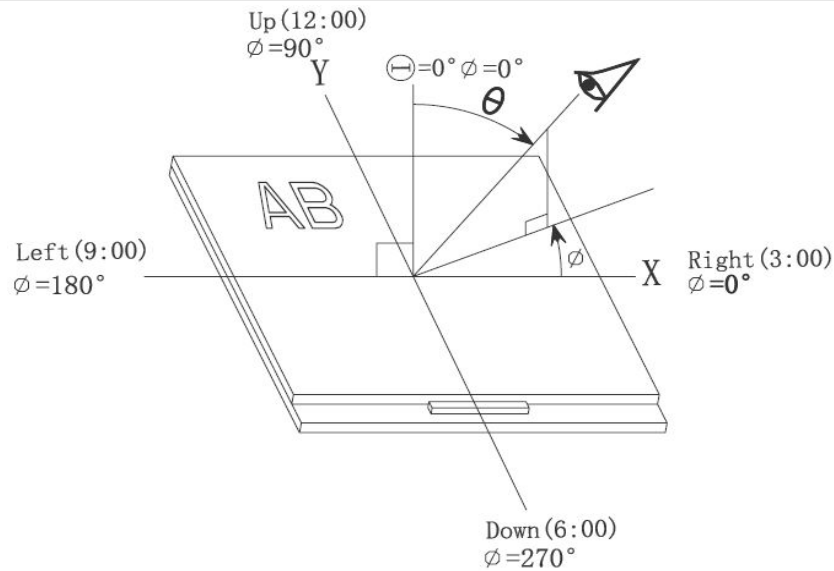
Bp (Min.) = Minimum brightness in 9 measured spots.



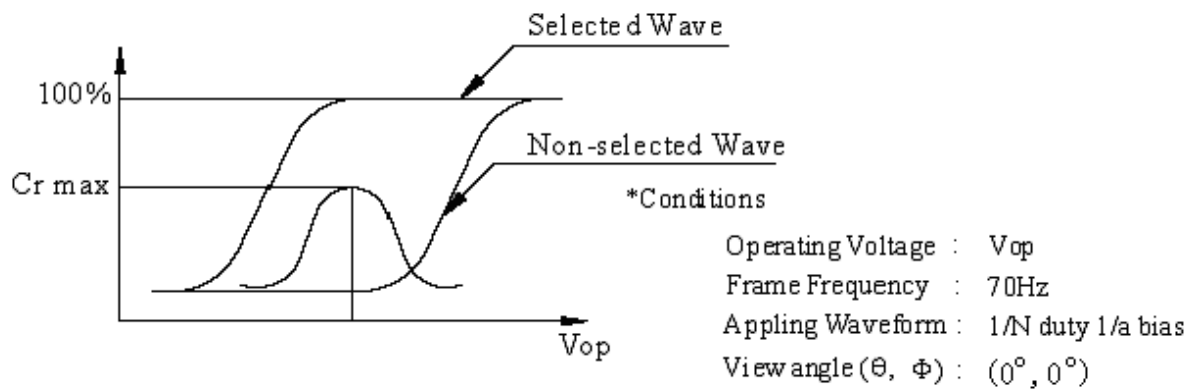
Note 3: The definition of viewing angle:

Refer to the graph below marked by θ and ϕ

A-TOPS ELECTRONICS COMPANY



Note 4: Definition of contrast ratio.(Test LCD using DMS501)

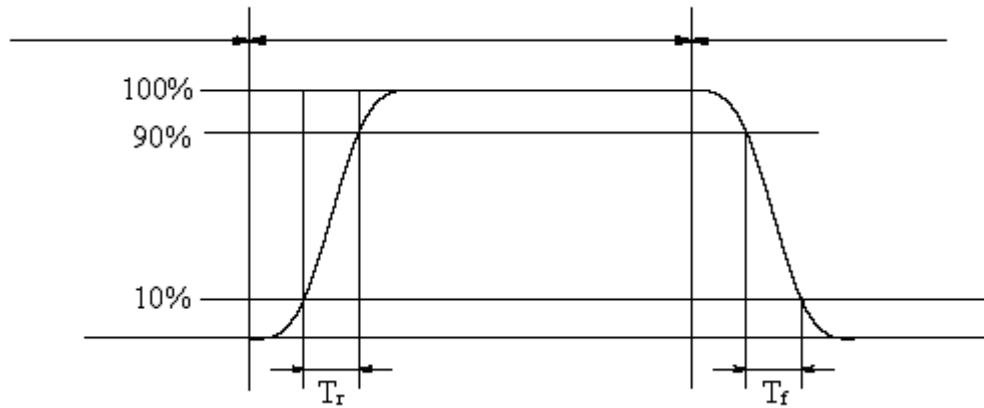


$$\text{Contrast ratio}(Cr) = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$

Note 5: Definition of Response time. (Test LCD using DMS501):

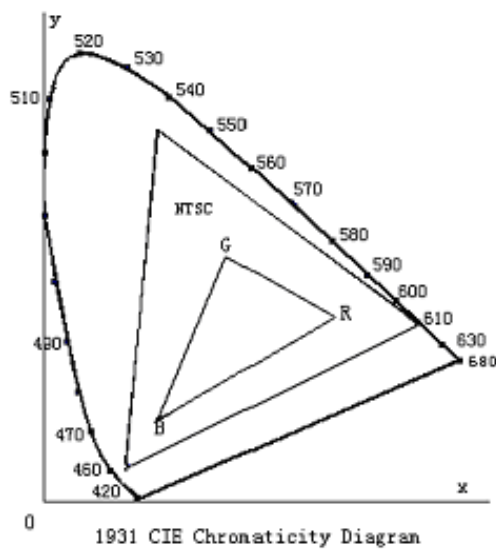
The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.

A-TOPS ELECTRONICS COMPANY



The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

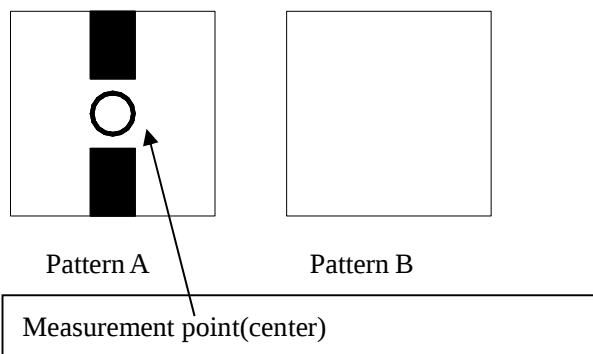


Color gamut:

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 7: Definition of cross talk.

$$\text{Cross talk ratio(\%)} = \frac{|\text{pattern A Brightness} - \text{pattern B Brightness}|}{\text{pattern A Brightness}} \times 100$$



Electric volume value=3F+/-3Hex

A-TOPS ELECTRONICS COMPANY

8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
1	High Temperature Storage	80°C±2°C 96H Restore 2H at 25°C Power off	1. After testing, cosmetic and electrical defects should not happen. 2. Total current consumption should not be more than twice of initial value.
2	Low Temperature Storage	-30°C±2°C 96H Restore 2H at 25°C Power off	
3	High Temperature Operation	70°C±2°C 96H Restore 2H at 25°C Power on	
4	Low Temperature Operation	-20°C±2°C 96H Restore 4H at 25°C Power on	
5	High Temperature/Humidity Operation	60°C±2°C 90%RH 96H Power on	
6	Temperature Cycle	-30°C —————> 80°C 30min 5min 30min after 5 cycle, Restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s², 120min	Not allowed cosmetic and electrical defects.
8	Shock Test	Half- sine wave, 300m/s², 11ms	

Note: Operation: Supply 3.0V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

9 Quality level

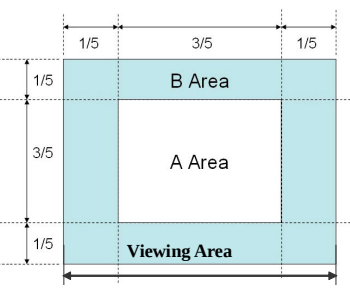
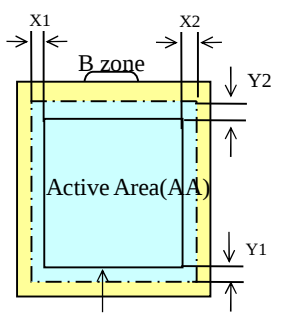
9.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially degrade usability for product applications, including all functional defects(such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

A-TOPS ELECTRONICS COMPANY

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

9.2 Definition of inspection range

This dot defect of TFT LCD which is for AT043B35-15I-10 V1.4 dividing three areas to make a judgment (according to figure 1). A area : center of viewing area B area : periphery of viewing area C area : Outside viewing area For other defects, dividing two areas to make a judgment (according figure 2). A zone : Inside Viewing area B zone : Outside Viewing area X1(A.A~V.A): 2mm X2(A.A~V.A): 2mm Y1(A.A~V.A): 2mm Y2(A.A~V.A): 2mm	 Figure 1  Figure 2
---	--

9.3 Inspection items and general notes

General notes	①Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and A-TOPS. ②Viewing area should be the area which A-TOPS guarantees. ③Limit sample should be prior to this Inspection standard. ④Viewing judgment should be under static pattern. ⑤Inspection conditions Inspection distance: 250 mm (from the sample) Temperature : 25±5 °C Inspection angle : 45 degrees in 12 o'clock direction (all defects in viewing area should be inspected from this direction)	
Inspection items	Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble	The color of a small area is different from the remainder. The phenomenon doesn't change with voltage
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage
	Polarizer defect	Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass
	Dot defect (TFT LCD)	The pixel appears bright or dark abnormally when display
	Functional defect	No display, Abnormal display, Open or missing segment, Short circuit, False viewing direction
	Glass defect	Glass crack, Shaved corner of glass, Surplus glass
	PCB defect	Components assembly defect

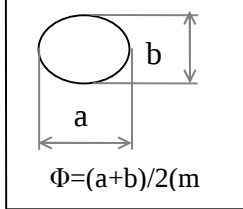
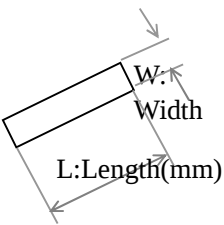
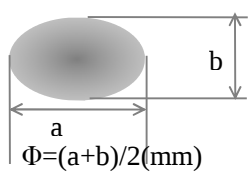
9.4 Outgoing Inspection level

A-TOPS ELECTRONICS COMPANY

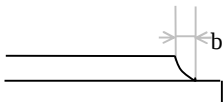
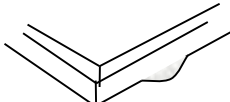
Outgoing Inspection standard	Inspection conditions	Inspection				
		Min.	Max.	Unit	IL	AQL
Major Defects	See 8.3 general notes	See 8.5			II	0.065
Minor Defects	See 8.3 general notes	See 8.5			II	0.065

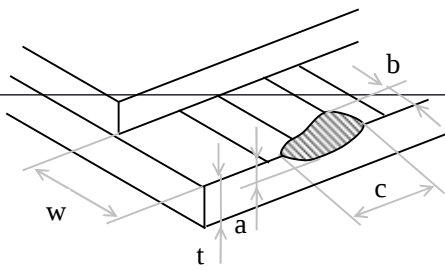
Note: Sampling standard conforms to GB2828

9.5 Inspection Items and Criteria

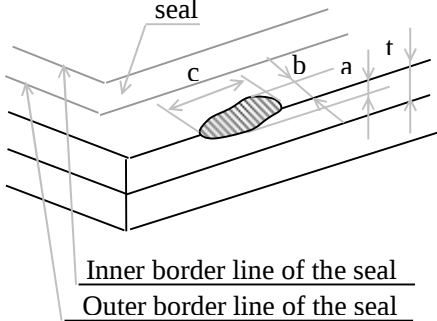
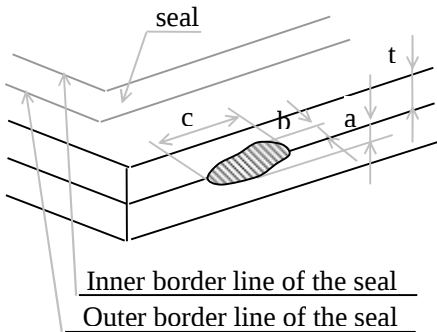
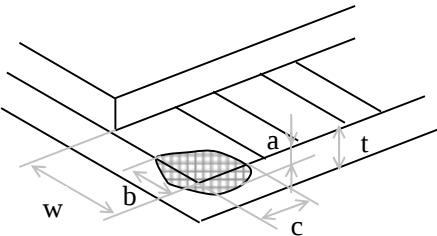
Inspection items			Judgment standard			
			Category		Acceptable number	
					A zone	B zone
1	Black spot, White spot, Pinhole, Foreign Particle, Particle in or on glass, Scratch on glass		A	$\Phi \leq 0.15$	Neglected	Neglected
			B	$0.15 < \Phi \leq 0.35$	3	
			C	$0.35 < \Phi$	0	
			D	-	-	
			Total defective point(B,C)		3	
2	Black line, White line, and Particle Between Polarizer and glass, Scratch on glass		A	$W \leq 0.05$	Neglected	Neglected
			B	$0.05 < W \leq 0.1$ $L \leq 5.0$	2	
			C	$0.1 < W, 5.0 < L$	0	
			Total defective point(B,C)		2	
3	Bright spot		any size		none	none
4	Contrast variation		A	$\Phi < 0.3$	Neglected	Neglected
			B	$0.3 < \Phi \leq 0.4$	3	
			C	$0.4 < \Phi$	0	
			Total defective point(B,C)		3	
5	Bubble inside cell		any size		none	none

A-TOPS ELECTRONICS COMPANY

6	Polarizer defect (if Polarizer is used)	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Refer to item 1 and item 2.			
		Bubble, dent and convex	A	$\Phi \leq 0.2$	Neglected	Neglected
			B	$0.2 < \Phi \leq 0.3$	3	
			C	$0.5 < \Phi$	0	
7	Surplus glass	Stage surplus glass 	$B \leq 0.4\text{mm}$			
		Surrounding surplus glass 	Should not influence outline dimension and assembling.			
8	Open segment or open common		Not permitted			
9	Short circuit		Not permitted			
10	False viewing direction		Not permitted			
11	Contrast ratio uneven		According to the limit specimen			
12	Crosstalk		According to the limit specimen			
13	Black /White spot(display)		Refer to item 1			
14	Black /White line(display)		Refer to item 2			

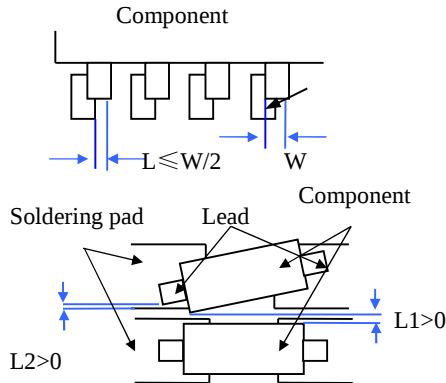
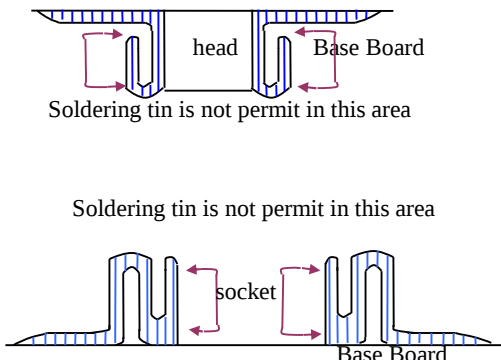
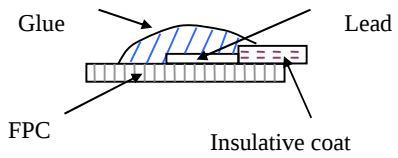
Inspection items			Judgment standard	
			Category(application: B zone)	Acceptable number
15	Glass defect crack	i) The front of lead terminals 	A	$a \leq t, \quad b \leq 1/5W, \quad c \leq 4\text{mm}$
				Max.3 defects allowed

A-TOPS ELECTRONICS COMPANY

			B	Crack at two sides of lead terminals should not cover patterns and alignment mark	
	ii) Surrounding crack-non-contact side			$b < \text{Inner borderline of the seal}$	
	iii) Surrounding crack- contact side			$b < \text{Outer borderline of the seal}$	
	iv) Corner		A	$a \leq t, \quad b \leq 5.0, \quad c \leq 5.0$	
			B	Glass crack should not cover patterns u and alignment mark and patterns.	

Inspection items	Judgment standard
	Category(application: B zone)

A-TOPS ELECTRONICS COMPANY

16	FPC defect	Component soldering: No cold soldering、short、open circuit、burr、tin ball The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1); the sheet component deviation: Pin deviates from the pad and contact with the near components is not permitted (Pic.2)	 Diagram illustrating component soldering defects. The top part shows a component with pins on a pad, with dimensions $L \leq W/2$ and W. The bottom part shows a component with a lead, with dimensions $L1 > 0$ and $L2 > 0$.
		lead defect: The lead lack must be less than 1/3 of its width; The lead burr must be less than 1/3 of the seam; Impurities connect with the near leads is not permitted	
		Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted	 Diagram illustrating connector soldering defects. The top part shows a head and base board with soldering tin not permitted in the area. The bottom part shows a socket and base board with soldering tin not permitted in the area.
		Glue on root of the speaker receiver and motor lead: The insulative coat of the lead must join into the FPC; the protected glue must envelop to the insulative coat.	 Diagram illustrating glue on root of the speaker receiver and motor lead. The diagram shows a lead with an insulative coat, glue, and FPC.

10. Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock

A-TOPS ELECTRONICS COMPANY

by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol

- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

- Water

- Ketone

- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- a. Be sure to ground the body when handling the LCD Modules.

- b. Tools required for assembly, such as soldering irons, must be properly ground.

- c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

- d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

A-TOPS ELECTRONICS COMPANY

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity: $\leq 80\%$

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.



**Стандарт
Электрон
Связь**

Мы молодая и активно развивающаяся компания в области поставок электронных компонентов. Мы поставляем электронные компоненты отечественного и импортного производства напрямую от производителей и с крупнейших складов мира.

Благодаря сотрудничеству с мировыми поставщиками мы осуществляем комплексные и плановые поставки широчайшего спектра электронных компонентов.

Собственная эффективная логистика и склад в обеспечивает надежную поставку продукции в точно указанные сроки по всей России.

Мы осуществляем техническую поддержку нашим клиентам и предпродажную проверку качества продукции. На все поставляемые продукты мы предоставляем гарантию .

Осуществляем поставки продукции под контролем ВП МО РФ на предприятия военно-промышленного комплекса России , а также работаем в рамках 275 ФЗ с открытием отдельных счетов в уполномоченном банке. Система менеджмента качества компании соответствует требованиям ГОСТ ISO 9001.

Минимальные сроки поставки, гибкие цены, неограниченный ассортимент и индивидуальный подход к клиентам являются основой для выстраивания долгосрочного и эффективного сотрудничества с предприятиями радиоэлектронной промышленности, предприятиями ВПК и научно-исследовательскими институтами России.

С нами вы становитесь еще успешнее!

Наши контакты:

Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

Адрес: 198099, Санкт-Петербург,
Промышленная ул, дом № 19, литера Н,
помещение 100-Н Офис 331