

Bipolar Transistors Silicon PNP Epitaxial Type

TTA003

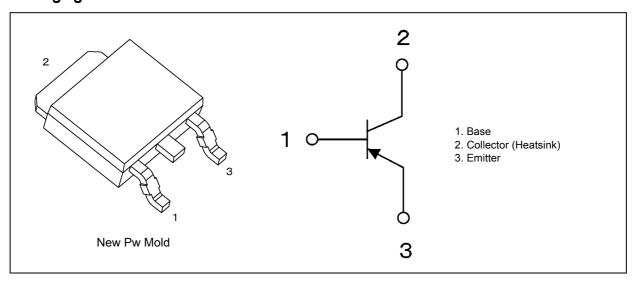
1. Applications

- · Power Amplifiers
- · Power Switching

2. Features

- (1) Low collector saturation voltage: $V_{CE(sat)}$ = -0.5 V (max) (I_C = -1 A, I_B = -100 mA)
- (2) High-speed switching: $t_{stg} = 300 \text{ ns (typ.)}$

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Unless otherwise specified, Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Collector-base voltage		V _{CBO}	-80	V
Collector-emitter voltage		V _{CEO}	-80]
Emitter-base voltage		V _{EBO}	-7	
Collector current (DC)	(Note 1)	I _C	-3	Α
Collector current (pulsed)	(Note 1)	I _{CP}	-5	
Base current		l _Β	-1.5	
Collector power dissipation (T _c = 25°C)		P _C	10	W
Junction temperature		Tj	150	°C
Storage temperature	_	T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Ensure that the junction temperature does not exceed 150°C.



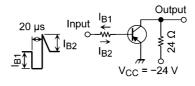
5. Electrical Characteristics

5.1. Static Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	V _{CB} = -80 V, I _E = 0 A	_	_	-100	nA
Emitter cut-off current	I _{EBO}	V _{EB} = -7 V, I _C = 0 A	_	_	-100	
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C = -10 mA, I _B = 0 A	-80		_	٧
DC current gain	h _{FE(1)}	$V_{CE} = -2 \text{ V}, I_{C} = -1 \text{ mA}$	80		_	_
	h _{FE(2)}	$V_{CE} = -2 \text{ V}, I_{C} = -0.5 \text{ A}$	100	_	200	
	h _{FE(3)}	V _{CE} = -2 V, I _C = -1 A	60	_	_	
Collector-emitter saturation voltage	V _{CE(sat)(1)}	$I_C = -0.5 \text{ A}, I_B = -50 \text{ mA}$	_		-0.3	V
	V _{CE(sat)(2)}	I _C = -1 A, I _B = -100 mA	_	_	-0.5	
Base-emitter saturation voltage	V _{BE(sat)}	I _C = -1 A, I _B = -100 mA	_	_	-1.5	

5.2. Dynamic Characteristics (Unless otherwise specified, Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Transition frequency	f _T	$V_{CE} = -2 \text{ V}, I_{C} = -0.5 \text{ A}$	_	100		MHz
Collector output capacitance	C _{ob}	V _{CB} = -10 V, I _E = 0 A, f = 1 MHz	_	25		pF
Switching time (rise time)	t _r	See Figure 5.2.1.	_	30	_	ns
Switching time (storage time)	t _{stg}		_	300	_	
Switching time (fall time)	t _f		_	40	1	



 I_{B1} = 100 mA, I_{B2} = 100 mA Duty cycle ≤ 1%

Fig. 5.2.1 Switching Time Test Circuit

6. Marking (Note)

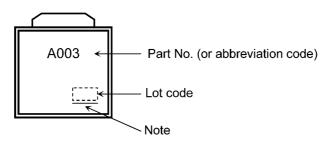


Fig. 6.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels.

[[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

7. Characteristics Curves (Note)

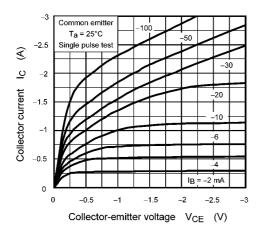
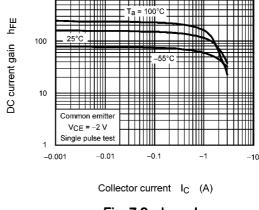


Fig. 7.1 I_C - V_{CE}



1000

Fig. 7.2 h_{FE} - l_C

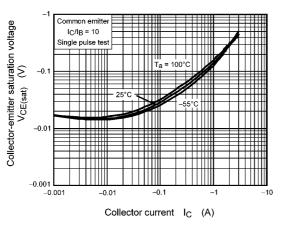


Fig. 7.3 V_{CE(sat)} - I_C

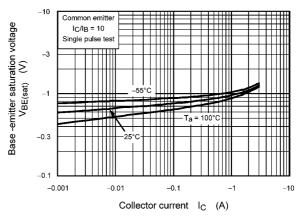


Fig. 7.4 V_{BE(sat)} - I_C

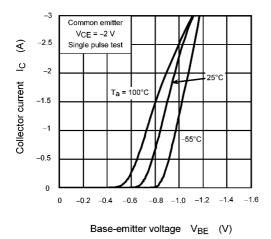


Fig. 7.5 I_C - V_{BE}

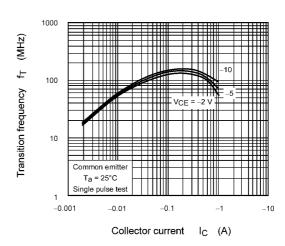


Fig. 7.6 f_T - I_C

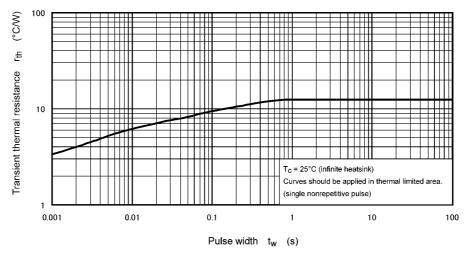


Fig. 7.7 r_{th} - t_w (Guaranteed Maximum)

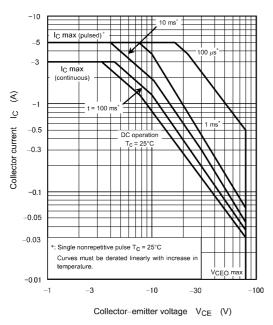


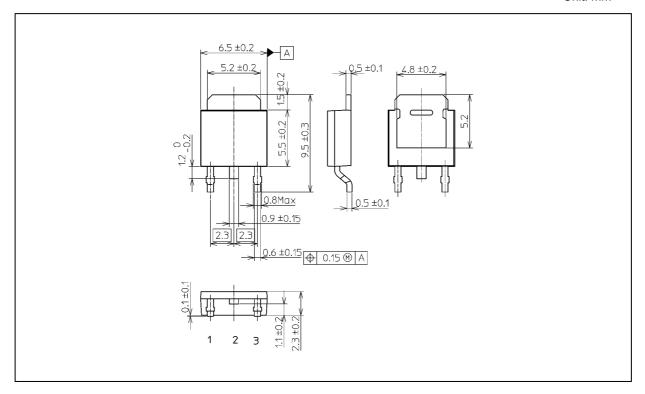
Fig. 7.8 Safe Operating Area (Guaranteed Maximum)

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.



Package Dimensions

Unit: mm



Weight: 0.36 g (typ.)

Package Name(s)	
TOSHIBA: 2-7J1S	
Nickname: New Pw Mold	



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