



L7591 Subscriber Line Interface Circuit Protector

Features

- Shunts lightning pulses to ground
 - Positive or negative 30 A, 10 x 1000 μ s
 - Positive or negative 40 A, 5 x 320 μ s
 - Positive or negative 80 A, 2 x 10 μ s
- Power-cross protection
 - 3.5 A_{PEAK}, 50 Hz to 60 Hz, 1 s
 - 5.0 A_{PEAK}, 50 Hz to 60 Hz, 10 ms
- Gate trigger current, 15 mA max
- Up to -80 V capability
- Holding current, 150 mA min

Pin Information

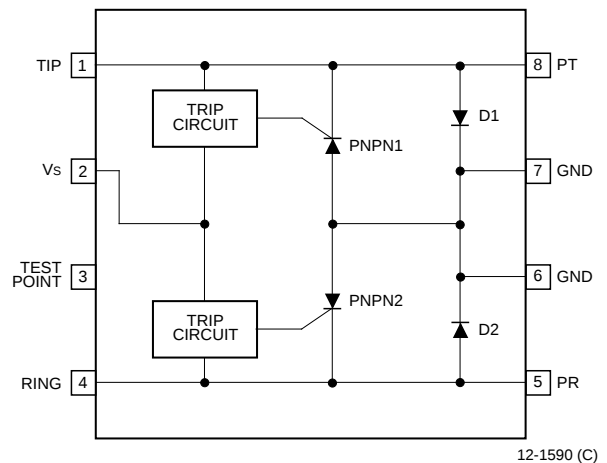
Table 1. Pin Description
(Applies to DIP and SONB packages)

Pin	Symbol	Name/Function
1	TIP	Tip signal from customer.
2	Vs	Supply voltage to gate (-20 V to -80 V).
3	—	Test Point, do not use.
4	RING	Ring signal from customer.
5	PR	Protected ring signal to line-feed-circuitry.
6	GND	Device ground/fault current return.
7	GND	Device ground/fault current return.
8	PT	Protected tip signal to line-feed circuitry.

Note: Pins 1 and 4 must always be connected to the protection resistors shown in Figures 4 and 5 (Line Feed Circuitry). The SLIC can be connected either to the protected outputs (pins 5 and 8) or to the inputs (pins 1 and 4).

Description

The L7591 Subscriber Line Interface Circuit (SLIC) Protector is designed to protect line-feed circuitry from fault-induced lightning and power-cross surge pulses. If a fault current forces TIP and/or RING to a more negative voltage than Vs, current is conducted through the trip circuit. When the specified trip current level is reached, transistors PNP1 and/or PNP2 will turn on and "crowbar" the majority of the current to ground. If similar pulses force TIP and/or RING to a more positive state than ground, diodes D1 and/or D2 will conduct the pulse to ground. The L7591 SLIC Protector is available in an 8-pin, plastic DIP (L7591AB) and in an 8-pin, plastic SONB package (L7591AS).



12-1590 (C)

Figure 1. Functional and Pin Diagram

Absolute Maximum Ratings (At 25 °C)

Stresses in excess of the Absolute Maximum Ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of the data sheet. Exposure to Absolute Maximum Ratings for extended periods can adversely affect the device reliability.

Parameter	Symbol	Value	Unit
Ambient Operating Temperature	T _A	–40 to +85	°C
Storage Temperature Range	T _{stg}	–40 to +125	°C
Pin Soldering Temperature (t = 15 s max)	—	300	°C
Supply Voltage	V _S	–83	V
Peak Pulse Current: * (See Figure 2.)			
10 x 1000 μs	—	30	A
5 x 320 μs	—	40	A
2 x 10 μs	—	80	A
Nonrepetitive Peak ON-state Current:			
t = 1 s, f = 50 Hz to 60 Hz	—	3.5	A
t = 10 ms, f = 50 Hz to 60 Hz	—	5	A
Maximum Gate Current (Half Sine Wave 10 ms)	—	2	A
Maximum Voltage:			
TIP or RING to Gnd	—	–100	V
V _S to Gnd	—	–80	V

*Pulse Waveform Data:

10 x 1000 μs	tr = 10 μs	tp = 1000 μs
5 x 320 μs	tr = 5 μs	tp = 320 μs
2 x 10 μs	tr = 2 μs	tp = 10 μs

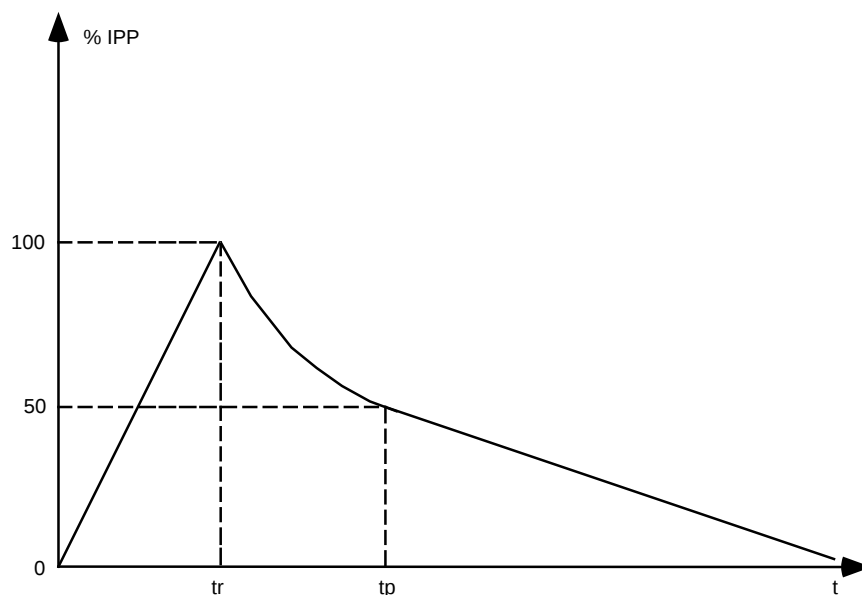


Figure 2. Pulse Waveform

Electrical Specifications (T_A = 25 °C)

The minimum and/or maximum limits specified for the parameters are based on the absolute system. The algebraic sign only applies to the direction of the parameter. These requirements apply to either the TIP or RING terminal; however, the device is capable of simultaneous Tip and Ring surges and continuous current, as noted below in Tip and Ring leads. See Figure 3 and Table 5 for symbol and test condition definition.

Table 2. Electrical Characteristics D1 and D2

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Forward Voltage	V _F	I _P = 5 A, t _P = 1 ms	—	1.6	3	V
Peak Forward Voltage	V _{FP}	I _{PP} = 30 A, 10 x 1000 μs	—	5.2	15	V

Table 3. Electrical Characteristics, PNP Circuitry

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Gate Trigger Current	I _{GT}	V _S = 48 V	0.2	—	15	mA
Holding Current	I _H	t _P = 10 ms, V _S = 48 V	–150	–250	—	mA
Trip Voltage	V _T	dc	—	V _S –2.0	V _S –2.8	V
Dynamic Trip Voltage	V _{SGL}	I _{PP} = 30 A, V _S = –48 V, 10 x 1000 μs	—	–51	–63	V
Reverse Leakage Current: V _S to Tip or Ring	I _{RG}	V _S = –75 V				
T _A = 25 °C	—	—	—	0.12	5	μA
T _A = 70 °C	—	—	—	—	50	μA
dv/dt Sensitivity	—	Tip or Ring Lead	±1000	—	—	V/μs
On-state Voltage:	V _{ON}	t _P = 1 ms				
I _T = 0.5 A	—	—	—	–1.3	—	V
I _T = 3.0 A	—	—	—	–2.3	—	V

Table 4. Electrical Characteristics, Diode and PNP Circuitry

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Reverse Leakage Current: (Gate Open)	I _R	V _R = –85 V				
T _A = 25 °C	—	—	—	0.14	5	μA
T _A = 70 °C	—	—	—	—	50	μA
Off-state Capacitance:	C _{off}	f = 1 MHz				
V _R = –3 V	—	—	—	50	—	pF
V _R = –48 V	—	—	—	40	—	pF

Characteristic Curves

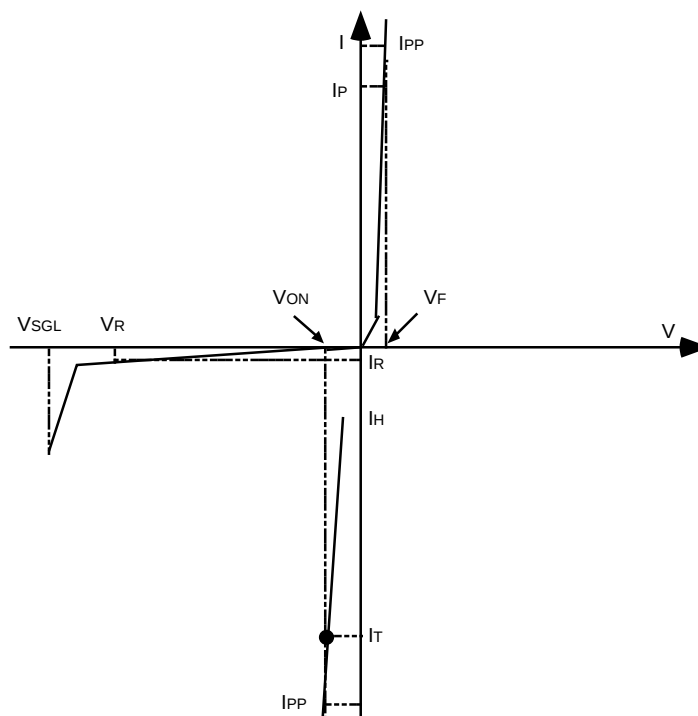
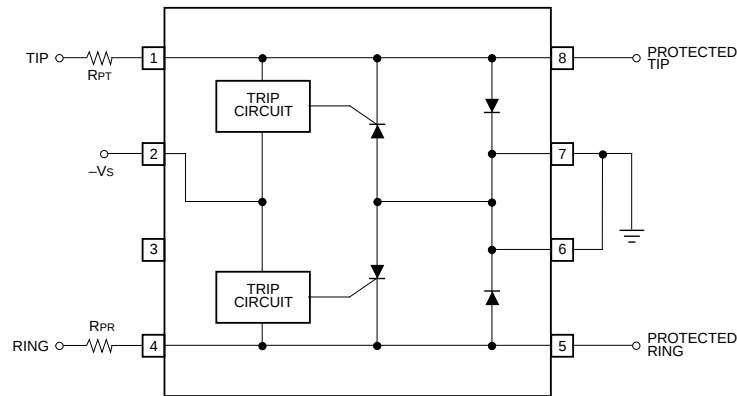


Figure 3. Typical Trip Characteristics of the SLIC Protector

Table 5. Symbols Definition

Symbol	Parameter
I_H	PNPN holding current
I_{GT}	Gate trigger current out of pin Vs
I_P	Pulse current
I_{PP}	Peak pulse current
I_{RG}	Reverse leakage current Vs to Tip or Ring
I_T	Tip or Ring current when PNPN is on
I_R	Reverse leakage current, Tip or Ring to Ground
V_F	Forward voltage, Tip or Ring to Ground
V_{FP}	Peak forward voltage, Tip or Ring to Ground
V_T	Trip voltage, Tip or Ring to Vs
V_{SGL}	Dynamic trip voltage, Tip or Ring to Vs
V_{ON}	PNPN on voltage at I_T
V_R	Tip or Ring voltage when PNPN is off
C_{off}	Off-state capacitance, Tip or Ring to Ground

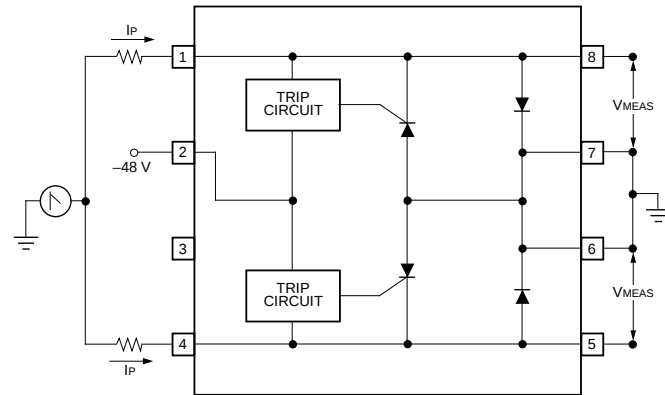
Applications



R_{PT} and R_{PR} must be properly selected for proper operation and/or response.

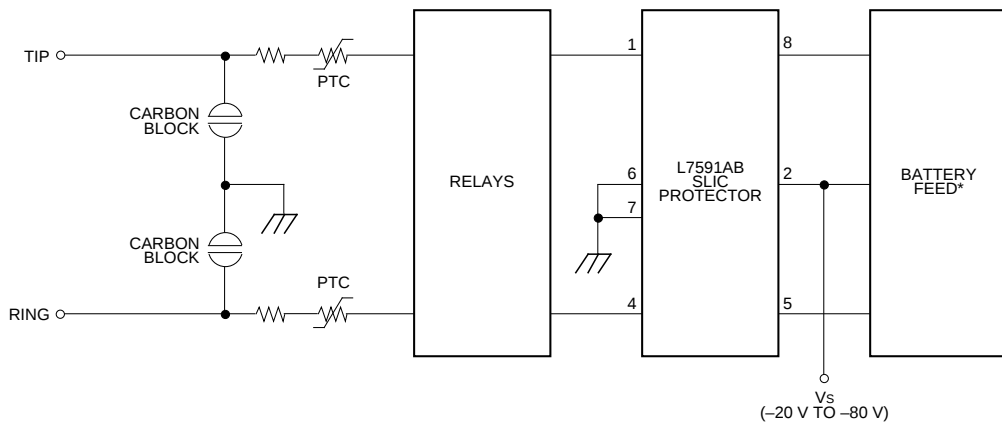
12-1632 (C)

Figure 4. Standard Configuration



12-1596 (C)

Figure 5. Test Circuit



12-1598 (C)

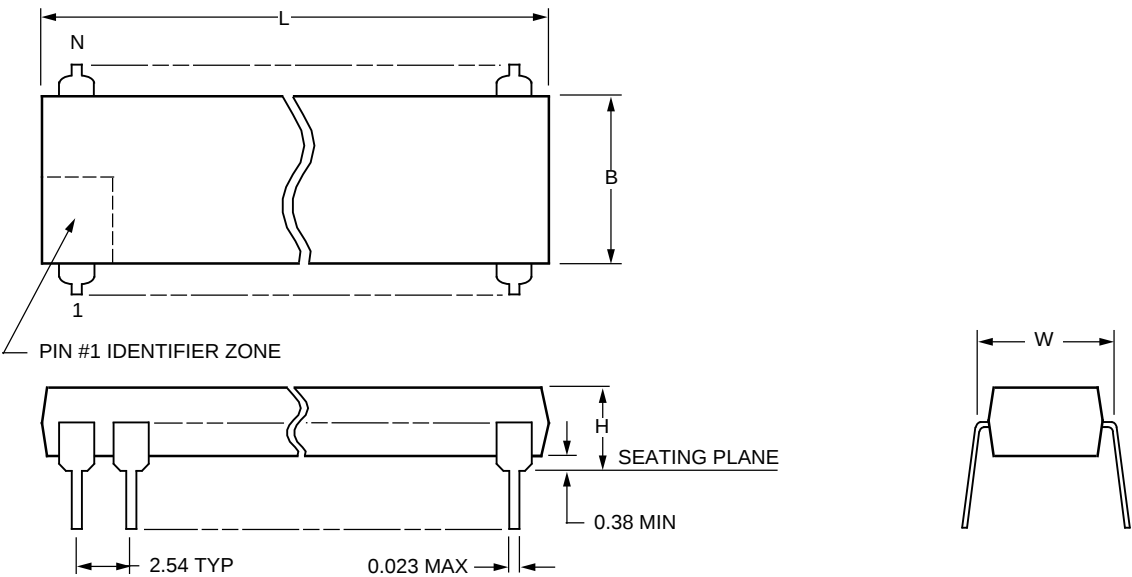
*An example device is Lucent Technologies Microelectronics Group's LB1276 High Balance SLIC.

Figure 6. Fully Protected Electronic TIP-RING Interface (Not all devices needed in all applications)

Outline Drawings

8-Pin DIP (L7591AB)

Dimensions are in millimeters.



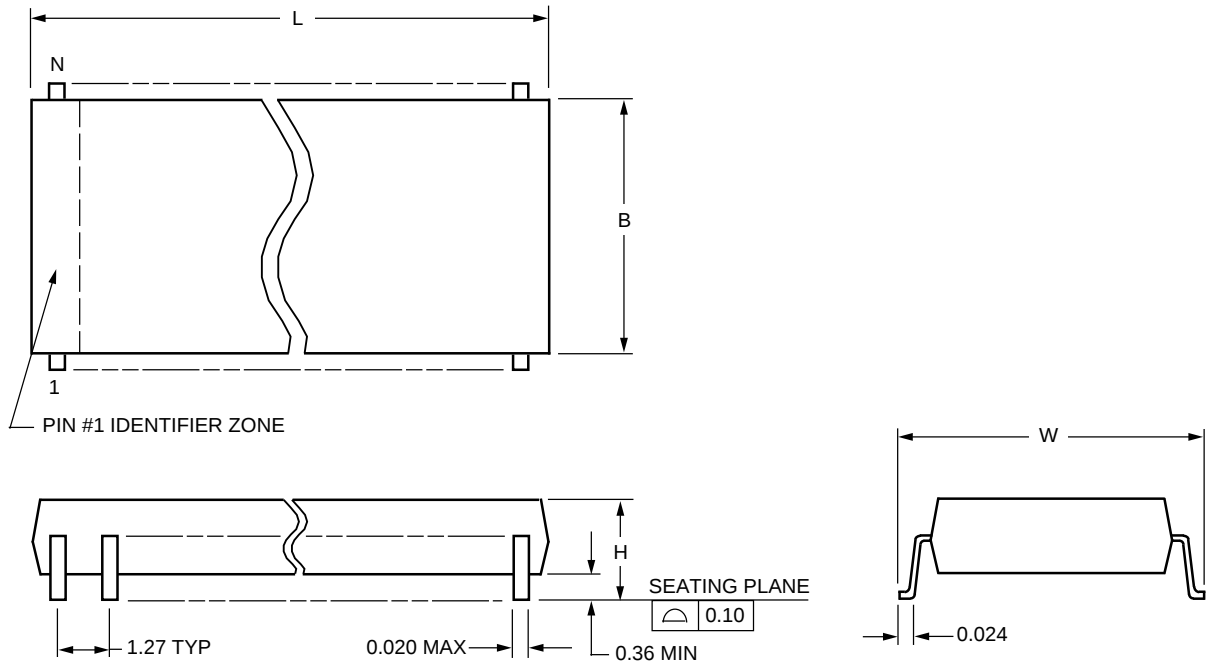
5-4410r.1

Package Description	Number of Pins (N)	Package Dimensions			
		Maximum Length (L)	Maximum Width Without Leads (B)	Maximum Width Including Leads (W)	Maximum Height Above Board (H)
PDIP3 (Plastic Dual-In-Line Package)	8	10.16	6.48	7.87	5.46

Outline Drawings (continued)

8-Pin, SONB (L7591AS)

Dimensions are in millimeters.



5-4414r.2

Package Description	Number of Pins (N)	Package Dimensions			
		Maximum Length (L)	Maximum Width Without Leads (B)	Maximum Width Including Leads (W)	Maximum Height Above Board (H)
SONB (Small Outline, Narrow Body)	8	5.08	4.01	6.17	1.73

Ordering Information

Device Part No.	Description	Package	Comcode
ATTL7591AB	SLIC Protector	8-Pin DIP	107056582
ATTL7591AS	SLIC Protector	8-Pin SONB	107056590
ATTL7591AS-TR	SLIC Protector	8-Pin SONB (Tape and Reel)	107232787

For additional information, contact your Microelectronics Group Account Manager or the following:

INTERNET: <http://www.lucent.com/micro>

E-MAIL: docmaster@micro.lucent.com

U.S.A.: Microelectronics Group, Lucent Technologies Inc., 555 Union Boulevard, Room 30L-15P-BA, Allentown, PA 18103

1-800-372-2447, FAX 610-712-4106 (In CANADA: 1-800-553-2448, FAX 610-712-4106)

ASIA PACIFIC: Microelectronics Group, Lucent Technologies Singapore Pte. Ltd., 77 Science Park Drive, #03-18 Cintech III, Singapore 118256

Tel. (65) 778 8833, FAX (65) 777 7495

JAPAN: Microelectronics Group, Lucent Technologies Japan Ltd., 7-18, Higashi-Gotanda 2-chome, Shinagawa-ku, Tokyo 141, Japan

Tel. (81) 3 5421 1600, FAX (81) 3 5421 1700

EUROPE: Data Requests: MICROELECTRONICS GROUP DATALINE: Tel. (44) 1189 324 299, FAX (44) 1189 328 148

Technical Inquiries: GERMANY: (49) 89 95086 0 (Munich), UNITED KINGDOM: (44) 1344 865 900 (Bracknell),

FRANCE: (33) 1 41 45 77 00 (Paris), SWEDEN: (46) 8 600 7070 (Stockholm), FINLAND: (358) 9 4354 2800 (Helsinki),

ITALY: (39) 2 6601 1800 (Milan), SPAIN: (34) 1 807 1441 (Madrid)

Lucent Technologies Inc. reserves the right to make changes to the product(s) or information contained herein without notice. No liability is assumed as a result of their use or application. No rights under any patent accompany the sale of any such product(s) or information.





**Стандарт
Электрон
Связь**

Мы молодая и активно развивающаяся компания в области поставок электронных компонентов. Мы поставляем электронные компоненты отечественного и импортного производства напрямую от производителей и с крупнейших складов мира.

Благодаря сотрудничеству с мировыми поставщиками мы осуществляем комплексные и плановые поставки широчайшего спектра электронных компонентов.

Собственная эффективная логистика и склад в обеспечивает надежную поставку продукции в точно указанные сроки по всей России.

Мы осуществляем техническую поддержку нашим клиентам и предпродажную проверку качества продукции. На все поставляемые продукты мы предоставляем гарантию .

Осуществляем поставки продукции под контролем ВП МО РФ на предприятия военно-промышленного комплекса России , а также работаем в рамках 275 ФЗ с открытием отдельных счетов в уполномоченном банке. Система менеджмента качества компании соответствует требованиям ГОСТ ISO 9001.

Минимальные сроки поставки, гибкие цены, неограниченный ассортимент и индивидуальный подход к клиентам являются основой для выстраивания долгосрочного и эффективного сотрудничества с предприятиями радиоэлектронной промышленности, предприятиями ВПК и научно-исследовательскими институтами России.

С нами вы становитесь еще успешнее!

Наши контакты:

Телефон: +7 812 627 14 35

Электронная почта: sales@st-electron.ru

Адрес: 198099, Санкт-Петербург,
Промышленная ул, дом № 19, литера Н,
помещение 100-Н Офис 331