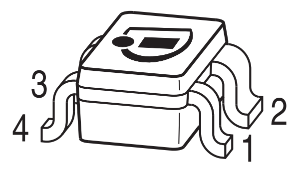
BFP405

NPN Silicon RF Transistor



- For low current applications
- For oscillators up to 12 GHz
- Noise figure F = 1.25 dB at 1.8 GHz
  outstanding G_{ms} = 23 dB at 1.8 GHz
- SIEGET ® 25 GHz f_T - Line

- Pb-free (RoHS compliant) package
- Qualified according AEC Q101

ESD (**E**lectro**s**tatic **d**ischarge) sensitive device, observe handling precaution!

Type	Marking	Pin Configuration						Package
BFP405	ALs	1=B	2=E	3=C	4=E	-	-	SOT343

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}		V
T_A > 0 °C		4.5	
$T_A \leq$ 0 °C		4.1	
Collector-emitter voltage	V_{CES}	15	
Collector-base voltage	V_{CBO}	15	
Emitter-base voltage	V_{EBO}	1.5	
Collector current	I_C	25	mA
Base current	I_B	1	
Total power dissipation[1)]	P_{tot}	75	mW
$T_S \leq$ 108 °C			
Junction temperature	T_j	150	°C
Ambient temperature	T_A	-65 ... 150	
Storage temperature	T_{stg}	-65 ... 150	

[1] T_S is measured on the collector lead at the soldering point to the pcb

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Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point[1)]	R_{thJS}	≤ 555	K/W

Electrical Characteristics at T_A = 25°C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics					
Collector-emitter breakdown voltage I_C = 1 mA, I_B = 0	$V_{(BR)CEO}$	4.5	5	-	V
Collector-emitter cutoff current V_{CE} = 15 V, V_{BE} = 0	I_{CES}	-	-	10	µA
Collector-base cutoff current V_{CB} = 5 V, I_E = 0	I_{CBO}	-	-	100	nA
Emitter-base cutoff current V_{EB} = 0.5 V, I_C = 0	I_{EBO}	-	-	1	µA
DC current gain I_C = 5 mA, V_{CE} = 4 V, pulse measured	h_{FE}	60	95	130	-

[1]For calculation of R_{thJA} please refer to Application Note Thermal Resistance

Electrical Characteristics at T_A = 25°C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling)					
Transition frequency I_C = 10 mA, V_{CE} = 3 V, f = 2 GHz	f_T	18	25	-	GHz
Collector-base capacitance V_{CB} = 2 V, f = 1 MHz, V_{BE} = 0 , emitter grounded	C_{cb}	-	0.05	0.1	pF
Collector emitter capacitance V_{CE} = 2 V, f = 1 MHz, V_{BE} = 0 , base grounded	C_{ce}	-	0.24	-	
Emitter-base capacitance V_{EB} = 0.5 V, f = 1 MHz, V_{CB} = 0 , collector grounded	C_{eb}	-	0.29	-	
Noise figure I_C = 2 mA, V_{CE} = 2 V, f = 1.8 GHz, $Z_S = Z_{Sopt}$	F	-	1.25	-	dB
Power gain, maximum stable1) I_C = 5 mA, V_{CE} = 2 V, $Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$, f = 1.8 GHz	G_{ms}	-	23	-	dB
Insertion power gain V_{CE} = 2 V, I_C = 5 mA, f = 1.8 GHz, $Z_S = Z_L$ = 50 Ω	$\|S_{21}\|^2$	14	18.5	-	
Third order intercept point at output2) V_{CE} = 2 V, I_C = 5 mA, f = 1.8 GHz, $Z_S = Z_L$ = 50 Ω	IP_3	-	15	-	dBm
1dB Compression point at output I_C = 5 mA, V_{CE} = 2 V, $Z_S = Z_L$ = 50 Ω, f = 1.8 GHz	P_{-1dB}	-	5	-	

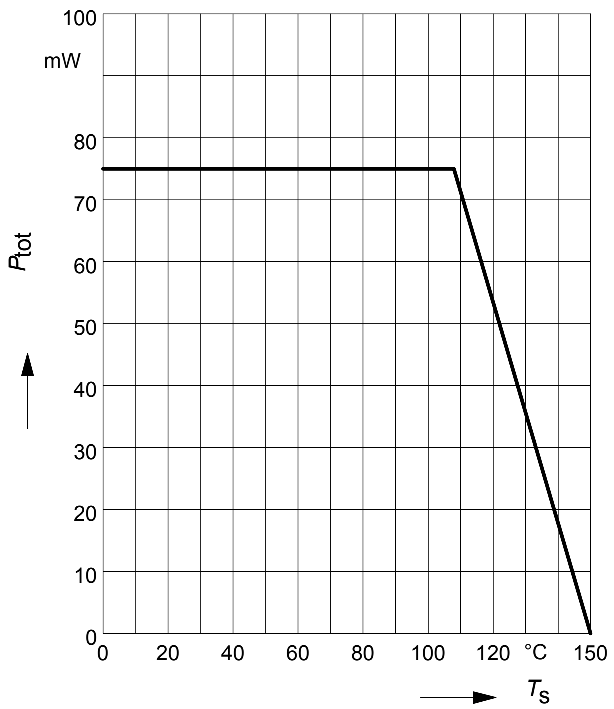
[1] $G_{ms} = |S_{21} / S_{12}|$

[2] IP3 value depends on termination of all intermodulation frequency components.
Termination used for this measurement is 50Ω from 0.1 MHz to 6 GHz

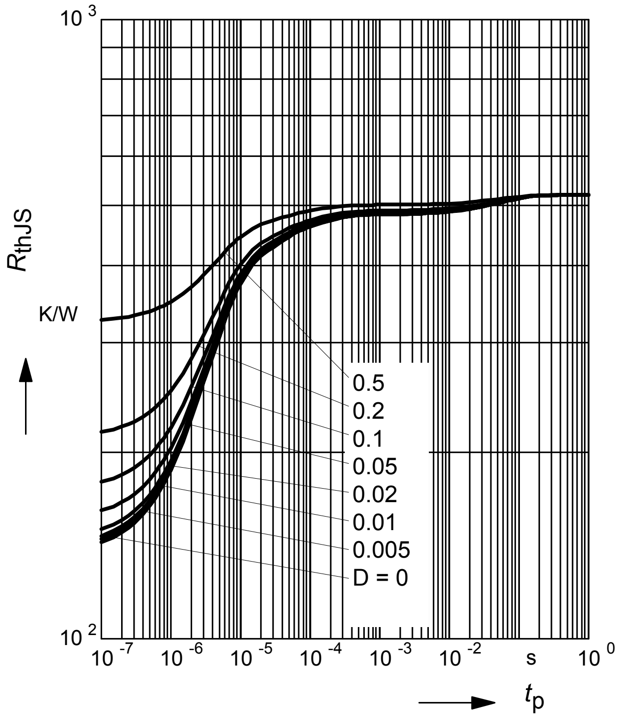
Simulation Data

For SPICE-model as well as for S-parameters including noise parameters refer to our internet website: **www.infineon.com/rf.models**. Please consult our website and download the latest version before actually starting your design.
The simulation data have been generated and verified up to 12GHz using typical devices. The BFP405 nonlinear SPICE-model reflects the typical DC- and RF-device performance with high accuracy.

Total power dissipation $P_{tot} = f(T_S)$

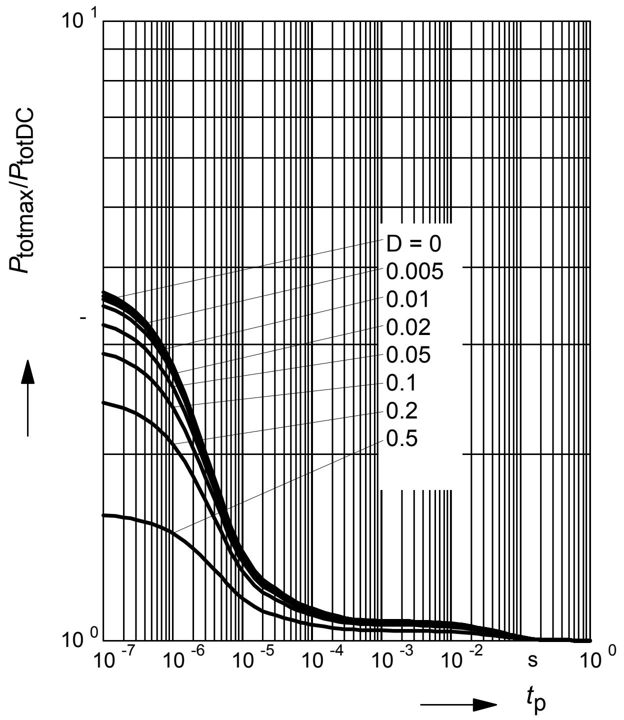


Permissible Pulse Load $R_{thJS} = f(t_p)$



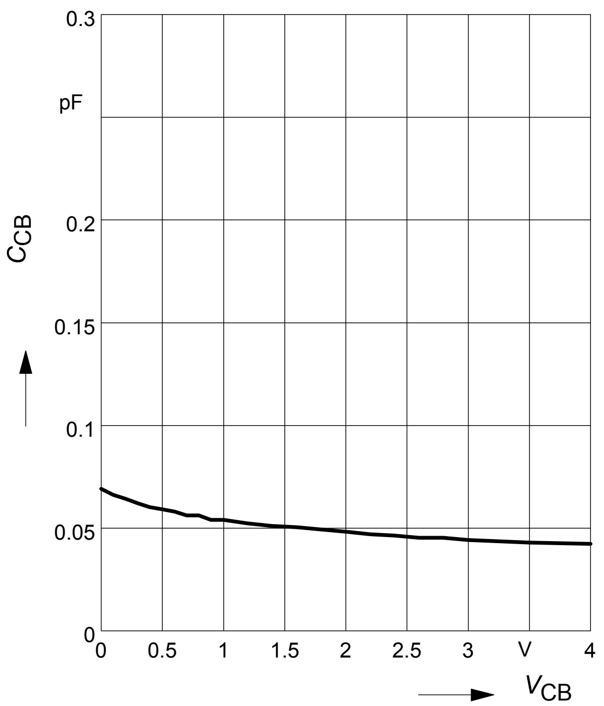
Permissible Pulse Load

$P_{totmax}/P_{totDC} = f(t_p)$



Collector-base capacitance $C_{cb} = f(V_{CB})$

f = 1MHz



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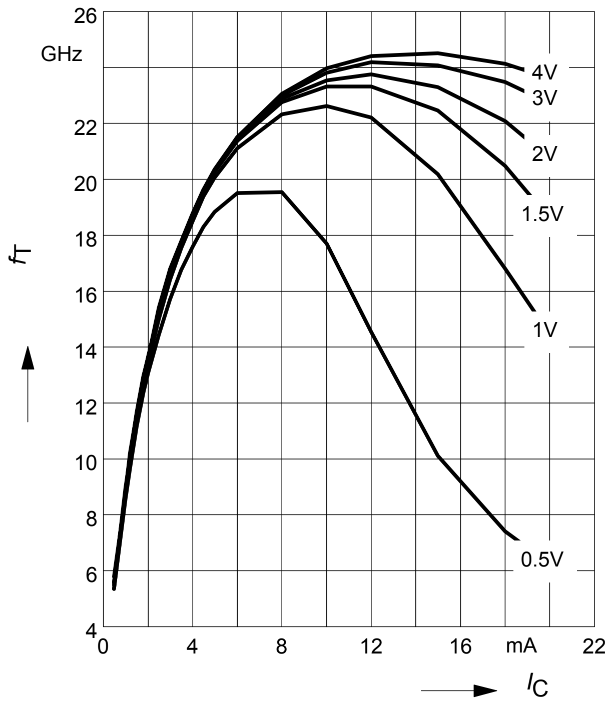
Transition frequency $f_T = f(I_C)$

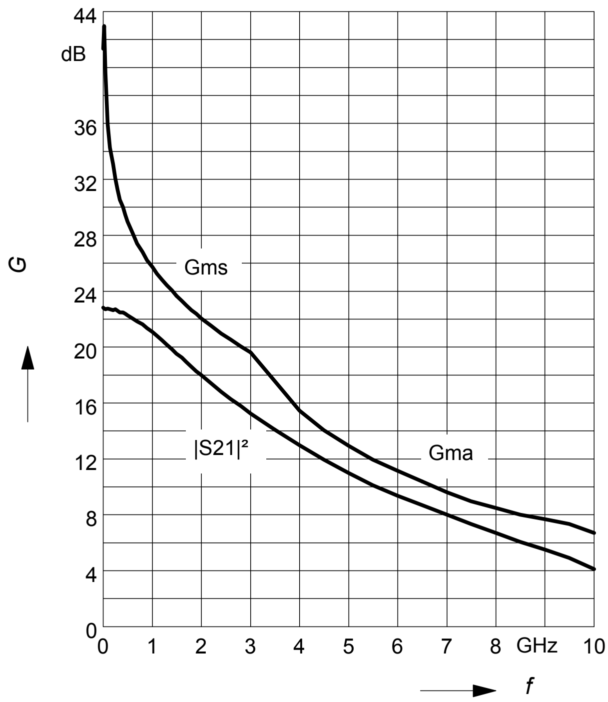
f = 2 GHz

V_{CE} = parameter in V



Power gain G_{ma}, G_{ms}, $|S_{21}|^2 = f(f)$

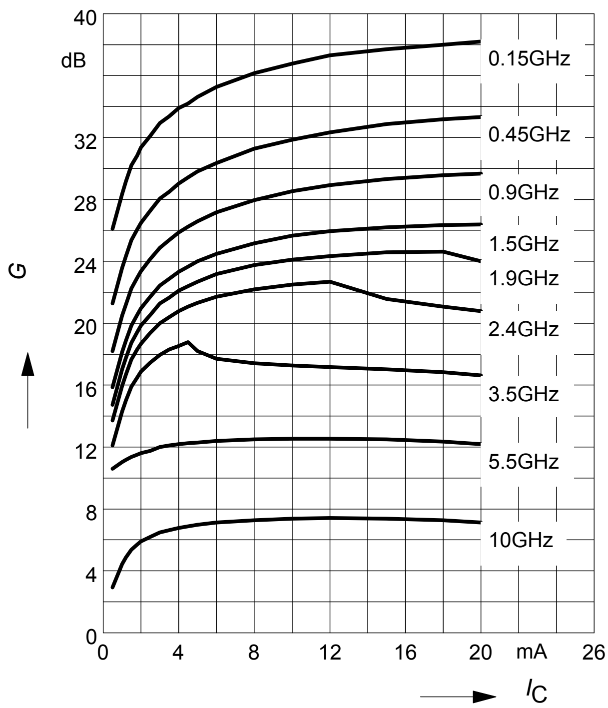
V_{CE} = 3 V, I_C = 5 mA



Power gain G_{ma}, $G_{ms} = f(I_C)$

V_{CE} = 3V

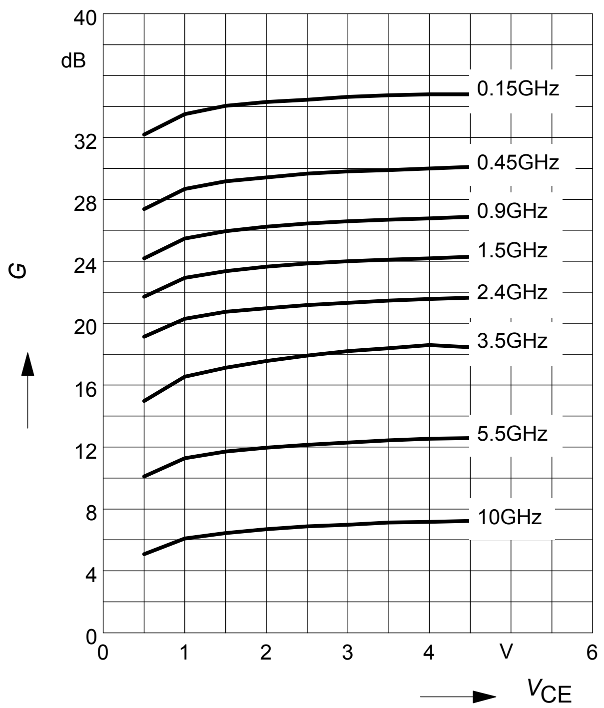
f = parameter in GHz



Power gain G_{ma}, $G_{ms} = f(V_{CE})$

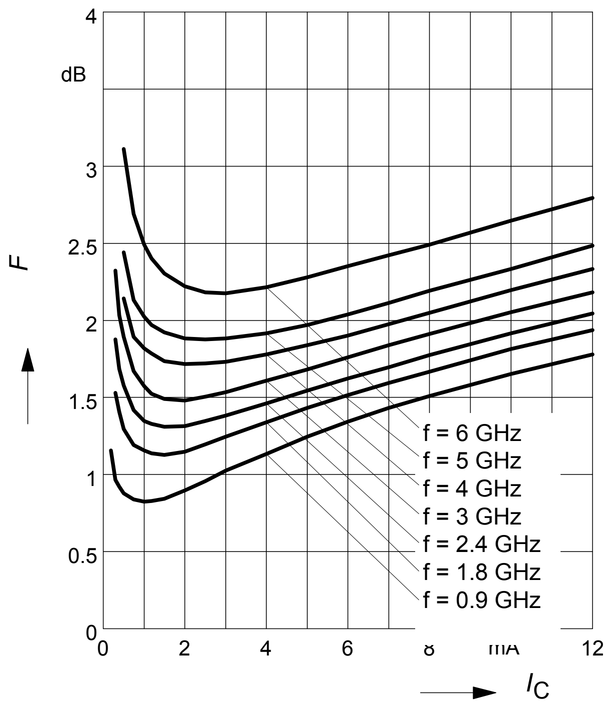
I_C = 5 mA

f = parameter in GHz



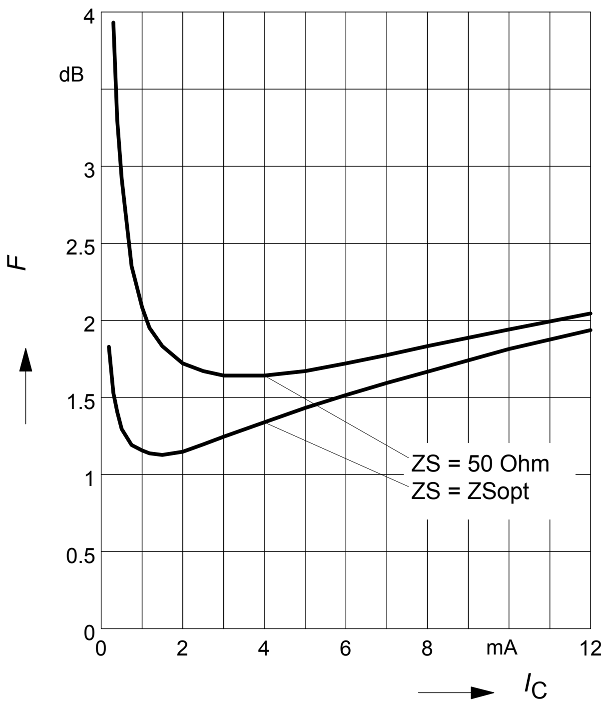
Noise figure $F = f(I_C)$

V_{CE} = 2 V, $Z_S = Z_{Sopt}$



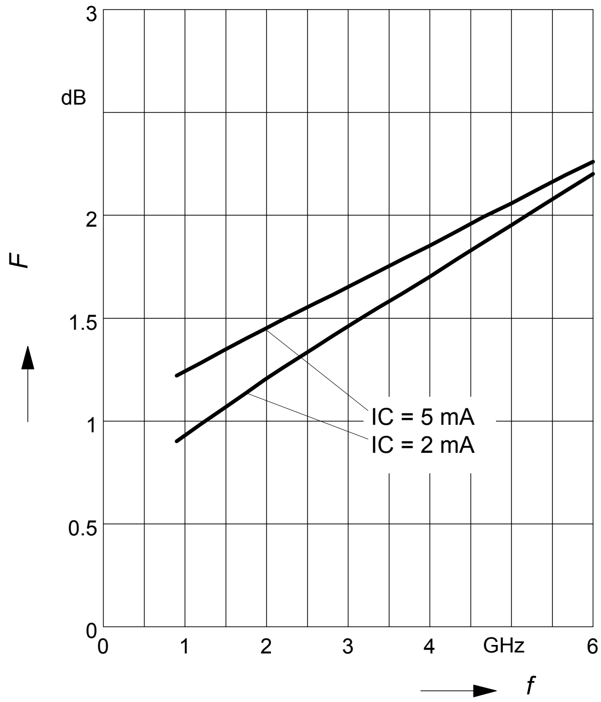
Noise figure $F = f(I_C)$

V_{CE} = 2 V, f = 1.8 GHz



Noise figure $F = f(f)$

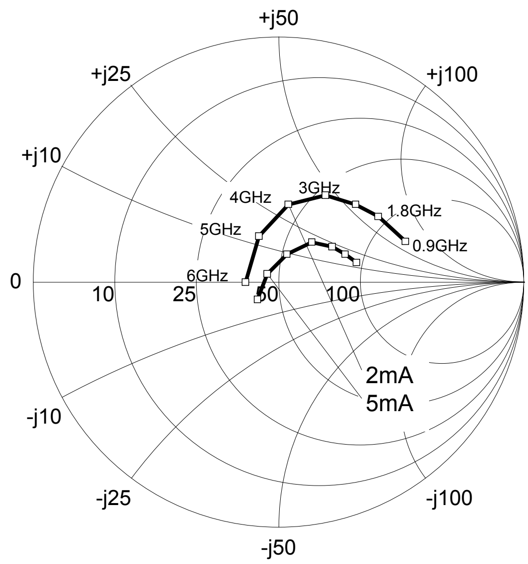
V_{CE} = 1 V, $Z_S = Z_{Sopt}$



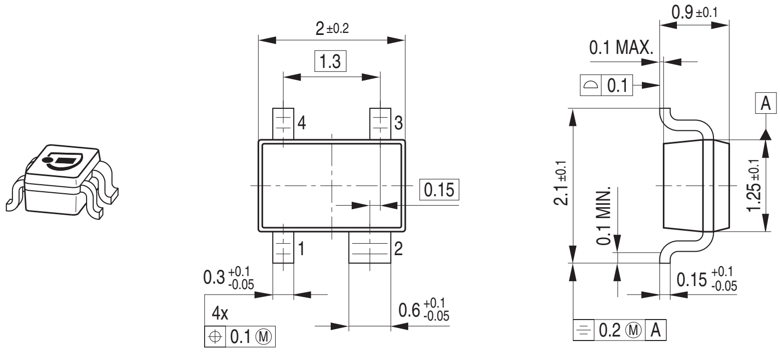
Source impedance for min.

noise figure vs. frequency

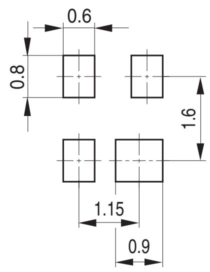
V_{CE} = 3 V, I_C = 2 mA / 5 mA



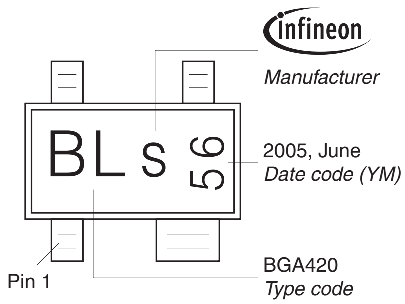
Package Outline



Foot Print

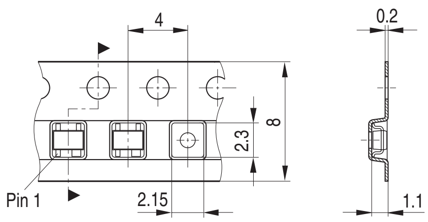


Marking Layout (Example)



Standard Packing

Reel ø180 mm = 3.000 Pieces/Reel
Reel ø330 mm = 10.000 Pieces/Reel



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