AN-2223 LMP8278Q MSOP Evaluation Board

1 Introduction

The LMP8278Q MSOP Evaluation Board, Figure 1, is designed to evaluate the LMP8278Q current sense amplifier. The LMP8278Q is a 40V Common Mode, Unidirectional Precision Current Sensing amplifier with a gain of 14x. This board has an LMP8278Q part mounted on the PCB together with a de-coupling capacitor, power supply connections, and input and output connectors.

2 General Description

The LMP8278Q is a fixed 14x gain precision current sense amplifier. The part amplifies and filters small differential signals in the presence of high common mode voltages. The part operates from a single 5V supply voltage. With an input common mode voltage range from –2V to +28V the gain is very precise (±0.5%). The part can handle common mode voltages in the range –2V to +40V with relaxed specifications. The LMP8278Q is a member of the Linear Monolithic Precision (LMP™) family and is ideal for unidirectional current sensing applications.

The part has a precise gain of 14x, which is adequate in most targeted applications to drive an ADC to its full scale value. The fixed gain is achieved in two separate stages, a preamplifier with a gain of 7x and an output stage buffer amplifier with a gain of 2x. The connection between the two stages of the signal path is brought out on two pins to enable the possibility to create an additional filter network around the output buffer amplifier. These pins can also be used for alternative configurations with different gain as described in the applications section of the datasheet.

The LMP8278Q incorporates enhanced manufacturing and support processes for the automotive market, including defect detection methodologies. Reliability qualification is compliant with the requirements and temperature grades defined in the AEC Q100 standard.

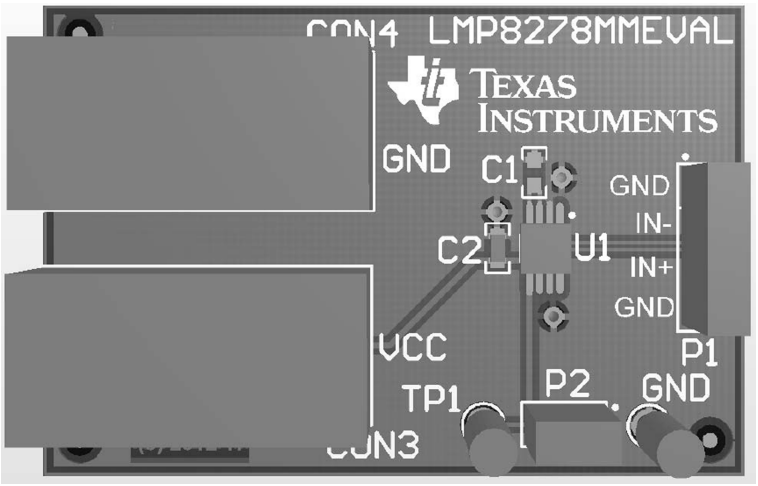

Figure 1. LMP8278Q Evaluation Board

3 LMP8278Q Operating Conditions

- Temperature Range −40°C to 125°C
- Power Supply Voltage 4.5V ≤ V_S ≤ 5.5V
- CMVR for ±1.0% gain accuracy −2V to +40V
- CMVR for ±0.5% gain accuracy −2V to 28V

4 Description of the LMP8278Q Evaluation Board

The LMP8278Q evaluation board requires a power supply with a voltage between 4.5V and 5.5V. The supply current will be <1 mA in normal operation with high impedance load on the output of the part. The positive supply voltage is connected to VCC (CON3) The negative supply voltage is connected to GND (CON4).

The input signal is connected between –IN and +IN (pins 2 and 3 of P1) and the output signal is available at OUT (pin 2 of P2).

There is the option to place a capacitor C1 on the board to create a low pass filter between the first and second stage of the amplifier. This can be used to reduce the output noise and glitches that might appear from switching large common mode voltages with very fast transients at the input (for more details about this filter, see the application information section of *LMP8278Q High Common Mode, 14 x Gain, Precision Current Sensing Amplifier* (SNAS575)).

Additional test points, TP1 and GND, are provided at the output of the LMP8278Q for convenience of use.

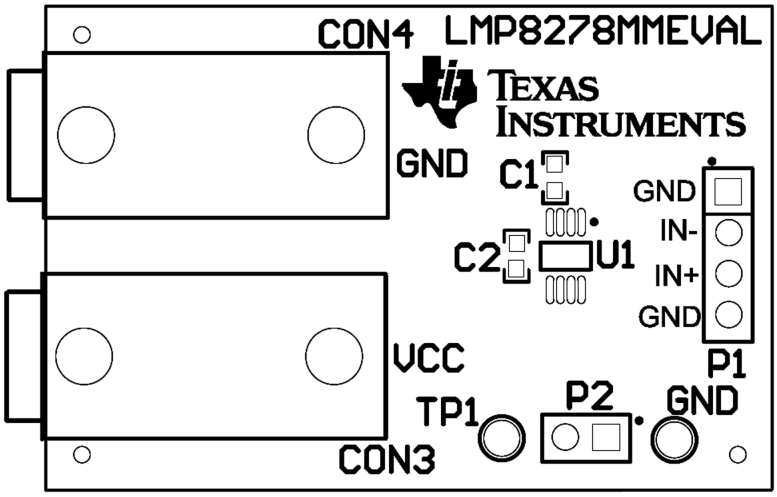

Figure 2. Power Supply Connectors and Headers

5 PCB Layout Guidelines

This section provides general practical guidelines for PCB layouts that use various power and ground traces. Designers should note that these are only "rule-of-thumb" recommendations and the actual results are predicated on the final layout.

5.1 Differential Signals

The two input pins of the LMP8278Q form a differential pair that must be handled following the rules given below:

- Keep both signals coupled by routing them closely together and keeping them of equal length.
- Do not allow any other signal in between these two signals of the differential pair.
- Keep all impedances in both traces of the signal equal.

5.2 Power, Ground, and De-coupling

Keep the power supply de-coupling capacitor close to the power supply pin (V_S of the part.)

Make sure all return currents of the signals can flow next to the originating signals.

6 Description of Headers and Connectors of the LMP8278Q Evaluation Board

The LMP8278Q Evaluation Board provides the following headers and connectors for connecting test equipment and supplying the LMP8278Q part.

7 Connector and Header Functions

Designator	Function or Use	Comment
P1	GND	Pin1
	Input	Pin2 = Negative Input
		Pin3 = Positive Input
	GND	Pin4
P2	Output	Pin1 = GND Pin2 = Ouput
CON3	Supply VCC	
CON4	Ground connection (GND)	
TP1	Output test point	
GND	GND test point	

8 Schematic with LMP8278QMM Mounted

Figure 3 shows the LMP8278Q evaluation board with an LMP8278QMM mounted on the PCB.

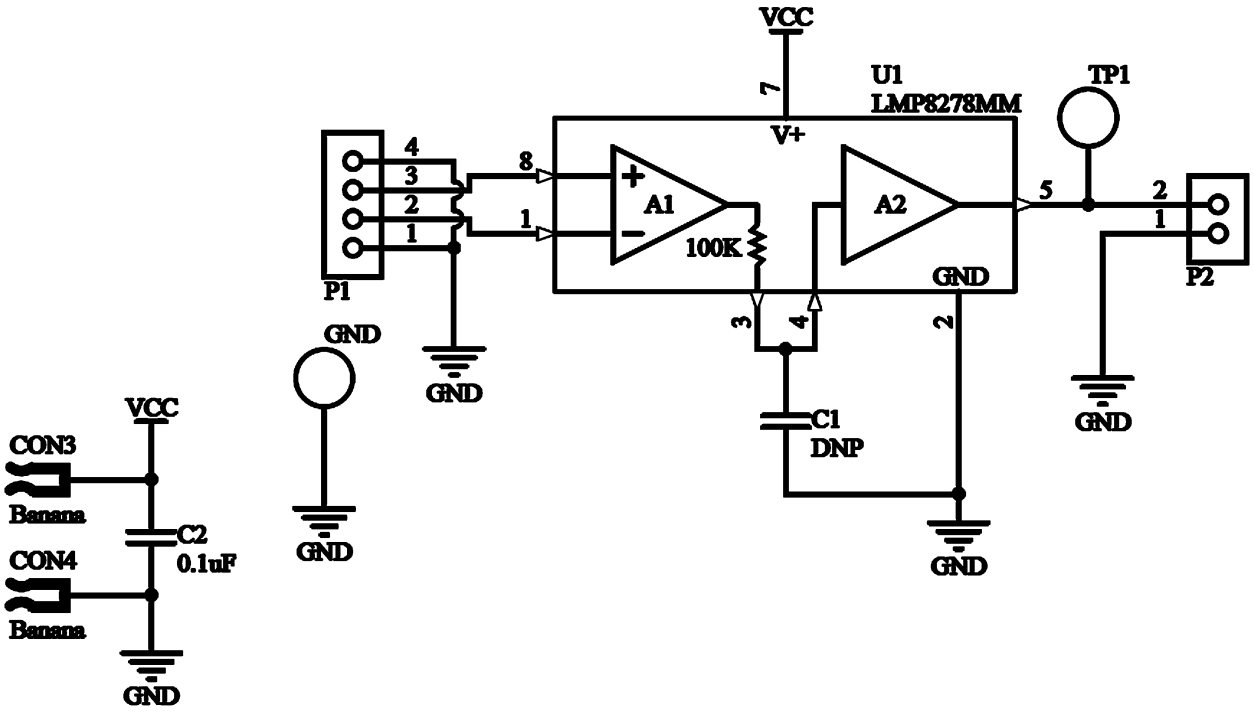

Figure 3. Schematic Diagram

9 Bill of Materials

Designator	Component	Value	Tolerance	Comment	Package Type
C1	Capacitor	N/A			0603
C2	Capacitor	100 nF			0603
P1	Header 4 pin			Input	
P2	Header 2 pin			Output	
CON3	Banana plug	RED		VCC	Banana_COLOR
CON4	Banana plug	BLACK		GND	Banana_COLOR
U1	LMP8278Q	LMP8278QMM			MUA08A
TP1	Test point	RED		VCC	
GND	Test point	BLACK		GND	

10 Layout

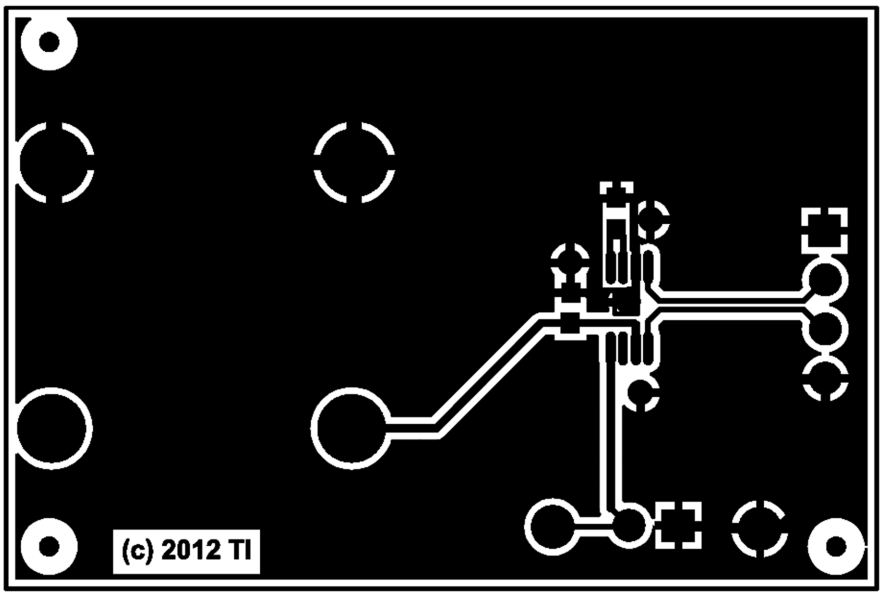

Figure 4. Layout, Top Layer

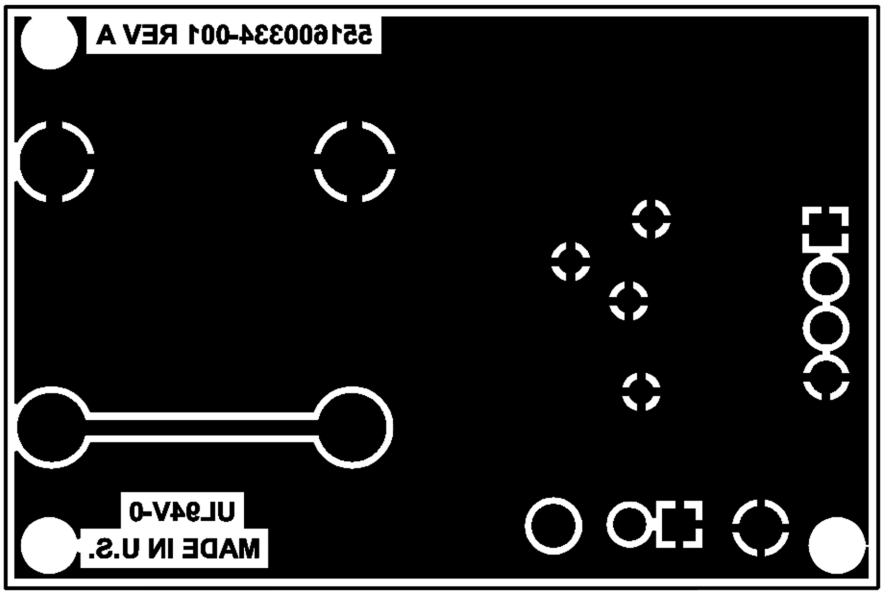

Figure 5. Layout, Bottom layer

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