



# FDS6688S

## 30V N-Channel PowerTrench® SyncFET™

### General Description

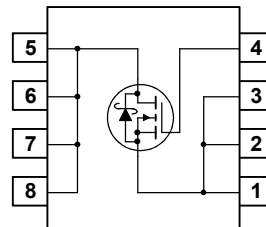
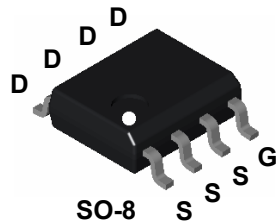
The FDS6688S is designed to replace a single SO-8 MOSFET and Schottky diode in synchronous DC:DC power supplies. This 30V MOSFET is designed to maximize power conversion efficiency, providing a low  $R_{DS(ON)}$  and low gate charge. The FDS6688S includes an integrated Schottky diode using Fairchild's monolithic SyncFET technology.

### Applications

- DC/DC converter
- Motor drives

### Features

- 16 A, 30 V.  $R_{DS(ON)} = 6.0\text{ m}\Omega @ V_{GS} = 10\text{ V}$   
 $R_{DS(ON)} = 7.5\text{ m}\Omega @ V_{GS} = 4.5\text{ V}$
- Includes SyncFET Schottky body diode
- High performance trench technology for extremely low  $R_{DS(ON)}$  and fast switching
- High power and current handling capability



### Absolute Maximum Ratings $T_A = 25^\circ\text{C}$ unless otherwise noted

| Symbol         | Parameter                                                                  | Ratings         | Units            |
|----------------|----------------------------------------------------------------------------|-----------------|------------------|
| $V_{DSS}$      | Drain-Source Voltage                                                       | 30              | V                |
| $V_{GSS}$      | Gate-Source Voltage                                                        | $\pm 20$        | V                |
| $I_D$          | Drain Current – Continuous (Note 1a)<br>– Pulsed                           | 16              | A                |
|                |                                                                            | 50              |                  |
| $P_D$          | Power Dissipation for Single Operation (Note 1a)<br>(Note 1b)<br>(Note 1c) | 2.5             | W                |
|                |                                                                            | 1.2             |                  |
|                |                                                                            | 1               |                  |
| $T_J, T_{STG}$ | Operating and Storage Junction Temperature Range                           | $-55$ to $+125$ | $^\circ\text{C}$ |

### Thermal Characteristics

|                 |                                                   |    |                    |
|-----------------|---------------------------------------------------|----|--------------------|
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 1a) | 50 | $^\circ\text{C/W}$ |
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case (Note 1)     | 25 |                    |

### Package Marking and Ordering Information

| Device Marking | Device   | Reel Size | Tape width | Quantity   |
|----------------|----------|-----------|------------|------------|
| FDS6688S       | FDS6688S | 13"       | 12mm       | 2500 units |

**Electrical Characteristics** $T_A = 25^\circ\text{C}$  unless otherwise noted

| Symbol | Parameter | Test Conditions | Min | Typ | Max | Units |
|--------|-----------|-----------------|-----|-----|-----|-------|
|--------|-----------|-----------------|-----|-----|-----|-------|

**Off Characteristics**

|                                      |                                           |                                                         |    |    |           |                      |
|--------------------------------------|-------------------------------------------|---------------------------------------------------------|----|----|-----------|----------------------|
| $BV_{DSS}$                           | Drain–Source Breakdown Voltage            | $V_{GS} = 0\text{ V}, I_D = 1\text{ mA}$                | 30 |    |           | V                    |
| $\frac{\Delta BV_{DSS}}{\Delta T_J}$ | Breakdown Voltage Temperature Coefficient | $I_D = 10\text{ mA}$ , Referenced to $25^\circ\text{C}$ |    | 24 |           | mV/ $^\circ\text{C}$ |
| $I_{DSS}$                            | Zero Gate Voltage Drain Current           | $V_{DS} = 24\text{ V}, V_{GS} = 0\text{ V}$             |    |    | 500       | $\mu\text{A}$        |
| $I_{GSS}$                            | Gate–Body Leakage                         | $V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$         |    |    | $\pm 100$ | nA                   |

**On Characteristics**

|                                        |                                                |                                                                                                                                                                 |   |                   |            |                      |
|----------------------------------------|------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------|---|-------------------|------------|----------------------|
| $V_{GS(th)}$                           | Gate Threshold Voltage                         | $V_{DS} = V_{GS}, I_D = 1\text{ mA}$                                                                                                                            | 1 | 1.4               | 3          | V                    |
| $\frac{\Delta V_{GS(th)}}{\Delta T_J}$ | Gate Threshold Voltage Temperature Coefficient | $I_D = 10\text{ mA}$ , Referenced to $25^\circ\text{C}$                                                                                                         |   | –4                |            | mV/ $^\circ\text{C}$ |
| $R_{DS(on)}$                           | Static Drain–Source On–Resistance              | $V_{GS} = 10\text{ V}, I_D = 16\text{ A}$<br>$V_{GS} = 4.5\text{ V}, I_D = 14.5\text{ A}$<br>$V_{GS} = 10\text{ V}, I_D = 16\text{ A}, T_J = 125^\circ\text{C}$ |   | 4.8<br>5.7<br>6.5 | 6.0<br>7.5 | m $\Omega$           |
| $g_{FS}$                               | Forward Transconductance                       | $V_{DS} = 10\text{ V}, I_D = 16\text{ A}$                                                                                                                       |   | 74                |            | S                    |

**Dynamic Characteristics**

|           |                              |                                                                      |  |      |  |          |
|-----------|------------------------------|----------------------------------------------------------------------|--|------|--|----------|
| $C_{iss}$ | Input Capacitance            | $V_{DS} = 15\text{ V}, V_{GS} = 0\text{ V},$<br>$f = 1.0\text{ MHz}$ |  | 3290 |  | pF       |
| $C_{oss}$ | Output Capacitance           |                                                                      |  | 890  |  | pF       |
| $C_{rss}$ | Reverse Transfer Capacitance |                                                                      |  | 290  |  | pF       |
| $R_G$     | Gate Resistance              | $V_{GS} = 15\text{ mV}, f = 1.0\text{ MHz}$                          |  | 1.5  |  | $\Omega$ |

**Switching Characteristics (Note 2)**

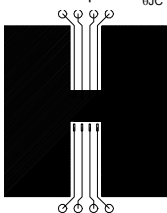
|              |                                          |                                                                                                |  |     |    |    |
|--------------|------------------------------------------|------------------------------------------------------------------------------------------------|--|-----|----|----|
| $t_{d(on)}$  | Turn–On Delay Time                       | $V_{DD} = 15\text{ V}, I_D = 1\text{ A},$<br>$V_{GS} = 10\text{ V}, R_{GEN} = 6\text{ }\Omega$ |  | 12  | 22 | ns |
| $t_r$        | Turn–On Rise Time                        |                                                                                                |  | 12  | 22 | ns |
| $t_{d(off)}$ | Turn–Off Delay Time                      |                                                                                                |  | 30  | 46 | ns |
| $t_f$        | Turn–Off Fall Time                       |                                                                                                |  | 60  | 96 | ns |
| $Q_{g(TOT)}$ | Total Gate Charge at $V_{GS}=10\text{V}$ | $V_{DS} = 15\text{ V}, I_D = 16\text{ A}$                                                      |  | 56  | 78 | nC |
| $Q_g$        | Total Gate Charge at $V_{GS}=5\text{V}$  |                                                                                                |  | 31  | 44 | nC |
| $Q_{gs}$     | Gate–Source Charge                       |                                                                                                |  | 8.2 |    | nC |
| $Q_{gd}$     | Gate–Drain Charge                        |                                                                                                |  | 9.0 |    | nC |

**Drain–Source Diode Characteristics and Maximum Ratings**

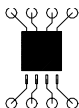
|          |                                    |                                                                       |  |     |     |    |
|----------|------------------------------------|-----------------------------------------------------------------------|--|-----|-----|----|
| $V_{SD}$ | Drain–Source Diode Forward Voltage | $V_{GS} = 0\text{ V}, I_S = 3.5\text{ A}$ (Note 2)                    |  | 380 | 700 | mV |
| $t_{rr}$ | Diode Reverse Recovery Time        | $I_F = 16\text{ A},$<br>$dI_F/dt = 300\text{ A}/\mu\text{s}$ (Note 3) |  | 30  |     | ns |
| $I_{RM}$ | Diode Reverse Recovery Current     |                                                                       |  | 2   |     | A  |
| $Q_{rr}$ | Diode Reverse Recovery Charge      |                                                                       |  | 31  |     | nC |

**Notes:**

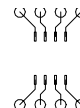
1.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\theta JC}$  is guaranteed by design while  $R_{\theta CA}$  is determined by the user's board design.



a) 50°/W when mounted on a 1 in<sup>2</sup> pad of 2 oz copper



b) 105°/W when mounted on a .04 in<sup>2</sup> pad of 2 oz copper



c) 125°/W when mounted on a minimum pad.

See "SyncFET Schottky body diode characteristics" below

Scale 1 : 1 on letter size paper

Pulse Test: Pulse Width < 300 $\mu\text{s}$ , Duty Cycle < 2.0%

## Typical Characteristics

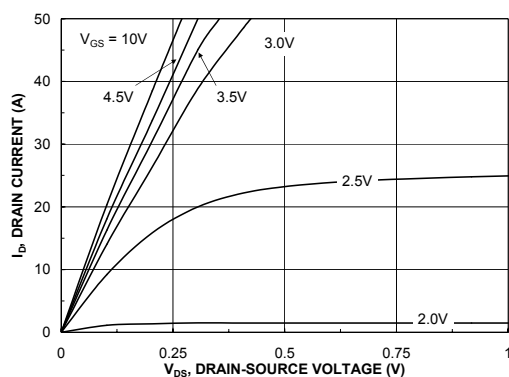


Figure 1. On-Region Characteristics.

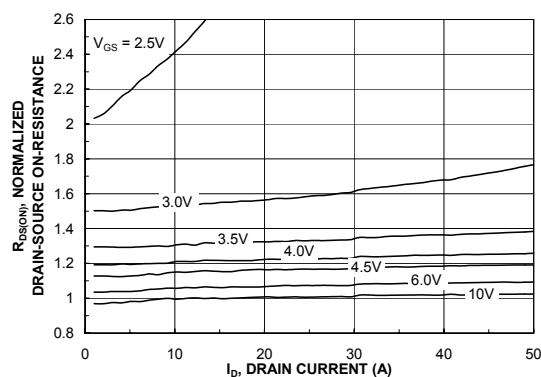


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

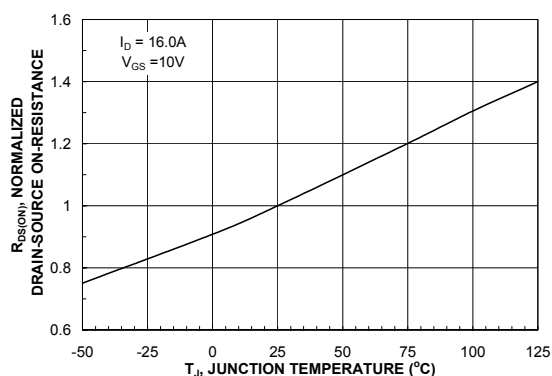


Figure 3. On-Resistance Variation with Temperature.

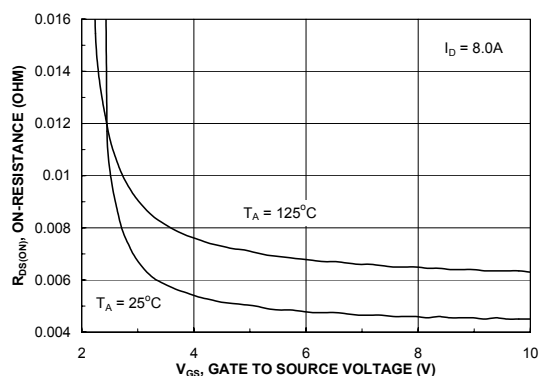


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

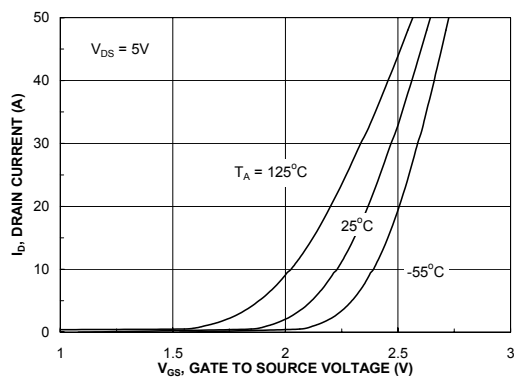


Figure 5. Transfer Characteristics.

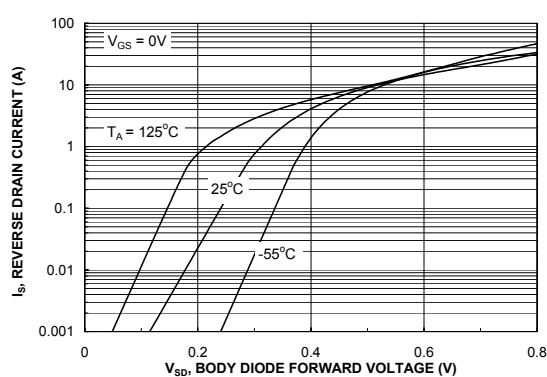


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

## Typical Characteristics (continued)

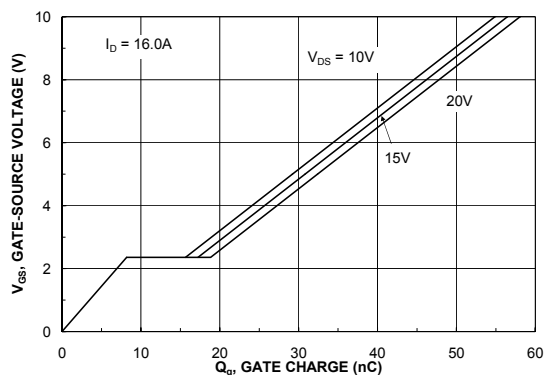


Figure 7. Gate Charge Characteristics.

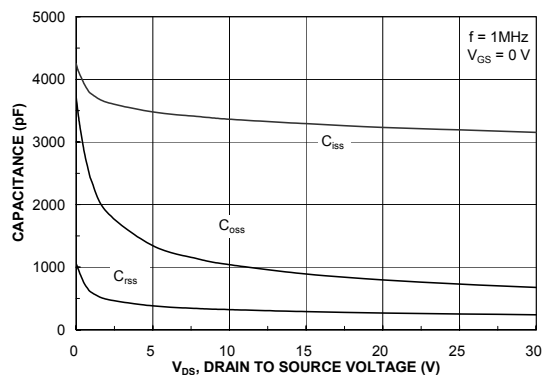


Figure 8. Capacitance Characteristics.

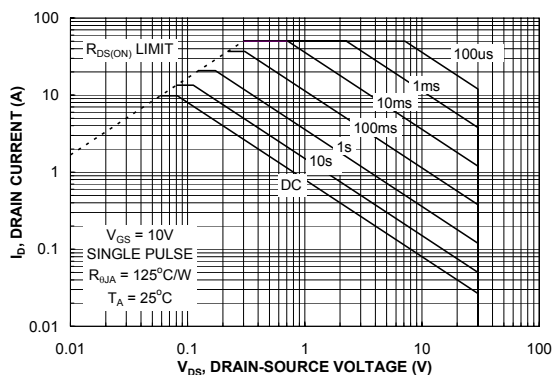


Figure 9. Maximum Safe Operating Area.

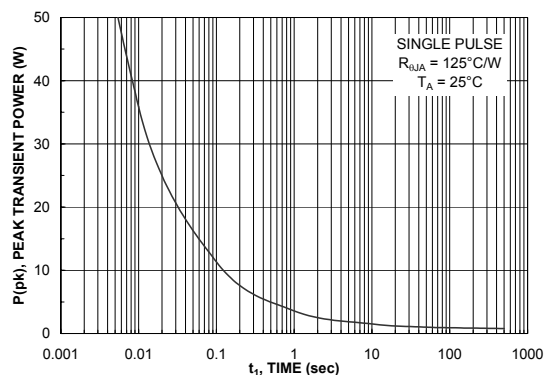


Figure 10. Single Pulse Maximum Power Dissipation.

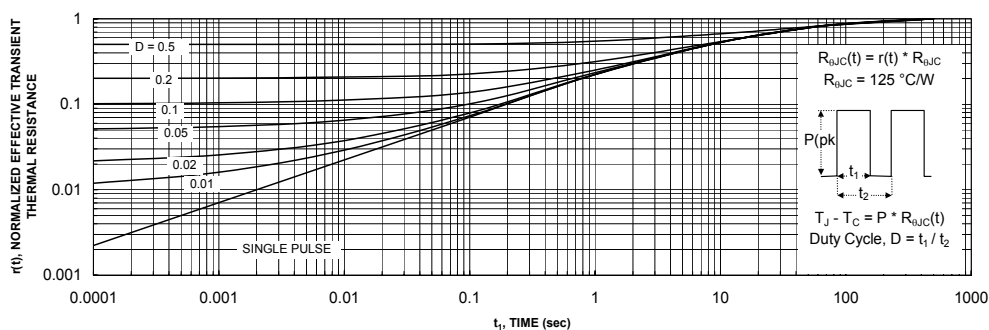


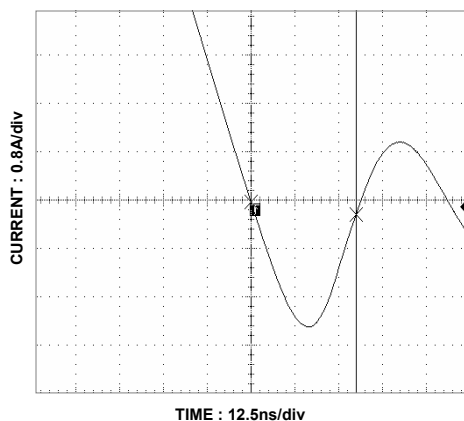
Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c.  
Transient thermal response will change depending on the circuit board design.

## Typical Characteristics (continued)

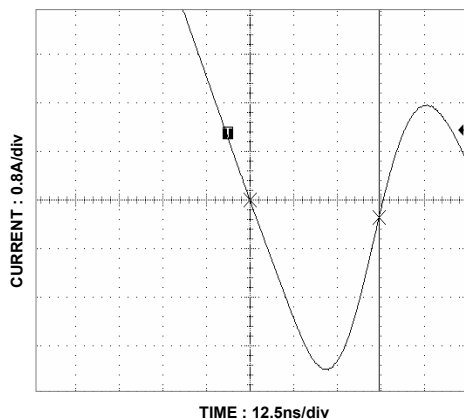
### SyncFET Schottky Body Diode Characteristics

Fairchild's SyncFET process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 12 shows the reverse recovery characteristic of the FDS6688S.



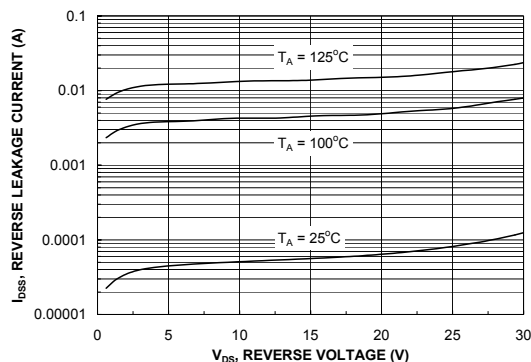
**Figure 12. FDS6688S SyncFET body diode reverse recovery characteristic.**

For comparison purposes, Figure 13 shows the reverse recovery characteristics of the body diode of an equivalent size MOSFET produced without SyncFET (FDS6688).



**Figure 13. Non-SyncFET (FDS6688) body diode reverse recovery characteristic.**

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.



**Figure 14. SyncFET body diode reverse leakage versus drain-source voltage and temperature.**

## TRADEMARKS

The following are registered and unregistered trademarks and service marks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

|                          |                      |                            |                      |
|--------------------------|----------------------|----------------------------|----------------------|
| ACEx®                    | Green FPS™           | Power247®                  | SuperSOT™-8          |
| Build it Now™            | Green FPS™ e-Series™ | POWEREDGE®                 | SyncFET™             |
| CorePLUS™                | GTO™                 | Power-SPM™                 | The Power Franchise® |
| CROSSVOLT™               | i-Lo™                | PowerTrench®               | the Power franchise  |
| CTL™                     | IntelliMAX™          | Programmable Active Droop™ | TinyBoost™           |
| Current Transfer Logic™  | ISOPANAR™            | QFET®                      | TinyBuck™            |
| EcoSPARK®                | MegaBuck™            | QS™                        | TinyLogic®           |
| F <sup>®</sup>           | MICROCOUPLER™        | QT Optoelectronics™        | TINYOPTO™            |
| Fairchild®               | MicroFET™            | Quiet Series™              | TinyPower™           |
| Fairchild Semiconductor® | MicroPak™            | RapidConfigure™            | TinyPWM™             |
| FACT Quiet Series™       | MillerDrive™         | SMART START™               | TinyWire™            |
| FACT®                    | Motion-SPM™          | SPM®                       | μSerDes™             |
| FAST®                    | OPTOLOGIC®           | STEALTH™                   | UHC®                 |
| FastvCore™               | OPTOPLANAR®          | SuperFET™                  | UniFET™              |
| FPS™                     | ®                    | SuperSOT™-3                | VCX™                 |
| FRFET®                   | PDP-SPM™             | SuperSOT™-6                |                      |
| Global Power ResourceSM  | Power220®            |                            |                      |

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

| Datasheet Identification | Product Status         | Definition                                                                                                                                                                                               |
|--------------------------|------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Advance Information      | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.                                                                       |
| Preliminary              | First Production       | This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design. |
| No Identification Needed | Full Production        | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.                                                   |
| Obsolete                 | Not In Production      | This datasheet contains specifications on a product that has been discontinued by Fairchild Semiconductor. The datasheet is printed for reference information only.                                      |



**Стандарт  
Электрон  
Связь**

Мы молодая и активно развивающаяся компания в области поставок электронных компонентов. Мы поставляем электронные компоненты отечественного и импортного производства напрямую от производителей и с крупнейших складов мира.

Благодаря сотрудничеству с мировыми поставщиками мы осуществляем комплексные и плановые поставки широчайшего спектра электронных компонентов.

Собственная эффективная логистика и склад в обеспечивает надежную поставку продукции в точно указанные сроки по всей России.

Мы осуществляем техническую поддержку нашим клиентам и предпродажную проверку качества продукции. На все поставляемые продукты мы предоставляем гарантию .

Осуществляем поставки продукции под контролем ВП МО РФ на предприятия военно-промышленного комплекса России , а также работаем в рамках 275 ФЗ с открытием отдельных счетов в уполномоченном банке. Система менеджмента качества компании соответствует требованиям ГОСТ ISO 9001.

Минимальные сроки поставки, гибкие цены, неограниченный ассортимент и индивидуальный подход к клиентам являются основой для выстраивания долгосрочного и эффективного сотрудничества с предприятиями радиоэлектронной промышленности, предприятиями ВПК и научно-исследовательскими институтами России.

С нами вы становитесь еще успешнее!

**Наши контакты:**

**Телефон:** +7 812 627 14 35

**Электронная почта:** [sales@st-electron.ru](mailto:sales@st-electron.ru)

**Адрес:** 198099, Санкт-Петербург,  
Промышленная ул, дом № 19, литера Н,  
помещение 100-Н Офис 331