## Digital Counter (DIN $72 \times 72$ ) <br> H7AN

## A DIN $72 \times 72$ mm Best-selling Counter

- Easy setting with thumbwheel switches.
- A draw-out construction enables maintenance without rewiring.
- Models with 2-, 4-, 6-, or 8-digit displays are also available.
- Total Counter models are also Available.
- Complies with UL and CSA Marking.

Refer to Safety Precautions for All Counters and Safety Precautions on page 17.


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## Ordering Information

Each model is sold together with a mounting bracket.

## Preset Counter



## Totalizing Counter




## Specifications

## Preset Counters

## Incrementing/Decrementing Counters

| Operating method | Incrementing and decrementing (selectable with DIP switch) |  |
| :--- | :--- | :--- |
| Mounting method | Flush mounting |  |
| Operation modes | N, F, C, R, K, P, Q (selectable with rotary DIP switch) |  |
| Input signal method (Count and reset <br> inputs) | Contact and solid-state input voltage (H and L) |  |
| Control output | 1-stage counters: Contact (SPDT) and solid-state output (H and L output switchable) <br> 2-stage counters: Contact (SPST-NO) and solid-state output (H and L output switchable) |  |
| Set value read | Continuous mode |  |
| Memory backup | No | Yes/No (Selectable using DIP switch) |
| Display | Yes (10-mm high 7-segment LED, Up indicator) | Yes (10-mm high 7-segment LED, Up indicator) |
| Models | 2 digits | 1 stage |
|  | 4 digits | H7AN-2D |
|  | 1 stage | H7AN-4D |

## Reversible Counters

| Operating method |  |  | Reversible (selectable with rotary DIP switch) between 0 and the set value Incrementing/decrementing A/D (command inputs) Incrementing/decrementing B/E (individual inputs) Incrementing/decrementing C/F (phase difference inputs) |  |
| :---: | :---: | :---: | :---: | :---: |
| Mounting method |  |  | Flush mounting |  |
| Operation modes |  |  | N, F, C, R, K, P, Q (selectable with rotary DIP switch) |  |
| Input signal method (Count, reset input) |  |  | Contact and solid-state input voltage (H and L) |  |
| Control output |  |  | 1-stage counters: Contact (SPDT) and solid-state output (H and L output switchable) 2-stage counters: Contact (SPST-NO) and solid-state output (H and L output switchable) |  |
| Set value read |  |  | Continuous mode |  |
| Memory backup |  |  | No | Yes/No (Selectable using |
| Display |  |  | Yes (10-mm high 7-segment LED) |  |
| Models | 2 digits | 1 stage | H7AN-E2D | H7AN-E2DM |
|  | 4 digits | 1 stage | H7AN-E4D | H7AN-E4DM |
|  |  | 2 stages | H7AN-WE4D | H7AN-WE4DM |

## Incrementing, Decrementing, and Reversible Counters

| Operating method |  |  | Incrementing, decrementing, and reversible (UP/DOWN A to F) (selectable with rotary DIP switch) |  |
| :---: | :---: | :---: | :---: | :---: |
| Mounting method |  |  | Flush mounting |  |
| Operation modes |  |  | N, F, C, R, K, P, Q (selectable with rotary DIP switch) |  |
| Input signal method (Count, reset input) |  |  | Contact and solid-state input voltage (H and L) |  |
| Control output |  |  | 1-stage counters: Contact (SPDT) and solid-state output (H and L output switchable) 2-stage counters: Contact (SPST-NO) and solid-state output (H and L output switchable) |  |
| Set value read |  |  | Continuous mode, Reset mode (selectable) |  |
| Memory backup |  |  | No | Yes/No (Selectab |
| Display |  |  | Yes (8-mm high 7-segment LED, Up indicator) |  |
| Models | 6 digits | 1 stage | H7AN-R6D | H7AN-R6DM |
|  |  | 2 stage | H7AN-RW6D | H7AN-RW6DM |
|  | 8 digits | 1 stages | H7AN-R8D | H7AN-R8DM |

## Totalizing Counters

## Incrementing/Decrementing Counters

| Operating method | Incrementing and decrementing (selectable with DIP switch) |  |
| :--- | :--- | :--- |
| Mounting method | Flush mounting |  |
| Input signal method (Count, reset input) | Contact and solid-state input voltage (H and L) |  |
| Memory backup | No | Yes/No (Selectable using DIP switch) |
| Display | Yes (10-mm high 7-segment LED) |  |
| Models | H7AN-T4 | H7AN-T4M |

## Reversible Counters

| Operating method | Reversible (selectable with rotary DIP switch) between 0 and the full scale <br> Incrementing/decrementing A/D (command inputs) <br> Incrementing/decrementing B/E (individual inputs) <br> Incrementing/decrementing C/F (phase difference inputs) |  |
| :--- | :--- | :--- |
| Mounting method | Flush mounting |  |
| Input signal method (Count, reset input) | Contact and solid-state input voltage (H and L) |  |
| Memory backup | No | Yes/No (Selectable using DIP switch) |
| Display | Yes (10-mm high 7-segment LED) |  |
| Models | H7AN-ET4 | H7AN-ET4M |

## Incrementing, Decrementing, and Reversible Counters

| Operating method | Incrementing, decrementing, and reversible (UP/DOWN A to F) |  |  |
| :--- | :--- | :--- | :--- |
| Mounting method | Flush mounting |  |  |
| Input signal method (Count, reset input) | Contact and solid-state input voltage (H and L) |  |  |
| Memory backup | No |  |  |
| Display | Yes (8-mm high 7-segment LED) |  |  |
| Models | 6 digits | H7AN-RT6 | H7AN-RT6M |
|  | H7AN-RT8 | H7AN-RT8M |  |

## Ratings

| Rated supply voltage | 100 to 240 VAC, $50 / 60 \mathrm{~Hz}$ (common use); 12 to 24, 48, 100 VDC* |
| :---: | :---: |
| Operating voltage range | 85\% to $110 \%$ of rated voltage |
| Power consumption | Approx. $10 \mathrm{VA}(240 \mathrm{VAC} \text { at } 50 \mathrm{~Hz} \text { ); Approx. } 5 \mathrm{~W} \text { (at } 24 \mathrm{VDC})^{* *}$ |
| Max. counting speed of CP1 and CP2 | 2-digit counters: 30 Hz <br> 4-digit counters: 30 Hz or 5 kHz <br> 6- or 8-digit counters: 30 Hz or 5 kHz <br> Minimum signal width (with ON/OFF ratio of 1:1): $30 \mathrm{~Hz}: 16.7 \mathrm{~ms}, 5 \mathrm{kHz}: 0.1 \mathrm{~ms}$ H: 4.5 to 30 VDC, L: 0 to 2 VDC |
| Reset | Power supply reset (except for H7AN Counter with suffix "-M"): <br> Minimum power-OFF time: 0.5 s with a reset time of 0.05 s after power-ON. <br> External, manual, reset signal time: 0.02 s <br> Reset time after completion of reset signal: 0.05 s <br> Automatic reset*** |
| Control output | Contacts: 3 A at 250 VAC, resistive load $(\cos \phi=1)$ No-contacts: 100 mA max. at 30 VDC max., open collector |
| Min. applicable load | 10 mA at 5 VDC (p level reference value) |
| External power supply | $80 \mathrm{~mA}, 12 \mathrm{VDC} \pm 10 \%$ (contains 5\% ripple max.) |
| Ambient temperature | Operating: $-10^{\circ} \mathrm{C}$ to $55^{\circ} \mathrm{C}$ (with no icing) Storage: $-25^{\circ} \mathrm{C}$ to $65^{\circ} \mathrm{C}$ (with no icing) |
| Ambient humidity | 35\% to 85\% |
| Case | Light gray (Munsell 5Y7/1) |

*The ripple is 20\% max.
**There is an inrush current of 14 A at 240 VAC for approximately $0.6 \mathrm{~ms}, 15 \mathrm{~A}$ at 12 to 24 VDC for $2 \mathrm{~ms}, 5 \mathrm{~A}$ at 48 VDC for 3 ms , or 8 A at 100 VDC for 2 ms immediately after power-ON.
***Only preset counters can be automatically reset.

## Characteristics

| Insulation resistance | 100 MW min. (at 500 VDC$)$ (between current-carrying terminal and exposed non-current-carrying metal parts, and <br> between non-continuous contacts) |
| :--- | :--- |
| Dielectric strength | $2,000 \mathrm{VAC}, 50 / 60 \mathrm{~Hz}$ for 1 min (between current-carrying terminal and exposed non-current carrying metal parts) <br> $750 \mathrm{VAC}, 50 / 60 \mathrm{~Hz}$ for 1 min (between non-continuous contacts) |
| Impulse withstand voltage | 6 kV (between power terminals), 6 kV (between current-carrying terminal and exposed non-current-carrying metal <br> parts) |
| Noise immunity | $\pm 2 \mathrm{kV}$ (between power terminals) and $\pm 500 \mathrm{~V}$ (between input terminals), square-wave noise by noise simulator |
| Vibration resistance | Destruction:10 to $55 \mathrm{~Hz}, 0.75-\mathrm{mm}$ double amplitude <br> Malfunction:10 to $55 \mathrm{~Hz}, 0.5-\mathrm{mm}$ double amplitude |
| Shock resistance | Destruction: $300 \mathrm{~m} / \mathrm{s}^{2}$ <br> Malfunction: $100 \mathrm{~m} / \mathrm{s}^{2}$ |
| Life expectancy | Mechanical: $10,000,000$ operations min. <br> Electrical: 100,000 operations min. (3 A at 250 VAC, resistive load) |
| Approved standards | UL508, CSA C22.2 No.14 |
| Weight | Approx. 360 g |

## Engineering Data

## Life of Contacts

## Electric Life Expectancy (Resistive load)



Electric Life Expectancy (Inductive load)


Reference:
0.15 A max. can be switched 100,000
times at 125 VDC $(\cos \phi=1)$.
0.1 A max. can be switched 100,000
times when L/R $=7 \mathrm{~ms}$.

## Operation

## Count Operation

## Preset Counters

| Incrementing/Decrementing selectable mode | Reversible mode |
| :---: | :---: |
| Incrementing mode | Incrementing/Decrementing A, B, C (incrementing) mode |
| Decrementing mode | Incrementing/Decrementing D, E, F (decrementing) mode |

Note: Two-stage counters, set the counters so that the interval between 1 st and 2 nd count out will be more than 5 ms . For Incrementing/ Decrementing switchable counters, only the 2 nd value will be effective if the 1st value is larger than the $2 n d$ value.
The Incrementing/Decrementing Counters give outputs in the following order; 1st to 2nd to 1st to 2nd.

## Totalizing Counters

| Incrementing/Decrementing selectable mode | Reversible mode |
| :---: | :---: | :---: |
| Incrementing mode <br> Reset | Incrementing/Decrementing A, B, C (decrementing) mode |
| Full scale* |  |

[^0]* The full scale value is 9999 for the 4 -digit counters, 999999 for the 6-digit counters, and 99999999 for the 8 -digit counters.


## *Output Timing Charts



## Output Delays

| Control output | Max. counting speed |  | Output delay |  |
| :--- | :--- | :--- | :--- | :---: |
|  |  | 2-, 4-digit counters | 6-, 8-digit counters |  |
| Contact output | 30 Hz | 14.0 to 16.0 ms | 14.0 to 18.0 ms |  |
|  | 5 kHz | 6.0 to 8.0 ms | 6.0 to 8.0 ms |  |
| Solid-state output | 30 Hz | 8.0 to 10.0 ms | 9.5 to 12.0 ms |  |
|  | 5 kHz | 0.4 to 0.6 ms | 0.3 to 0.5 ms |  |

## Input Mode Setting

## Incrementing/Decrementing Selectable Mode

Note: The width of (A) must be the same as or lager than the minimum signal width, because an error of $\pm 1$ count may occur if the width of $(A)$ is smaller than the minimum signal width.

| Incrementing mode | Decrementing mode |
| :---: | :---: |
| CP1: Count input; CP2: prohibit (gate) input | CP1: Count input; CP2: prohibit (gate) input |
| CP1: Prohibit (gate) input; CP2: count input | CP1: Prohibit (gate) input; CP2: count input |

## Reversible Mode

Note: 1. A: Minimum signal width; $B$ : Must be at least $1 / 2$ of minimum signal width. An error of $\pm 1$ count may occur if the width of $(A)$ and (B) are smaller than the minimum signal width.
2. Set the same counting speed for CP1 and CP2 when in Up/Down C, or F mode.

| Incrementing mode | Decrementing mode |
| :---: | :---: |
| Incrementing/Decrementing A command input mode | Incrementing/Decrementing D command input mode |
| Incrementing/Decrementing B individual input mode | Incrementing/Decrementing E individual input mode |
| Incrementing/Decrementing C phase difference input mode | Incrementing/Decrementing F phase difference input mode |

## ■ Output Modes

Incrementing, decrementing, or reversible


Note: 1. In the $C, K, P$, and $Q$ modes, the counters must not count out again while the one-shot timer is working.
2. In the $C$ mode, the present value is placed in reset start status as soon as the preset count is reached and the count-out status is not displayed.

| Output mode | Incrementing, Incrementing/Decrementing A, B, C | Decrementing, Incrementing/Decrementing D, E, F |  |
| :---: | :---: | :---: | :---: |
| N |  | Reset 2nd Display 1st 0 1st output 2nd output |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  |  |
|  |  |  | C. |


| Output mode | Incrementing, Incrementing/Decrementing A, B, C | Decrementing, Incrementing/Decrementing D, E, F |
| :---: | :---: | :---: |
| F |  |  |
| C |  |  |
| R |  |  |
| K |  |  |
| P |  |  |
| Q |  |  |

Nomenclature

## Nomenclature

## Preset Counter

H7AN－2D，－2DM
H7AN－E2D，－E2DM


## Arrangement and Functions of Specification Selection Switches



SW2：（A）Operating mode selector
SW3：Power failure memory backup －$\uparrow$ Memory backup （See note．） No memory backup
SW4－1：Output phase selector for transistor output section
$\square \uparrow \stackrel{\mathrm{L}}{\mathrm{L} \rightarrow \mathrm{H} \text {（at count－out）}}$ （See note．） $\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Manual reset selector
$\square \uparrow$ Manual reset possible （See note．） Manual reset not possible SW4－3：UP／DOWN selector
$\square \uparrow$ Up counting（incrementing）
（See note．） Down counting （decrementing）


SW1：（A）Counting function selector SW2：（A）Operating mode selector SW4－1：Output phase selector for transistor output section －$\uparrow \stackrel{\mathrm{L} \rightarrow \mathrm{H} \text {（at count－out）}) ~}{\text { and }}$
（See note．） $\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Manual reset selector （ $\uparrow$ Manual reset possible $\square$（See note．）

Manual reset not possible


SW1：（A）Counting function selector SW2：（A）Operating mode selector SW3：Power failure memory backup －$\uparrow$ Memory backup
－（See note．）
No memory backup
SW4－1：Output phase selector for transistor output section
$\square \uparrow \stackrel{L}{\square} \mathrm{H}$（at count－out）
（See note．）
$\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Manual reset selector －$\uparrow$ Manual reset possible （See note．）
Manual reset not possible

H7AN－4D，－4DM
H7AN－E4D，－E4DM


## Arrangement and Functions of Specification Selection Switches



SW2：（A）Operating mode selector SW3－1：CP1 maximum counting speed selector
回 30 Hz （See note．） 5 kHz
SW3－2：CP2 maximum counting speed selector
I 30 Hz （See note．） $\downarrow 5 \mathrm{kHz}$
SW4－1：Output phase selector for transistor output section
－$\uparrow \mathrm{L} \rightarrow \mathrm{H}$（at count－out）
（See note．） $\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Manual reset selector
回 Manual reset possible
（See note．）
Manual reset not possible
SW4－3：UP／DOWN selector
－Up counting（incrementing）
（See note．） Down counting （decrementing）

## H7AN－4DM



SW2：（A）Operating mode selector
SW3－1：CP1 maximum counting speed selector
$\square \rrbracket^{30 \mathrm{Hzz}} 5 \mathrm{kHz}$（See note．）
SW3－2：CP2 maximum counting speed selector
－${ }_{5}^{30 \mathrm{Hzz} \text {（See note．）}) ~}$
W3－3：SW3－3：Power failure memory backup －$\uparrow$ Memory backup（See note．） $\downarrow$ No memory backup
SW4－1：Output phase selector for transistor output section
$\square \uparrow L \rightarrow H$（at count－out）（See note．）
SW4－2：Manual reset
－$\uparrow$ Manual reset possible
（See note．）
Manual reset not possible
SW4－3：UP／DOWN selector
$\square \downarrow$ Up counting（incrementing）
Down counting（decrementing）


SW1：（A）Counting function selector SW2：（A）Operating mode selector SW3－1：CP1 maximum counting speed selector
回 30 Hz （See note．）
$\downarrow 5 \mathrm{kHz}$
SW3－2：CP2 maximum counting speed selector ［ ${ }^{30 \mathrm{~Hz} \text {（See note．）}) ~}$ $\downarrow 5 \mathrm{kHz}$
SW4－1：Output phase selector for transistor output section目 $\mathrm{L} \rightarrow \mathrm{H}$（at count－out） $\downarrow$（See note．） $\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Manual reset selector
［ Manual reset possible
（See note．）
Manual reset not possible


SW1：（A）Counting function selector SW2：（A）Operating mode selector SW3－1：CP1 maximum counting speed selector
回 ${ }^{30 \mathrm{~Hz} \text {（See note．）}) ~}$
$\downarrow$ kHz
SW3－2：CP2 maximum counting speed selector
回 30 Hz （See note．）
SW3－3：Power failure memory backup回 Memory backup （See note．） No memory backup
SW4－1：Output phase selector for transistor output section －$\uparrow \mathrm{L} \rightarrow \mathrm{H}$（at count－out） $\downarrow$（See note．） $\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Manual reset selector －$\uparrow$ Manual reset possible （See note．） Manual reset not possible

Note：These settings are the defaults for the specification selection switches．

H7AN－W4D，－W4DM
H7AN－WE4D，－WE4DM


H7AN－R6D，－R6DM
H7AN－R8D，－R8DM


Arrangement and Functions of Specification Selection Switches


SW2：（B）Operating mode selector SW3－1：CP1 maximum counting speed selector
$\square \downarrow_{5 \mathrm{kHz}}^{30 \mathrm{~Hz} \text {（See note．）}}$
SW3－2：CP2 maximum counting speed selector

$$
\text { 目 } 30 \mathrm{~Hz} \text { (See note.) }
$$

SW4－1：Output phase selector for first transistor output section －$\uparrow ~ L \rightarrow H$（at count－out） （See note．） $\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Output phase selector for second transistor output section －$\uparrow ~ L \rightarrow H$（at count－out） $\downarrow$（See note．）
$\xrightarrow{\mathrm{H} \rightarrow \mathrm{L}} \mathrm{L}$（at count－out）
SW4－3：Manual reset selector $\square$ Manual reset possible －（See note．） Manual reset not possible
SW4－4：UP／DOWN selector
－$\downarrow$ Up counting（incrementing）
（See note．）
Down counting（decrementing）


SW1：（A）Counting function selector
SW2：（B）Operating mode selector
SW3－1：CP1 maximum counting
speed selector
回 30 Hz （See note．）
$\downarrow 5 \mathrm{kHz}$
SW3－2：CP2 maximum counting speed selector
$\square \rrbracket_{5 \mathrm{kHz}}^{30 \mathrm{~Hz} \text {（See note．）}}$
SW4－1：Output phase selector for first transistor output section
目 $\mathrm{L} \rightarrow \mathrm{H}$（at count－out）
$\downarrow$（See note．）
$\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Output phase selector for second transistor output section －$\uparrow \mathrm{L} \rightarrow \mathrm{H}$（at count－out） （See note．）
$\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－3：Manual reset selector
回 Manual reset possible （See note．） Manual reset not possible


SW1：（A）Counting function selector
SW2：（B）Operating mode selector SW3－1：CP1 maximum counting speed selector
$\square{ }_{\square} 30 \mathrm{kHz}$（See note．）
SW3－2：CP2 maximum counting speed selector
－$\downarrow=30 \mathrm{kHz}$
SW3－3：Power failure memory backup
$\square \uparrow \begin{aligned} & \text { Memory backup } \\ & \text {（See note．）}\end{aligned}$ （See note．）
SW4－1：Output phase selector first transistor output section
目 $\mathrm{L} \rightarrow \mathrm{H}$（at count－out）
（See note．）
$\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Output phase selector for second transistor output section
D $\mathrm{L} \rightarrow \mathrm{H}$（at count－out）
（See note．）
$\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－3：Manual reset selector
$\square \downarrow \begin{aligned} & \text { Manual reset possible } \\ & \text {（See note．）}\end{aligned}$ （See note．） Manual reset not possible

## Arrangement and Functions of Specification Selection Switches



SW1：（B）Counting function selector SW2：（A）Operating mode selector SW3－1：CP1 maximum counting
speed selector
回 30 Hz （See note．）
SW3－2：CP2 maximum counting speed selector
D 30 Hz （See note．）
SW4－1：Output phase selector for transistor output section
回 $\mathrm{L} \rightarrow \mathrm{H}$（at count－out）
$\downarrow$（See note．）
$\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Set value read selector ［1］Always read（See note．） $\downarrow$ Read only at reset
SW4－3：Manual reset selector回 Manual reset possible $\downarrow$（See note．） Manual reset not possible


SW1：（B）Counting function selector
SW2：（A）Operating mode selector
SW3－1：CP1 maximum counting
speed selector
回 30 Hz （See note．）
SW3－2：CP2 maximum counting speed selector

$\downarrow 5 \mathrm{kHz}$
SW3－3：Power failure memory backup
（－Memory backup
$\downarrow$（See note．）
No memory backup

SW4－1：Output phase selector for transistor output section
回 $\mathrm{L} \rightarrow \mathrm{H}$（at count－out）
$\downarrow$（See note．） $\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－2：Set value read selector
（1）Always read（See note．）
$\downarrow$ Read only at reset
SW4－3：Manual reset selector －$\downarrow$ Manual reset possible
$\downarrow$（See note．） Manual reset not possible

Note：These settings are the defaults for the specification selection switches．


## Totalizing Counter

H7AN－T4，－T4M，－ET4，－ET4M
H7AN－RT6，－RT6M，－RT8，－RT8M


Arrangement and Functions of Specification Selection Switches


SW1：（B）Counting function selector
SW2：（B）Operating mode selector
SW3－1：CP1 maximum counting speed selector
$\square \downarrow 50 \mathrm{kHz}$（See note 1．）
$\downarrow 5 \mathrm{kHz}$
SW3－2：SW3－2：CP2 maximum counting speed selector －$\uparrow$ 30 Hz（See note 1．）
SW4－1：Output phase selector for first transistor output section －$\uparrow \mathrm{L} \rightarrow \mathrm{H}$（at count－out） $\square \downarrow($ See note 1.$)$
SW4－2：Output phase selector for
sW4－2：Output phase selector for
second transistor output section $\square \uparrow \mathrm{L} \rightarrow \mathrm{H}$（at count－out） $\downarrow$（See note 1．）
$\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－3：Set value read selector
$\square \begin{aligned} & \text { Always read（See n } \\ & \text { Read only at reset }\end{aligned}$
SW4－4：Manual reset selector $\square \uparrow$ Manual reset possible （See note 1．） Manual reset not possible

## H7AN－RW6DM



SW1：（B）Counting function selector SW2：（B）Operating mode selector
SW3－1：CP1 maximum counting speed selector
－ 30 Hz （See note 1．）
SW3－2：CP2 maximum counting speed selector
If 30 Hz （See note 1．）
$\square 5 \mathrm{kHz}$
SW3－3：Power failure memory backup
$\square \uparrow$ Memory backup（See note 1．） $\downarrow$ No memory backup
SW4－1：Output phase selector for first transistor output section $\square \uparrow \mathrm{L} \rightarrow \mathrm{H}$（at count－out） H $\rightarrow$（at coun
SW4－2．Output phase selector for second transistor output section
$\square \uparrow \mathrm{L} \rightarrow \mathrm{H}$（at count－out） （See note 1．）
$\mathrm{H} \rightarrow \mathrm{L}$（at count－out）
SW4－3：Set value read selector
$\square \uparrow$ Always read（See note 1．）
W4－4：Manual
$\square \uparrow$ Manual reset possible $\downarrow$（See note 1．） Manual reset not possible

Arrangement and Functions of Specification Selection Switches


T $\uparrow$ Up counting（incrementing）
$\downarrow$（See note 1．） Down counting （decrementing）


SW3－1：CP1 maximum counting speed selector
回 $\uparrow=3 \mathrm{kHz}$（See note 1．）
SW3－2：CP2 maximum counting speed selector
回 $\uparrow 30 \mathrm{~Hz}$（See note 1．）
$\downarrow 5 \mathrm{kHz}$
SW3－3：Power failure memory backup $\square \uparrow$ Memory backup（See note 1．） $\downarrow$ No memory backup
SW4－1：Manual reset selector
$\square \uparrow$ Manual reset possible
$\downarrow$（See note 1．）
Manual reset not possible
SW4－2：UP／DOWN selector
$\square$ Up counting（incrementing）
Down counting（decrementing）

SW3－1：CP1 maximum counting
speed selector

## 偖 ${ }^{30 \mathrm{~Hz} \text {（See note } 1 .) ~}$

 $\downarrow 5 \mathrm{kHz}$SW3－2：CP2 maximum counting speed selector

## － 30 Hz （See note 1．） $\downarrow 5 \mathrm{kHz}$

SW4：Manual reset selector （1）Manual reset possible （See note 1．） Manual reset not possible


SW1：（A）Counting function selector
SW3－1：CP1 maximum counting
speed selector
回 30 Hz （See note 1．）
SW3－2：CP2 maximum counting speed selector － 430 Hz （See note 1．） $\downarrow 5 \mathrm{kHz}$
SW3－3：Power failure memory backup ㅁ $\uparrow$ Memory backup（See note 1．）

No memory backup
SW4：Manual reset selector
ㄱ Manual reset possible
（See note 1．）
Manual reset not possible


SW1：（B）Counting function selector
SW3－1：CP1 maximum counting
speed selector
回 130 Hz （See note 1．） $\downarrow 5 \mathrm{kHz}$
SW3－2：CP2 maximum counting speed selector
－$\downarrow{ }_{5 \mathrm{kHz}}^{30 \mathrm{~Hz} \text {（See note 1．）}}$
SW4：Manual reset selector
$\square$ Manual reset possible
$\downarrow$（See note 1．） Manual reset not possible
－


SW1：（B）Counting function selector SW3－1：CP1 maximum counting
speed selector
回 30 Hz （See note 1．）
5 kHz
SW3－2：CP2 maximum counting speed selector
－ 130 Hz （See note 1．）
SW3－3：Power failure memory backup （－Memory backup（See note 1．）
$\downarrow$ No memory backup
SW4：Manual reset selector
－ 1 Manual reset possible
$\downarrow$（See note 1．）
Manual reset not possible

Note：1．These settings are the defaults for the specification selection switches．
2．Specifications selected using the internal specification selection switches become after switching once a reset has been performed（e．g．，power supply reset，external reset，or manual reset，but not automatic reset）．
If a reset is not performed，operation will continue with the specifications before switching．

SW1 (A) Counting function selector

| Switch <br> position | Function |
| :--- | :--- |
| $0,1,8,9$ <br> (See note.) | Up/Down A (command inputs) |
| 2 | Up/Down B (individual inputs) |
| 3 | Up/Down C (differential phase inputs) |
| 4,5 | Up/Down D (command inputs) |
| 6 | Up/Down E (individual inputs) |
| 7 | Up/Down F (differential phase inputs) |

Note: These settings are the defaults.

## SW1 (B) Counting function selector

| Switch <br> position | Function |
| :--- | :--- |
| 0,1 | Up/Down A (command inputs) |
| 2 | Up/Down B (individual inputs) |
| 3 | Up/Down C (differential phase inputs) |
| 4,5 | Up/Down D (command inputs) |
| 6 | Up/Down E (individual inputs) |
| 7 | Up/Down F (differential phase inputs) |
| 8 | DOWN |
| 9 (See note.) | UP |

Note: These settings are the defaults.

SW2 (A) Operating mode selector (1-stage preset models)

| Switch <br> position | Function |
| :--- | :--- |
| 0, 7, 8, F <br> (See note.) | N (count stop, output hold) |
| 1, 9 | F (overcount, output hold) |
| 2, A | C (automatic reset, one-shot output) |
| 3, B | R (automatic reset, one-shot output 1) |
| 4, C | K (overcount reset, one-shot output) |
| 5, D | P (automatic reset, one-shot output 2) |
| 6, E | Q (automatic reset, one-shot output 3) |

Note: These settings are the defaults.
SW2 (B) Operating mode selector (2-stage preset models)

| Switch position |  | Second-stage function |
| :---: | :---: | :---: |
| $\begin{aligned} & 0,7 \\ & \text { (See note.) } \end{aligned}$ | First-stage output hold | N (count stop, output hold) |
| 1 |  | F (overcount, output hold) |
| 2 |  | C (automatic reset, one-shot output) |
| 3 |  | R (automatic reset, one-shot output 1) |
| 4 |  | K (overcount reset, one-shot output) |
| 5 |  | P (automatic reset, one-shot output 2) |
| 6 |  | Q (automatic reset, one-shot output 3) |
| 8, F | First-stage one-shot output | N (count stop, output hold) |
| 9 |  | F (overcount, output hold) |
| A |  | C (automatic reset, one-shot output) |
| B |  | R (automatic reset, one-shot output 1) |
| C |  | K (overcount reset, one-shot output) |
| D |  | P (automatic reset, one-shot output 2) |
| E |  | Q (automatic reset, one-shot output 3) |

Note: These settings are the defaults.

## Dimensions

Note: All units are in millimeters unless otherwise indicated.


Note: 1. The panel cutouts for the H7AN are as shown (according to DIN437000).
2. The recommended mounting panel thickness is from 1 to 5 mm .
3. Use the mounting bracket (sold together) to mount the digital counter.

(When N units are flush-mounted horizontally.)

$(\mathrm{n}-1) \times 72+70 \mathrm{~min}$. (including a tolerance of 2 mm )

## Mounting

Mounting brackets are included with the H7AN. Use these brackets to mount the H7AN securely so that there is no play.


Turn the screws on the mounting brackets counterclockwise to loosen them sufficiently. Attach the bottom mounting bracket first.


After attaching the mounting brackets, turn the screws clockwise to tighten them sufficiently. When the screws are completely tightened, you will hear the threads disengage.

## Installation

## Terminal Arrangement

1-stage Preset Counters


2-stage Preset Counters


## Total Counters



Note: 1. The polarities of the DC power supply terminals are as follows:
Terminal 1: negative; terminal 2: positive
2. If there is excessive external noise, terminal 3 must be grounded to an appropriate place where the grounding resistance is $100 \Omega$ max. There will be a current leakage of 0.2 mA each from terminals 1 and 2 to terminal 3.
3. The open terminals cannot be used as relay terminals.
4. Insert surge absorbers between each of the power supply terminals and the ground terminal. If the ground terminal is not used, insert the surge absorbers between terminal 1 and terminal 2.

## Solid-state Contact Input Signal Levels

1. High level: 4.5 V min.
$4.7 \mathrm{k} \Omega \times \mathrm{E} /(4.7 \mathrm{k} \Omega+\mathrm{R} 1$ or R 2$)$ must satisfy the above level.E: 30 VDC max.
(12 VDC if power is supplied from the external power supply.)
2. Low level: 2 V max.


High level: Transistor: OFF


High level: Transistor: OFF


High level: PNP Transistor: ON; R3 is optional.

## Contact Inputs

For contact inputs, the contact must have a switching capacity of 2.5 mA min. at 12 V . If a $680-\mathrm{W}$ resistor ( $1 / 2 \mathrm{~W}$ ) is used for R 4 , reliability will be improved.

## Input Conditions of the H7AN

$\mathrm{H}: 5$ to 30 V
L: 0 to 2 V

High level:
Contact: ON

## Connections of Single Solid-state Inputs

The following illustrations show how to connect a single solid-state input to digital counters connected in parallel. The H7AN has an input resistance of 4.7 kW . If the number of counters is N , the total input resistance will be $4.7 / \mathrm{N} \mathrm{kW}$. In this case, the high level input signal voltage can be calculated as follows:
(4.7/N) x $\mathrm{E} /(4.7 / \mathrm{N}+\mathrm{R})$

Determine the value of $E(V)$ and $R(k W)$ so that the high level input signal voltage will be 5 to 30 VDC.


## Connections of Single Contact Inputs

The following illustrations show how to connect a single contact input to digital counters connected in parallel. If the number of digital counters is N , the total contact input current will be $2.5 \times \mathrm{N}(\mathrm{mA})$ at 12 VDC .


## Solid-state Outputs (One-stage Counters)

Load Operation when Transistor is ON
External power supply for Load

## Load Operation when Transistor is OFF

The output voltage (V out) is calculated as follows:

Connected to the input terminals of other equipment

$$
\begin{equation*}
\text { Vout }=\frac{12 R L}{1.5(\mathrm{k} \Omega)+R L}(V \tag{V}
\end{equation*}
$$


for load

## Safety Precautions

Refer to Safety Precautions for All Counters.

## $\triangle$ CAUTION

Minor injury due to electric shock may occasionally occur. Do not touch any of the terminals while power is being supplied.

Minor injury due to electric shock may occasionally occur. Do not touch the terminals for at least 1 minute after turning OFF the power supply.


Minor injury due to explosion may occasionally occur. Do not use the H7AN where subject to flammable or explosive gas.

Minor electric shock, fire, or malfunction may occasionally occur. Never attempt to disassemble, modify, or repair the H7AN or touch any of the internal parts.

If the output relay is used beyond its life expectancy, its contacts may become fused or there may be a risk of fire.
Use the output relay within its rated load and electrical life expectancy. The life expectancy of the output relay varies considerably according to its usage.

Fire may occasionally occur. Tighten the terminal screws to a torque of 0.74 to $0.90 \mathrm{~N} \cdot \mathrm{~m}$.


Do not allow metal fragments, lead wire scraps, or chips from processing during installation to fall inside the H7AN. Otherwise, minor electric shock, fire or malfunction may occasionally occur.

## Precautions for Safe Use

- The H7AN is not waterproof or oil resistant. Do not use it in locations subject to water or oil.
- The load current must be within the rated current.
- Pay careful attention to polarity to avoid wrong connections when wiring the counter input terminals and reset input terminal.

- Do not apply directly apply an external voltage to either the transistor output terminals or external power supply terminals.

- When using heaters, be sure to use a thermal switch for the load circuit.
- Use a switch, relay, or other contact device so that the power is turned ON or OFF quickly. If the power supply voltage is not reached quickly enough, the H7AN may malfunction.
- Use the specified wires for wiring.

Applicable Wires:
AWG24 to AWG18 (cross-sectional area of 0.205 to $0.823 \mathrm{~mm}^{2}$ )

- Do not connect more than two crimp terminals to the same terminal.
- Do not connect anything to unused terminals.
- Leaving the H7AN with outputs ON at a high temperature for a long time may hasten the degradation of internal parts (such as electrolytic capacitors). Therefore, use the product in combination with relays and avoid leaving the product with the output turned ON for a long period of time (e.g., a month or more).
- The set value of the Preset Counter can be changed at any time during operation and the new set value will be used immediately. The output will turn ON whenever the set value equals the current count, so change the set value with care. (If a setting switch is accidentally touched during operation, the set value will be changed and the new set value will be used for operation. Always leave the front panel cover closed unless you are changing a setting.)
- When removing the Counter body from the case, never touch the terminals or electronic components with your hands or subject them to shock. When inserting the body, do not allow electronic components to come in contact with the case.
- Static electricity may destroy internal components. When removing the Counter body from the case, do not touch an electronic components other than the setting switches with your hands.
- When changing the set value during operation, do not leave a switch set so that two numbers are visible at the same time. The set value will not be accurate. Push the buttons on the thumbwheel switches firmly.


## Precautions for Correct Use

- Inrush current of approximately 23 A will flow for a short period of time when the power supply is turned ON. If the capacity of the power supply is insufficient, the H7AN may not start. Use a power supply, breakers, an contacts with sufficient capacity.
- After turning ON the power supply, 50 ms is required for the internal circuits to reach the operating voltage. Operation for input signals may not be correctly during this time.
- After turning OFF the power supply, 50 ms is required for voltage in the internal circuits to drop. Operation may be performed for input signals during this time.

- Operation after the power supply is interrupted with be as shown below for models without data backup and for models that back up data for power interruptions (models with model numbers ending in "-M").


Note: Use a model with a backup function (models ending in "-M") to maintain the settings even for power interruptions. Be sure to enable the backup function.

- The following timing chart shows how the H7AN indicates when there is an external or manual reset input.

- To mount the casing on the digital counter, insert the digital counter body into the casing by hand as far as possible and then tighten the mounting screw. Press by the hand the front panel as indicated by the arrow so that the screw will tighten securely.



## Retaining Data during Power Interruptions

Data is written to EEPROM when the power is turned OFF. The write life is $1,000,000$ writes min.

## Self-diagnosis

The following indications will be made when an error occurs.

| 7-segment <br> display | Reset <br> indicator | Count-out <br> indicator | Error | Output <br> status |
| :--- | :--- | :--- | :--- | :--- |
| $E!$ | OFF | OFF | CPU error | OFF |
| $E \Omega$ | OFF | OFF | RAM <br> memory <br> error | OFF |
| $E 3$ | OFF | OFF | EEPROM <br> memory <br> error | OFF |

Try correcting the error by cycling the power supply. If the indications do not change, try inputting the reset signal. If that does not work, the Counter will need to be repaired. If normal operation is recovered, it might have been caused by noise. Check for noise generation.

## Changes in Specifications

This product was upgraded in January 2006. The main changes are described below.

1. Maximum Counting Speeds

A DIP switch on models with 4,6 , or 8 digits could be used to set the maximum counting speed to $3 \mathrm{kHz}, 5 \mathrm{kHz}$, or 30 kHz . The upgraded models have been changed so the maximum counting speed can be set to 5 kHz or 30 kHz .
2. Addition of Setting to Enable/Disable Data Backup for Power Interruptions
On models that back up data when power is turned OFF (models with model numbers ending in "-M"), a DIP switch setting has been provided to enable or disable backup.
3. Changes in Functions Allocated to Function Setting Switches Some of the functions allocated to the DIP switches have been changed to allow for the changes described in 1 and 2, above.

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[^0]:    Note: The count value will return to " 0 " when it reaches the full scale limit.

