

# RJH60D7DPQ-E0

600V - 50A - IGBT  
Application: Inverter

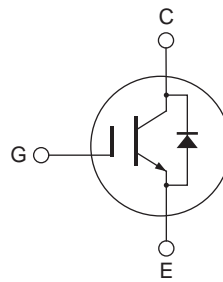
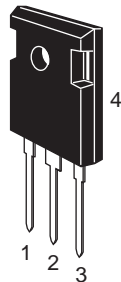
R07DS0740EJ0100  
Rev.1.00  
Apr 19, 2012

## Features

- Short circuit withstand time (5  $\mu$ s typ.)
- Low collector to emitter saturation voltage  
 $V_{CE(sat)} = 1.6$  V typ. (at  $I_C = 50$  A,  $V_{GE} = 15$  V,  $T_a = 25^\circ\text{C}$ )
- Built in fast recovery diode (100 ns typ.) in one package
- Trench gate and thin wafer technology
- High speed switching  
 $t_f = 50$  ns typ. (at  $V_{CC} = 300$  V,  $V_{GE} = 15$  V,  $I_C = 50$  A,  $R_g = 5 \Omega$ ,  $T_a = 25^\circ\text{C}$ , inductive load)

## Outline

RENESAS Package code: PRSS0003ZE-A  
(Package name: TO-247)



1. Gate
2. Collector
3. Emitter
4. Collector

## Absolute Maximum Ratings

( $T_a = 25^\circ\text{C}$ )

| Item   | Symbol                           | Ratings     | Unit                        |
|--|----------------------------------|-------------|-----------------------------|
| Collector to emitter voltage / diode reverse voltage | $V_{CES} / V_R$                  | 600         | V                           |
| Gate to emitter voltage                              | $V_{GES}$                        | $\pm 30$    | V                           |
| Collector current                                    | $T_C = 25^\circ\text{C}$         | $I_C$       | 90 A                        |
|  | $T_C = 100^\circ\text{C}$        | $I_C$       | 50 A                        |
| Collector peak current                               | $i_{c(peak)}$ <sup>Note1</sup>   | 200         | A                           |
| Collector to emitter diode forward current           | $i_{DF}$                         | 50          | A                           |
| Collector to emitter diode forward peak current      | $i_{DF(peak)}$ <sup>Note1</sup>  | 200         | A                           |
| Collector dissipation                                | $P_C$ <sup>Note2</sup>           | 300         | W                           |
| Junction to case thermal resistance (IGBT)           | $\theta_{j-c}$ <sup>Note2</sup>  | 0.42        | $^\circ\text{C} / \text{W}$ |
| Junction to case thermal resistance (Diode)          | $\theta_{j-cd}$ <sup>Note2</sup> | 1.07        | $^\circ\text{C} / \text{W}$ |
| Junction temperature                                 | $T_j$                            | 150         | $^\circ\text{C}$            |
| Storage temperature                                  | $T_{stg}$                        | -55 to +150 | $^\circ\text{C}$            |

Notes: 1.  $PW \leq 10 \mu\text{s}$ , duty cycle  $\leq 1\%$

2. Value at  $T_C = 25^\circ\text{C}$

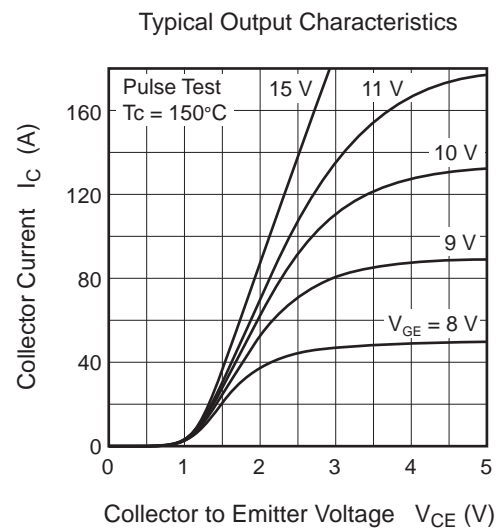
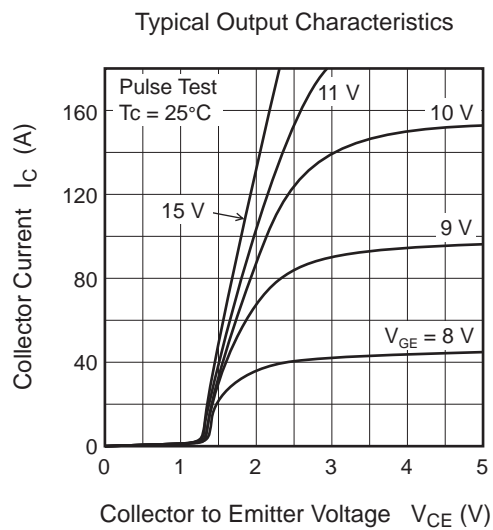
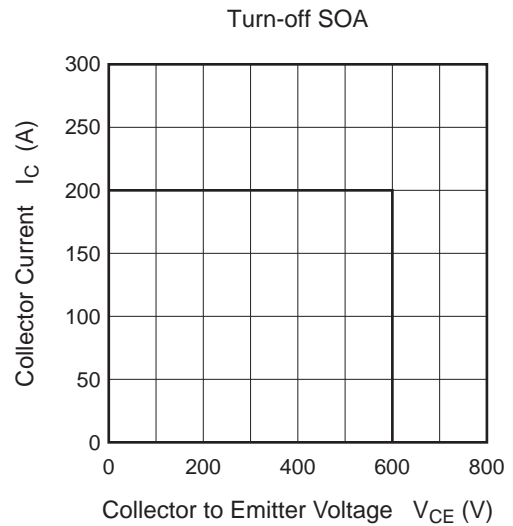
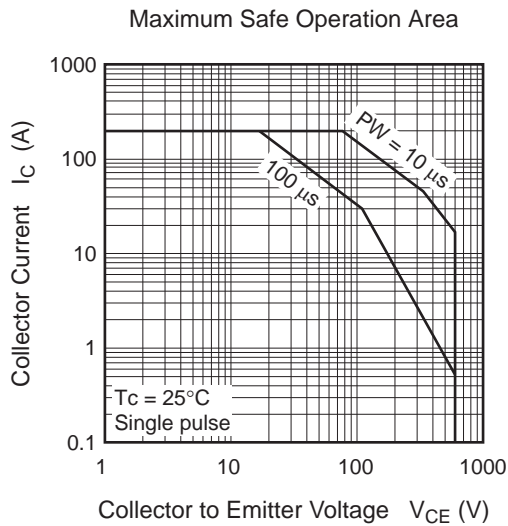
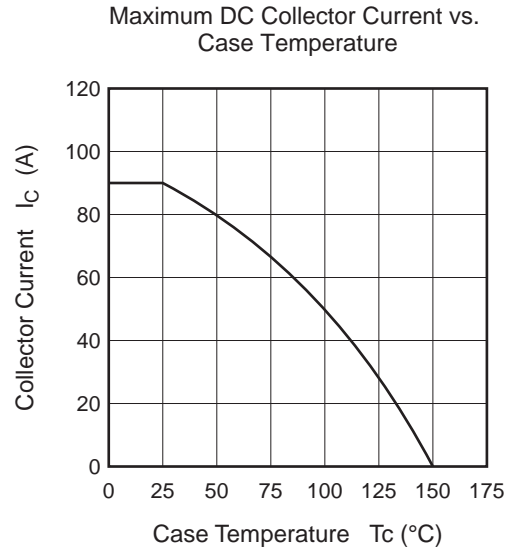
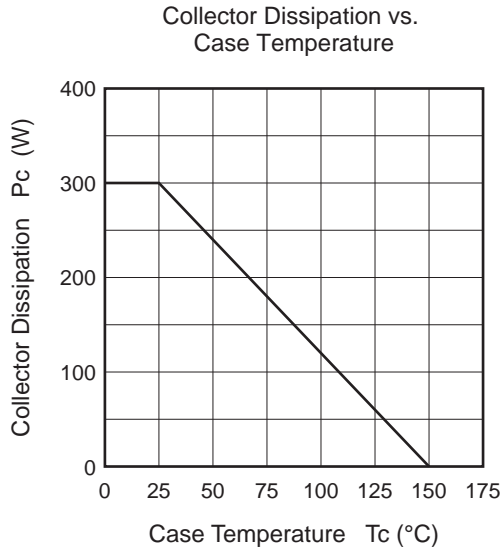
## Electrical Characteristics

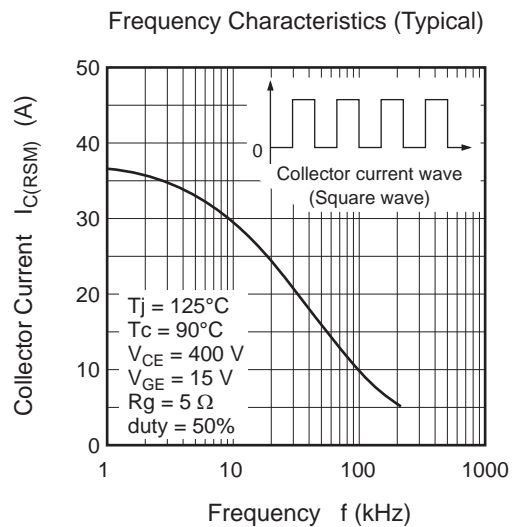
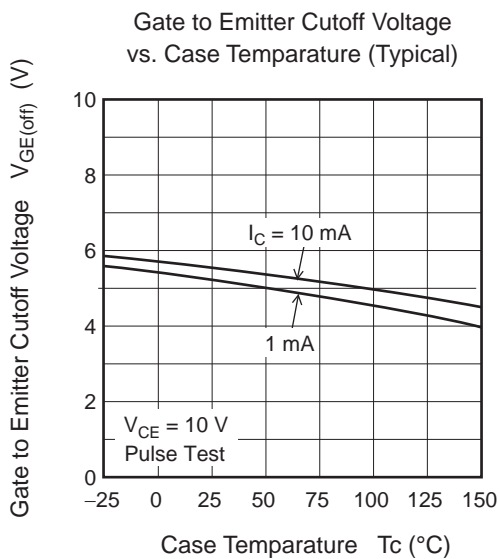
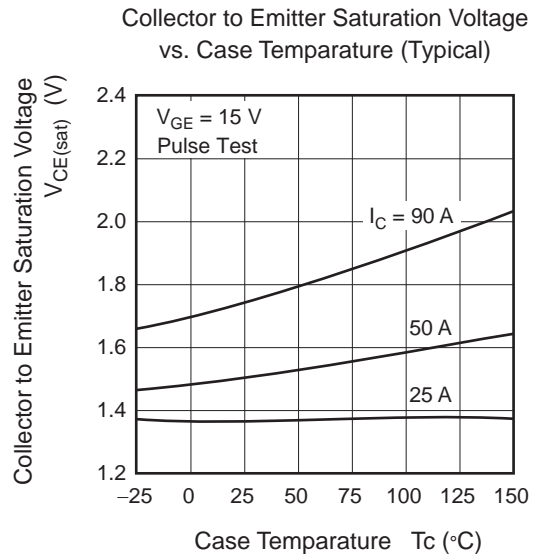
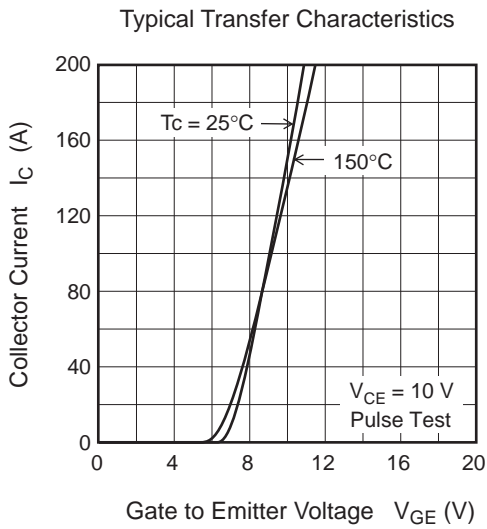
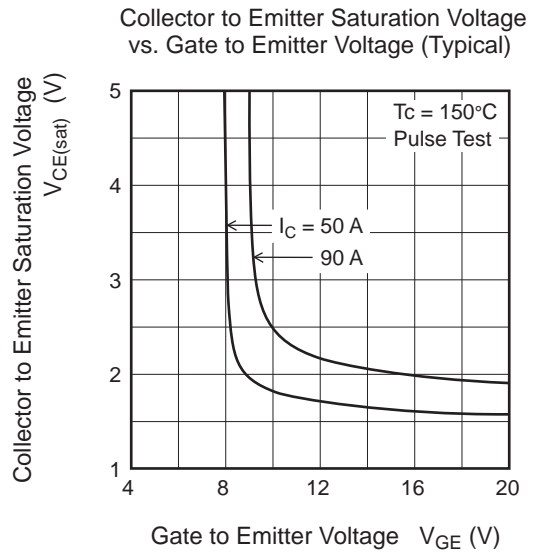
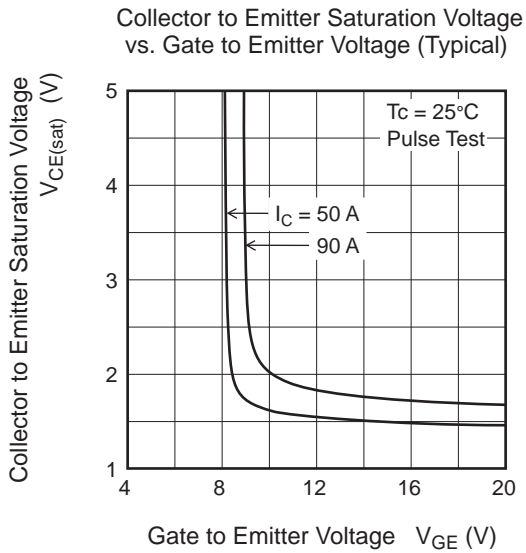
(Ta = 25°C)

| Item  | Symbol          | Min | Typ  | Max     | Unit    | Test Conditions                              |
|---|-----------------|-----|------|---------|---------|--|
| Collector to emitter breakdown voltage                      | $V_{BR(CES)}$   | 600 | —    | —       | V       | $I_C = 10 \mu A, V_{GE} = 0$                 |
| Zero gate voltage collector current / Diode reverse current | $I_{CES} / I_R$ | —   | —    | 5       | $\mu A$ | $V_{CE} = 600 V, V_{GE} = 0$                 |
| Gate to emitter leak current                                | $I_{GES}$       | —   | —    | $\pm 1$ | $\mu A$ | $V_{GE} = \pm 30 V, V_{CE} = 0$              |
| Gate to emitter cutoff voltage                              | $V_{GE(off)}$   | 4.0 | —    | 6.0     | V       | $V_{CE} = 10 V, I_C = 1 mA$                  |
| Collector to emitter saturation voltage                     | $V_{CE(sat)}$   | —   | 1.6  | 2.2     | V       | $I_C = 50 A, V_{GE} = 15 V$ <sup>Note3</sup> |
|   | $V_{CE(sat)}$   | —   | 1.8  | —       | V       | $I_C = 90 A, V_{GE} = 15 V$ <sup>Note3</sup> |
| Input capacitance   | $C_{ies}$       | —   | 3000 | —       | pF      | $V_{CE} = 25 V$                              |
| Output capacitance  | $C_{oes}$       | —   | 160  | —       | pF      | $V_{GE} = 0$                                 |
| Reveres transfer capacitance                                | $C_{res}$       | —   | 85   | —       | pF      | $f = 1 MHz$                                  |
| Total gate charge   | $Q_g$           | —   | 130  | —       | nC      | $V_{GE} = 15 V$                              |
| Gate to emitter charge                                      | $Q_{ge}$        | —   | 20   | —       | nC      | $V_{CE} = 300 V$                             |
| Gate to collector charge                                    | $Q_{gc}$        | —   | 45   | —       | nC      | $I_C = 50 A$                                 |
| Turn-on delay time  | $t_{d(on)}$     | —   | 60   | —       | ns      | $V_{CC} = 300 V$                             |
| Rise time   | $t_r$           | —   | 46   | —       | ns      | $V_{GE} = 15 V$                              |
| Turn-off delay time   | $t_{d(off)}$    | —   | 190  | —       | ns      | $I_C = 50 A$                                 |
| Fall time   | $t_f$           | —   | 50   | —       | ns      | $R_g = 5 \Omega$                             |
| Turn-on energy  | $E_{on}$        | —   | 1.1  | —       | mJ      | (Inductive load)                             |
| Turn-off energy   | $E_{off}$       | —   | 0.6  | —       | mJ      |  |
| Total switching energy                                      | $E_{total}$     | —   | 1.7  | —       | mJ      |  |
| Short circuit withstand time                                | $t_{sc}$        | 3.0 | 5.0  | —       | $\mu s$ | $V_{CC} \leq 360 V, V_{GE} = 15 V$           |
| FRD forward voltage   | $V_F$           | —   | 1.4  | 2.0     | V       | $I_F = 50 A$ <sup>Note3</sup>                |
| FRD reverse recovery time                                   | $t_{rr}$        | —   | 100  | —       | ns      | $I_F = 50 A$                                 |
| FRD reverse recovery charge                                 | $Q_{rr}$        | —   | 0.4  | —       | $\mu C$ | $di_F/dt = 100 A/\mu s$                      |
| FRD peak reverse recovery current                           | $I_{rr}$        | —   | 6.5  | —       | A       |  |

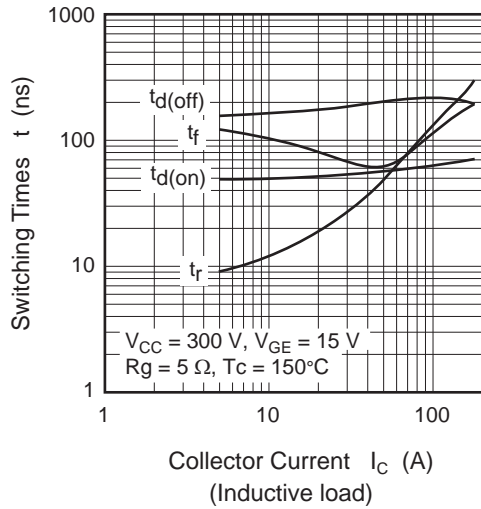
Notes: 3. Pulse test

### Main Characteristics

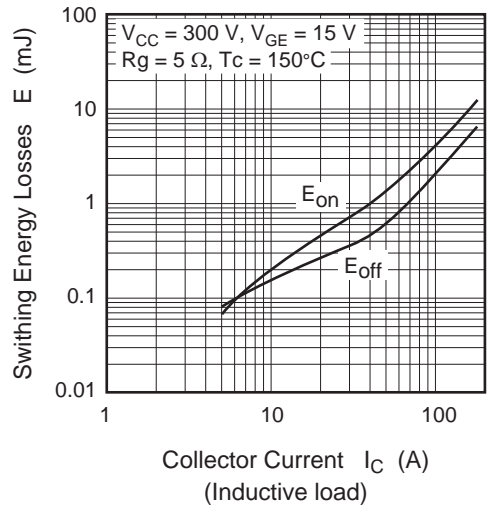




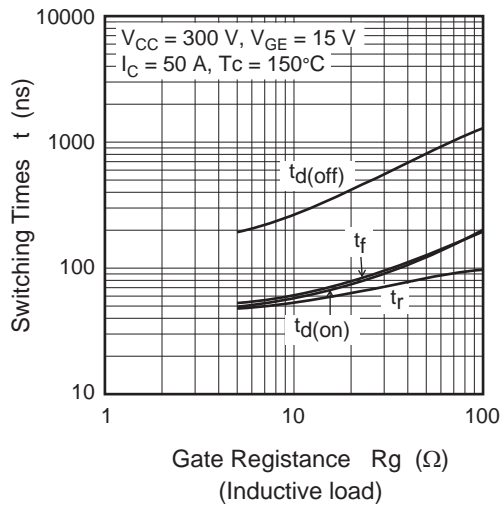
Switching Characteristics (Typical) (1)



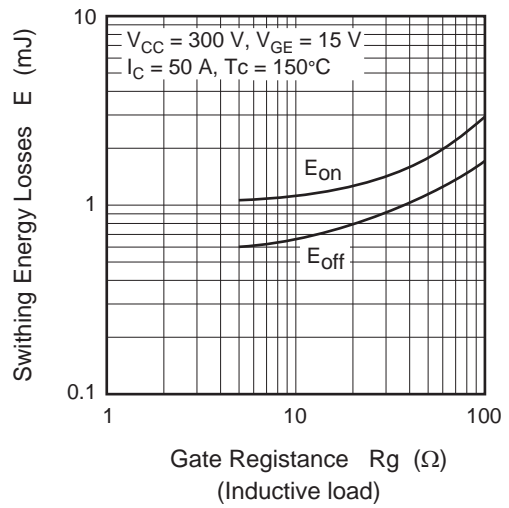
Switching Characteristics (Typical) (2)



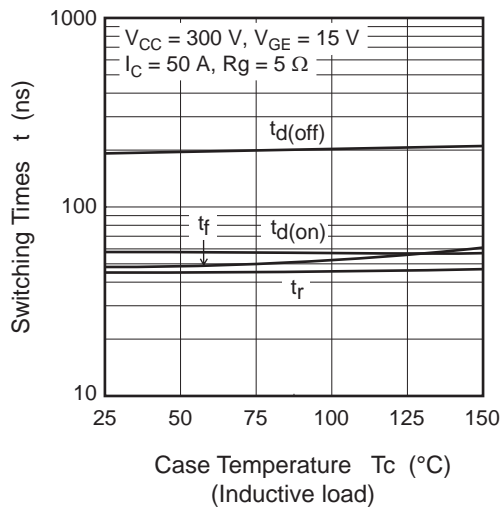
Switching Characteristics (Typical) (3)



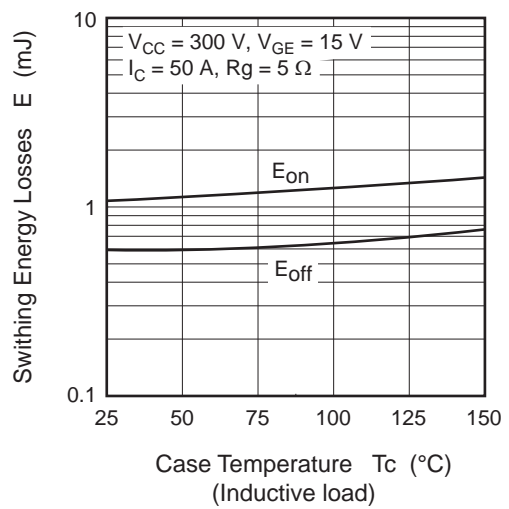
Switching Characteristics (Typical) (4)



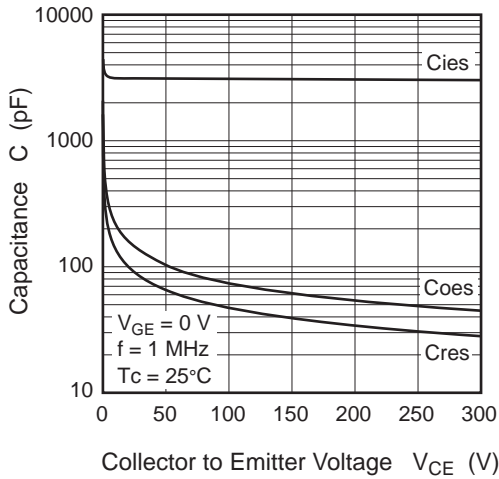
Switching Characteristics (Typical) (5)



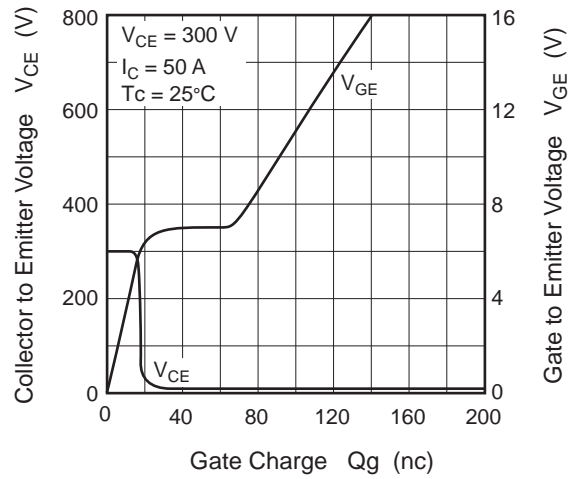
Switching Characteristics (Typical) (6)



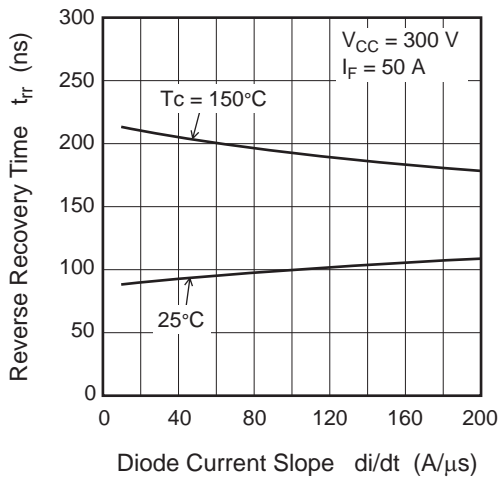
Typical Capacitance vs. Collector to Emitter Voltage



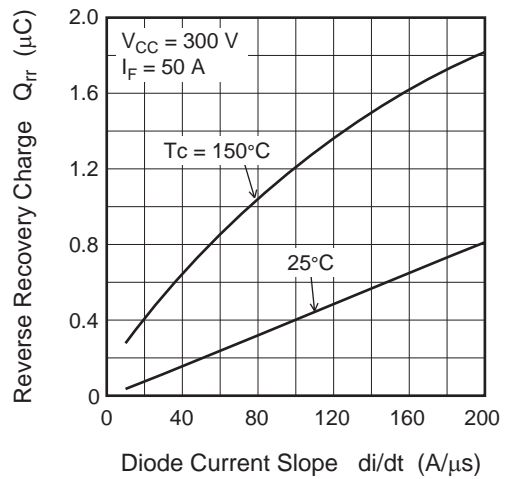
Dynamic Input Characteristics (Typical)



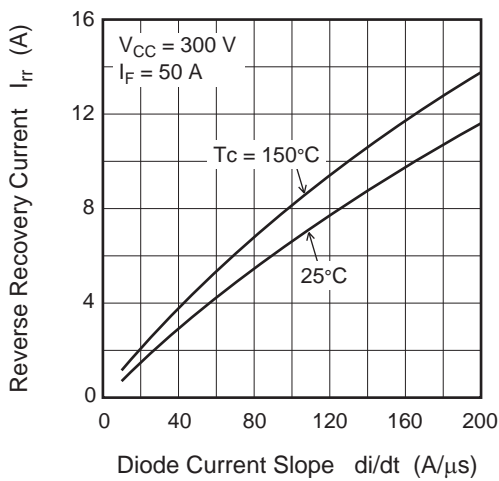
Reverse Recovery Time vs. Diode Current Slope (Typical)



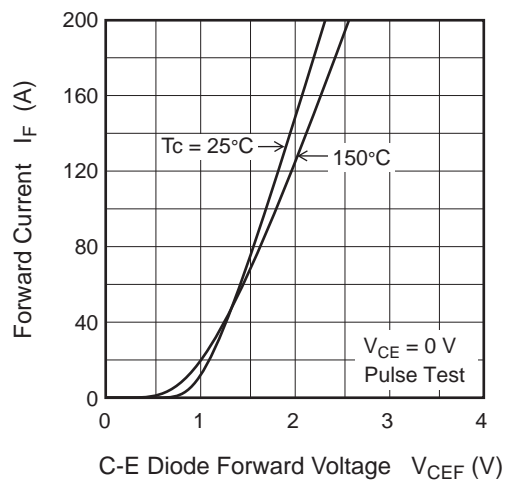
Reverse Recovery Charge vs. Diode Current Slope (Typical)

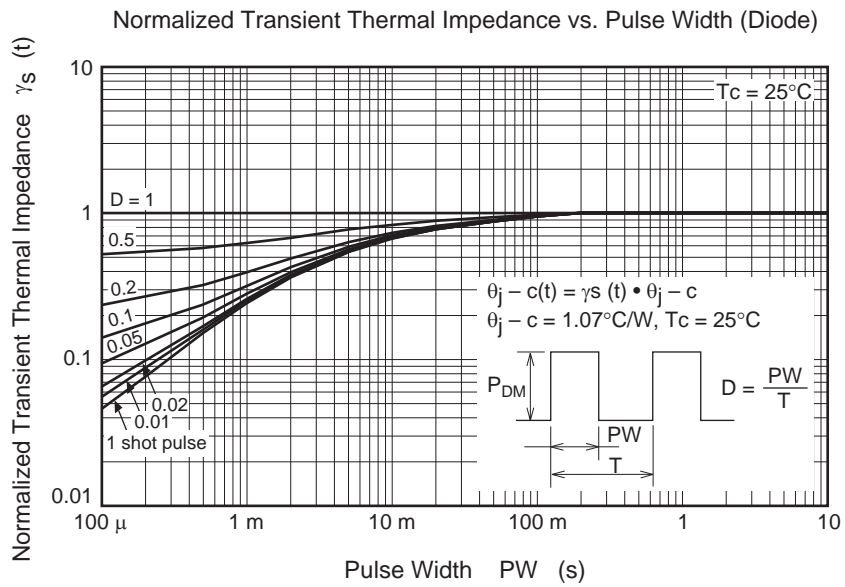
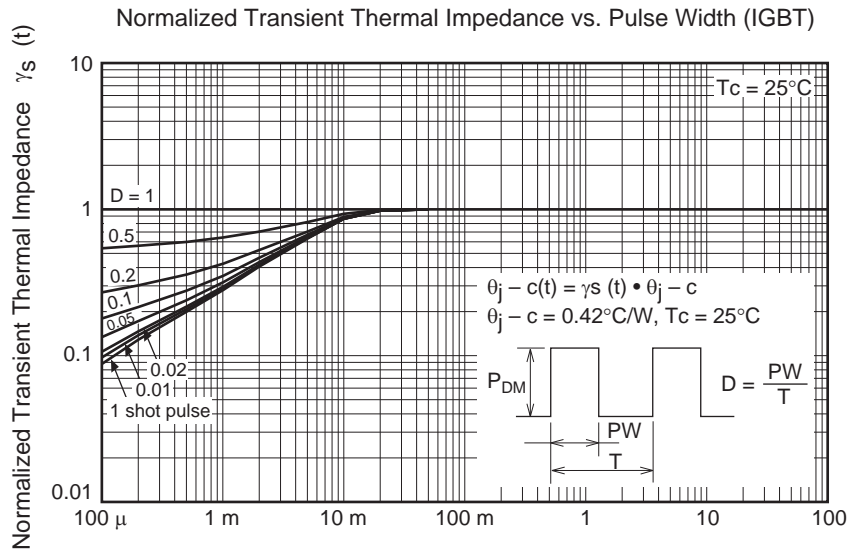


Reverse Recovery Current vs. Diode Current Slope (Typical)

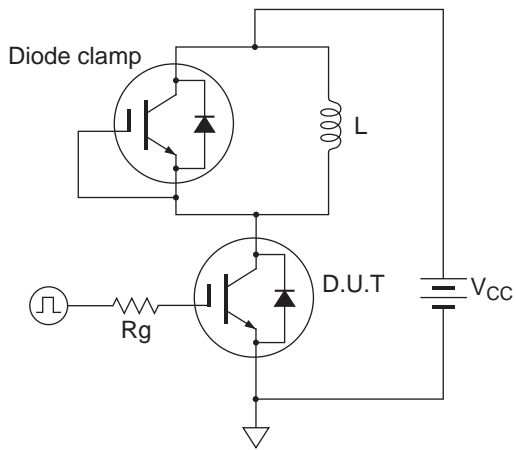


Forward Current vs. Forward Voltage (Typical)

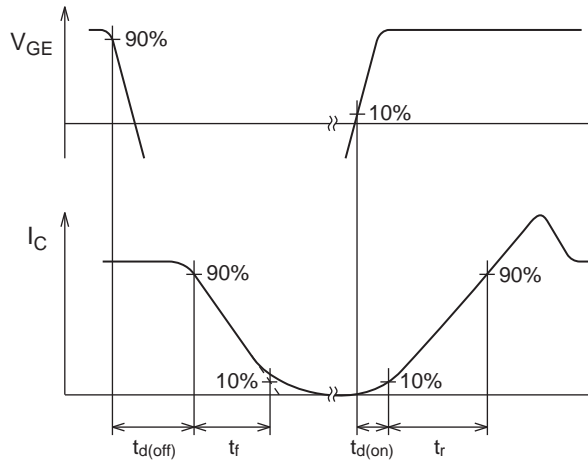




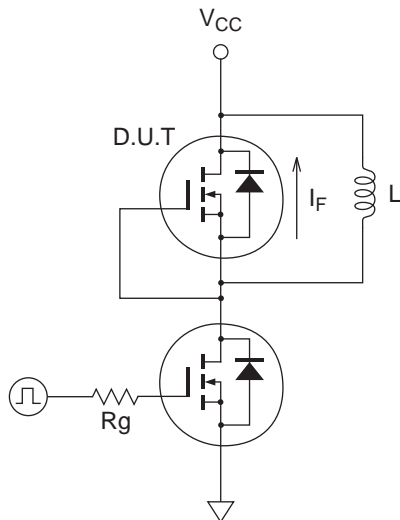
Switching Time Test Circuit



Waveform



Diode Reverse Recovery Time Test Circuit

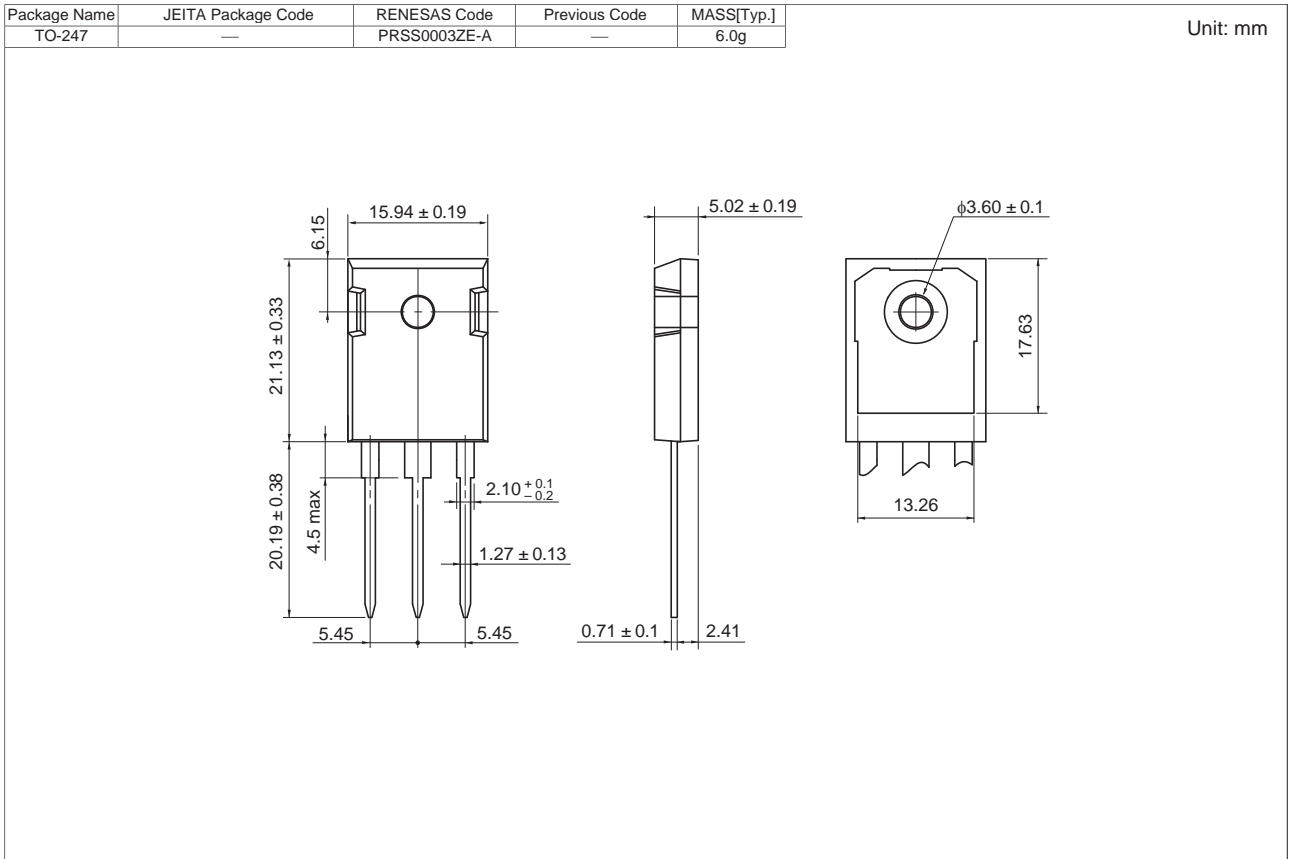


Waveform





### Package Dimension



### Ordering Information

| Orderable Part No. | Quantity | Shipping Container |
|--------------------|----------|--------------------|
| RJH60D7DPQ-E0#T2   | 240 pcs  | Box (Tube)         |

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